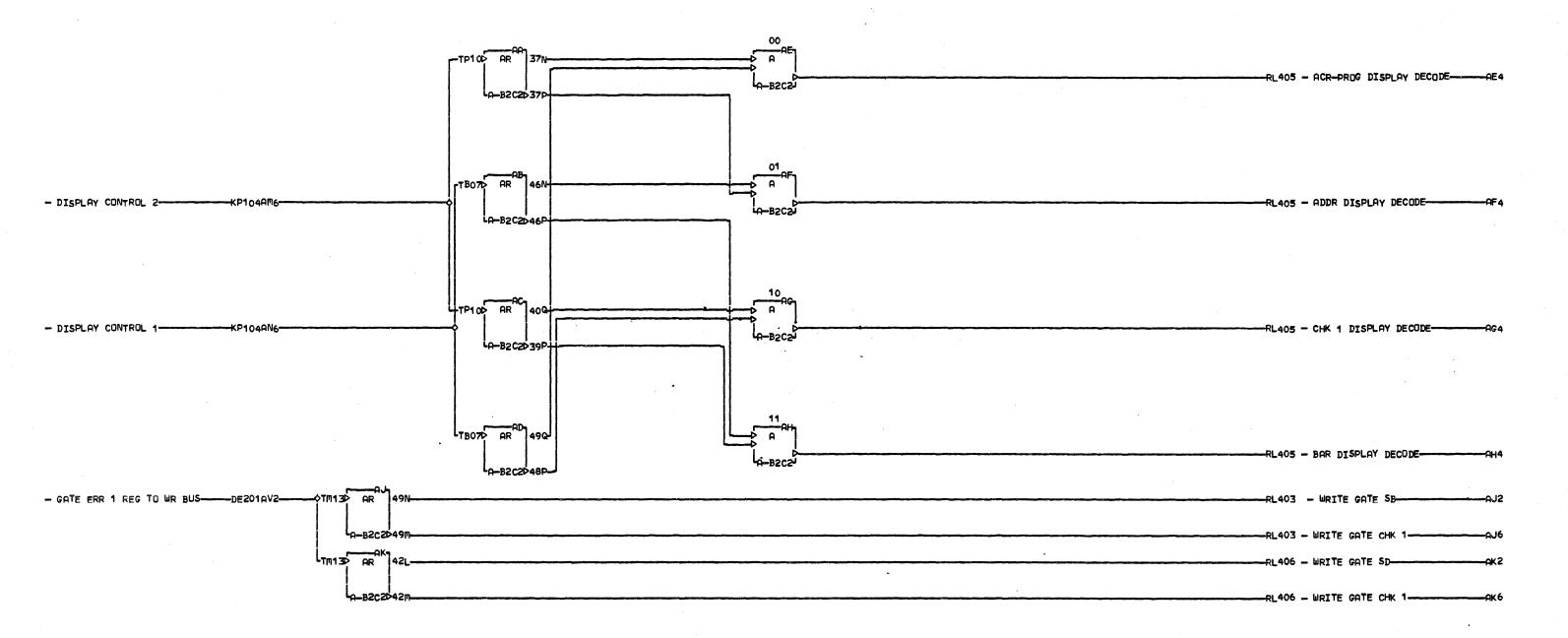
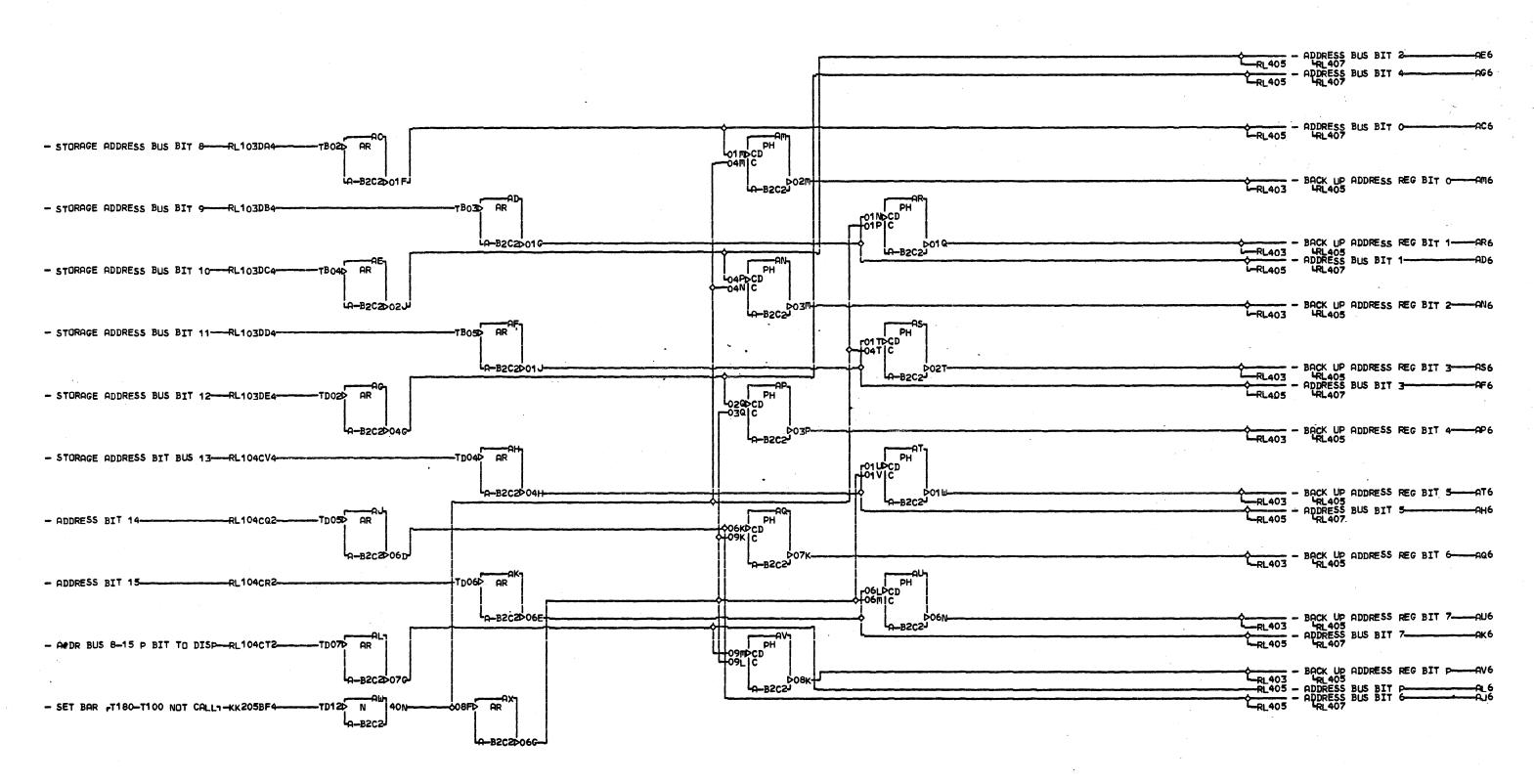
000 RL401



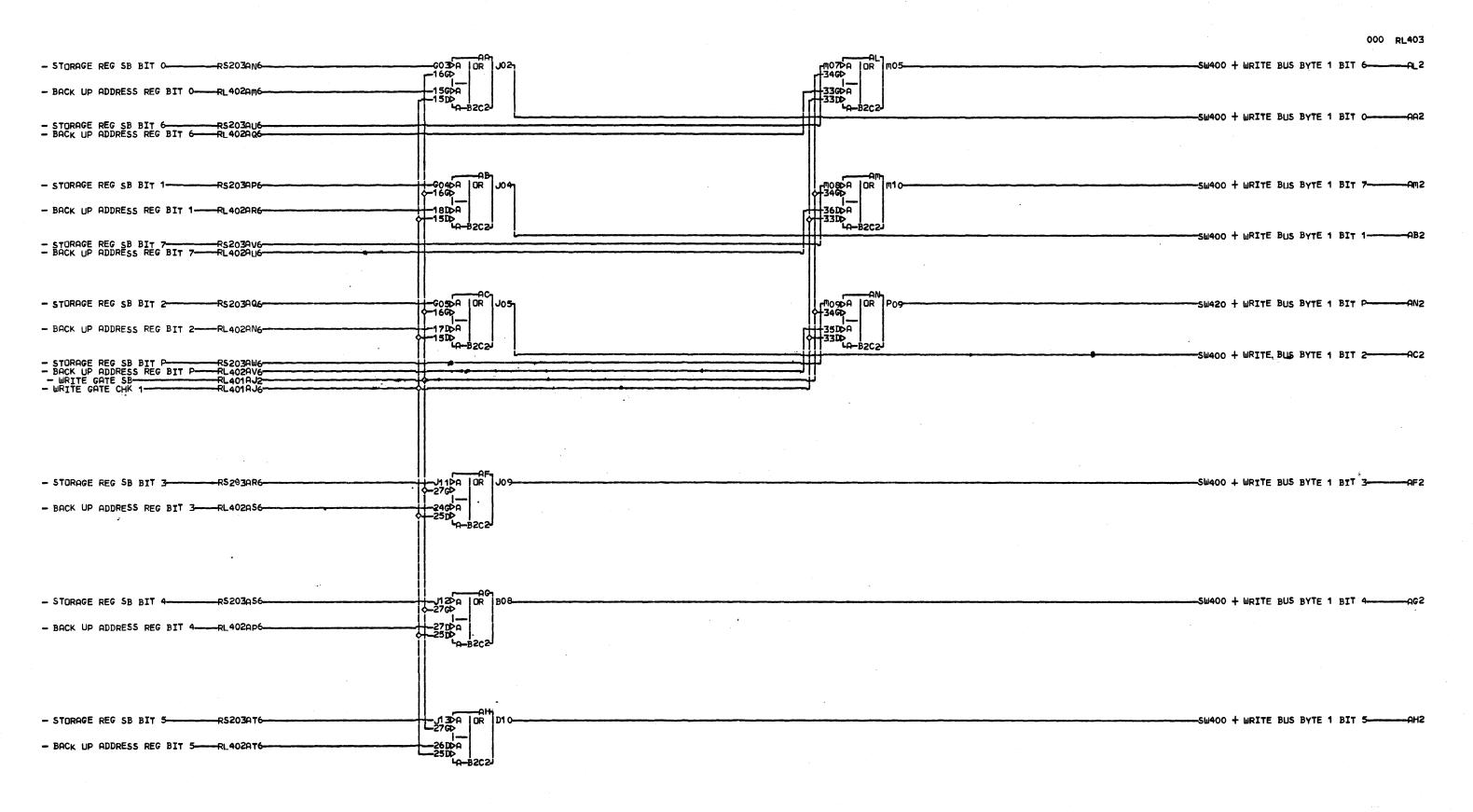
LOC. TYPE A-B2C2 0690 R L401

000 RL402



LOC. TYPE A-B2C2 0690 BACK UP ADDRESS REGISTER -E.C.-HISTORY-MACH-3830-2 IBM CORP. SDD DATE LAST EC 06-15-73 437310

PeNe 2345924



LOC. TYPE A-B2C2 0690

000 RL404 -RL407 - CE ADDR LO BYTE BIT O-OH CD -3V CE ADDR'DATA LO BYTE BIT 0-SM955AD4cv -20FD -21JD LA-B2C2 - STORAGE REG SB BIT 0-RS203AN6 CD 021F - ADDR COMPARE REG BIT 0---RL 405 TG07 AR C CV -B09 نحمعهما -3V CE ADDRIDATA LO BYTE BIT 6-SM955AF4 -RL407 - CE ADDR LO BYTE BIT 1-BA4 22.00 -21.00 -3V CE ADDRJDATA LO BYTE BIT 1-SM955AD6-CV CDH - STORAGE REG SB BIT 1-RS203AP6 -TG040A | CD 022G L-RL405 -TGO7 ARIC J Cv **V**3 ia-B2C2i -3V CE ADDRJ DATA LO BYTE BIT 7-SM955AF6--RL407 - CE ADDR LO BYTE BIT 2----A -3V CE ADDRIDATA LO BYTE BIT 2-SM955AE2-CV PH | 19F0 | 0-19F0 | 0-21J0 | 1-نهـ820عا - - ADDR COMPARE REG BIT 2-AC6 - STORAGE REG SB BIT 2--RS203AQ6 -RL405 -19h -TG07 AR C | A-B2C2 cv La-B2C2J - ADDR COMPARE REG BIT 5-RL407 - CE ADDR LO BYTE BIT 6-RL405 - ADDR COMPARE REG BIT 3-RL405 - RL407 -3V CE ADDRIDATA LO BYTE BIT P-SM955AD2 AR. - SPECIAL OPERATION 6----DE402AE6--TUO2D 45N 49-B2C2D44F 38FD CD A PH 29FD CD -3V CE ADDR'DATA LO BYTE BIT 3-SM955AE4cv 39 JD CD D39F-TG07 AP TJ11DA 30HD A-B2C2 RL405 - ADDR COMPARE REG BIT 6-- STORAGE REG SB BIT 3-RS203AR6 CD P30F -30HD +TG07 | AR | C A-B2C2 -RL407 - CE ADDR LO BYTE BIT 3-----BG4 + SET ACR-STORAGE REG SB BIT 6-40UD CD CD -RL407 - CE ADDR LO BYTE BIT 4-----BK4 A PH -31 JP CD -3V CE ADDR'DATA LO BYTE BIT 4-SM955AE6-CV -TJ12DA CD D316 Tm08ba CD D40G-ل<sub>0-82C2</sub>J - STORAGE REG SB BIT 4--RS203AS6 - ADDR COMPARE REG BIT 7-----RL405 30H> -TG07 AR C A-B2C2 TG07 ARIC - STORAGE REG SB BIT 7-RS203AV6 -RL407 - CE ADDR LO BYTE BIT 5----28FD CD A PH -3V CE ADDRIDATA LO BYTE BIT 5-Sm955AF2-CV \_37F⊅ \_39JÞ لـ820عـما TM09PA CD 037J-.130A -RS203AT6-CD 528. - STORAGE REG SB BIT 5--RL405 - ADDR COMPARE REG BIT P----TGO7 AR C -TG07 AR C - STORAGE REG SB BIT P-RS203AW6 LOC. TYPE ADDRESS COMPARE REGISTER \_E.C. HISTORY-B, MACH. 3830-2 01 IBM CORP. SDD DATE LAST EC 106-15-73 437310 000 000

PeNe 2345926

000 RL405 TOR 10pbA 13qb 10VDA 13WP I DR OGUDA IOR - BACK UP ADDRESS REG BIT 0-RL402AM6--07VD 10WPA 11TD A-B2C2 -RL402AC6 -06VDA -06SD -A-B2C2 100DA 11mb A-B2C2 - ADDRESS BUS BIT O-- BACK UP ADDRESS REG BIT 6-ADDRESS BUS BIT 3 OR -28T -29T -sm950 + ADDR-CHK-PROG-DISP BIT 8----AU6 - BACK UP ADDRESS REG BIT 7-RL402AU6----AB-112PDA | OR 12VDA | OR - BACK UP ADDRESS REG BIT 1-RL402AR6-OBLIDA OR 13TDA 11TD 4A-82C2 - ADDRESS BUS BIT 1--RL402AD6-HO9SDA -1 3MbA Ocean PSCS1 11MD HA-B2C2 -SM950 + ADDR-CHK-PROG-DISP BIT 9-LA-B2C2J P04-- ADDRESS BUS BIT 4--RL402AG6 OR - ADDR DISPLAY DECODE-- BAR DISPLAY DECODE-- BACK UP ADDRESS REG BIT 4-RL401AF4-RL401AH4in-B2C2JP05-1 130ba | r OBVDA OR L 12WDA | OR | 13WD —AJ IOR - BACK UP ADDRESS REG BIT 2-RL402AN6-130-OBSDA - ADDRESS BUS BIT 2--RL402AE6-11mp 11mp 14-B2C2 10650 10650 10650 1110 B2C2 RL402AL6-RL402AK6-RL402AH6-OR in-B262- P02 -SM950 + ADDR-CHK-PROG-DISP BIT 11---AX6 OR -SM950 + ADDR-CHK-PROG-DISP BIT 12----AY6 -010 LBSCS-PJ C1 00 A | OR RC103AY6 -J070A -2000 1904 24L IOR OR - CHECK 1 REG BIT 8rz4NDA 24KD T PROPERTY OF THE PROPERTY OF —11900 11900 A - ADDR COMPARE REG BIT O-RL404AA6 -SM950 + ADDR-CHK-PROG-DISP BIT 13---AZ6 -B2C2<sup>J</sup> G13---B2C2-LA-BSCS "LA-B2C2 RI 4040F6 -RC103BB6 -LA204CE6 508ÞA | OR | 27m---AL7 - CHECK 1 REG BIT 9--RC103AZ6 04ÞA IOR OR | | \$\_31 @5 \$20QD 27KDA 0++24KD 1A-B2C2 31 mDA 31 mDA 29mD - ADDR COMPARE REG BIT 1--SM950 + ADDR-CHK-PROG-DISP BIT 14----BA6 -0-BSCS- D09 PSCS - ADDR COMPARE REG BIT 7- ADDR COMPARE REG BIT 4- CHECK 1 REG BIT 12- CHK 1 DISPLAY DECODE- ACR-PROG DISPLAY DECODE--RL404AK6--RL404AF6--RC103BC6--RL401AG4--RL401AE4- $\Pi\Pi$ 11-24V OR -----SM950 + ADDR-CHK-PROG-DISP BIT 15-----BB6 A101AR2 -A-B2C2JG12-SOZDA JOR L IOR IOR RC1 03BA6 60854 2005 22N--31 Q⊅ - CHECK 1 REG BIT 10-OR 

-30mDA

LOC. TYPE

LA-B2C2J P06-

ADDR-CHK-PROG-DISPLAY --E.C.-HISTORY--B7 MACH . 3830-2 FRAME 01 IBM CORP. SDD DATE LAST EC 000 06-15-73 437310 P.N. 2345927

-SM950 + ADDR-CHK-PROG-DISP BIT P LO-BC6

000

- ADDR COMPARE REG BIT 2-

- ADDR COMPARE REG BIT P-- ADDR COMPARE REG BIT 5-- CHECK 1 REG BIT 13-

21 mpA

LO-B2C2.

RI 404AC6

RL404AL6-RL404AG6-RC103BD6-

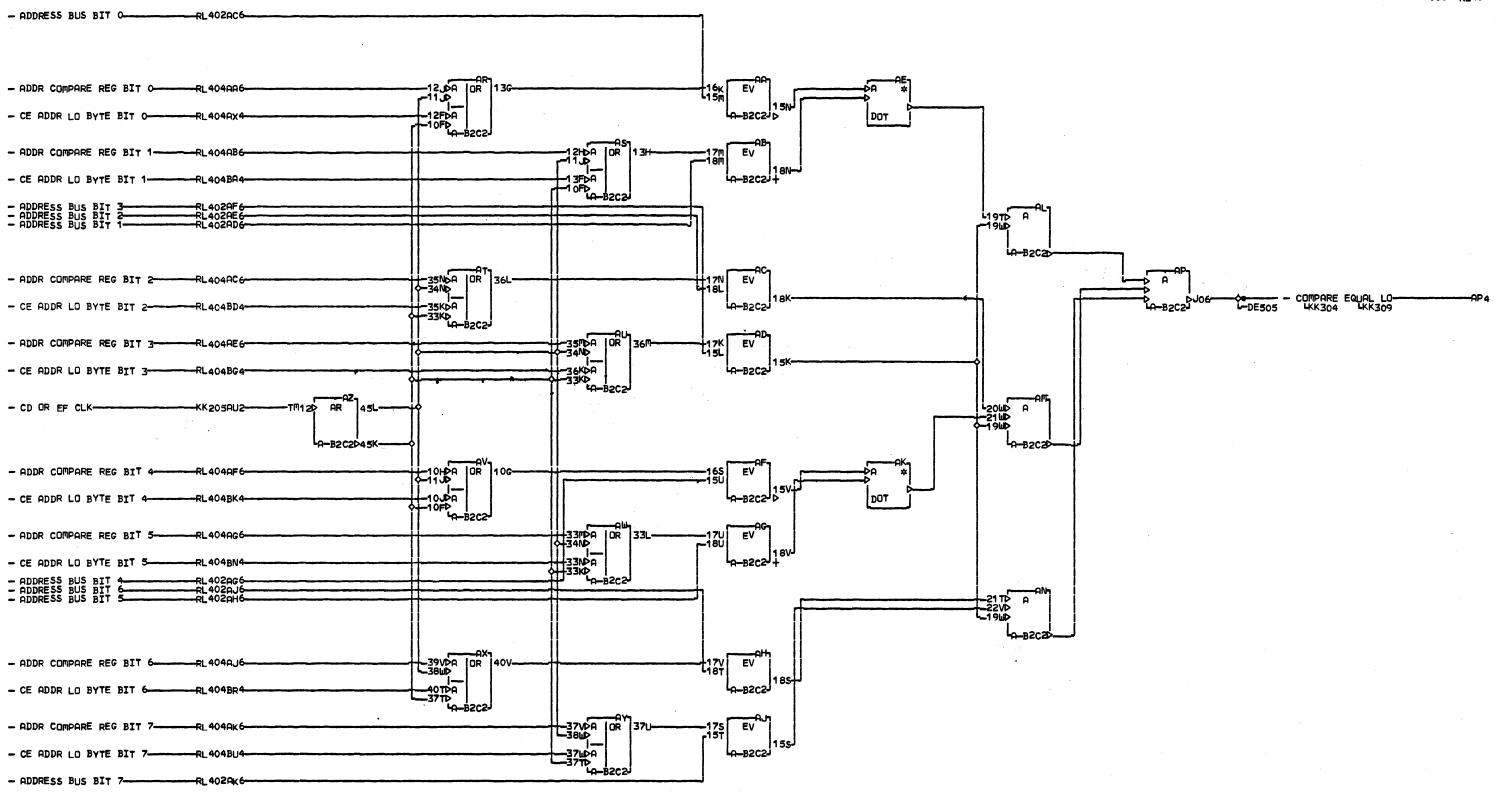
000 RL406 -U100A | DR -J07PA | DR - CHECK 1 REG BIT 8--RC103AY6 -S02DA -43DD -U110A -U110A -H5100 A-B2C2 -RS201AM6 - STORAGE REG SD BIT O--SW400 + WRITE BUS BYTE 3 BIT O-- STORAGE REG SD BIT 6- MPL NOT RDY TO CHK 1 REG-MO4DA IOR IOR -SW400 + WRITE BUS BYTE 3 BIT 7-----AM2 - CHECK 1 REG BIT 9----RC103AZ6ru125A 01151D0 A-B2C2 - STORAGE REG SD BIT 1--SW400 + WRITE BUS BYTE 3 BIT 1-- STORAGE REG SD BIT 7-+ TIE UP TO USER IOR -SW420 + WRITE BUS BYTE 3 BIT P----AN2 --RC103BA6-- CHECK 1 REG BIT 10--ru130A +151 DQ A-B2C2 -RS201AP6-- STORAGE REG SD BIT 2--SW400 + WRITE BUS BYTE 3 BIT 2----AC2 - STORAGE REG SD BIT P-- CHECK 1 REG BIT P 8-15-- WRITE GATE CHK 1-- WRITE GATE SD-RS202AQ6-RL401AK6 - CHECK 1 REG BIT 11--RC103BB6 -SW400 + WRITE BUS BYTE 3 BIT 3----U05DA -47FD - STORAGE REG SD BIT 3--RS201AQ6 -\$08⊳A -RC103BC6 -SW400 + WRITE BUS BYTE 3 BIT 4---- CHECK 1 REG BIT 12--RS202AL6 - STORAGE REG SD BIT 4--SW400 + WRITE BUS BYTE 3 BIT 5-----RC103BD6-- CHECK 1 REG BIT 13-- STORAGE REG SD BIT 5--RS202Am6 LOC. TYPE STOR WRITE BUS BYTE 3 ---EeCe-HISTORY----B7 MACHe 3830-2 437301 437302 FRAME 01 IBM CORP. SDD DATE LAST EC 106-15-73 437310

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P.N. 2345928

000 RL407



LOC. TYPE A-B2C2 0690

R L 4 0 7

000 RI 40

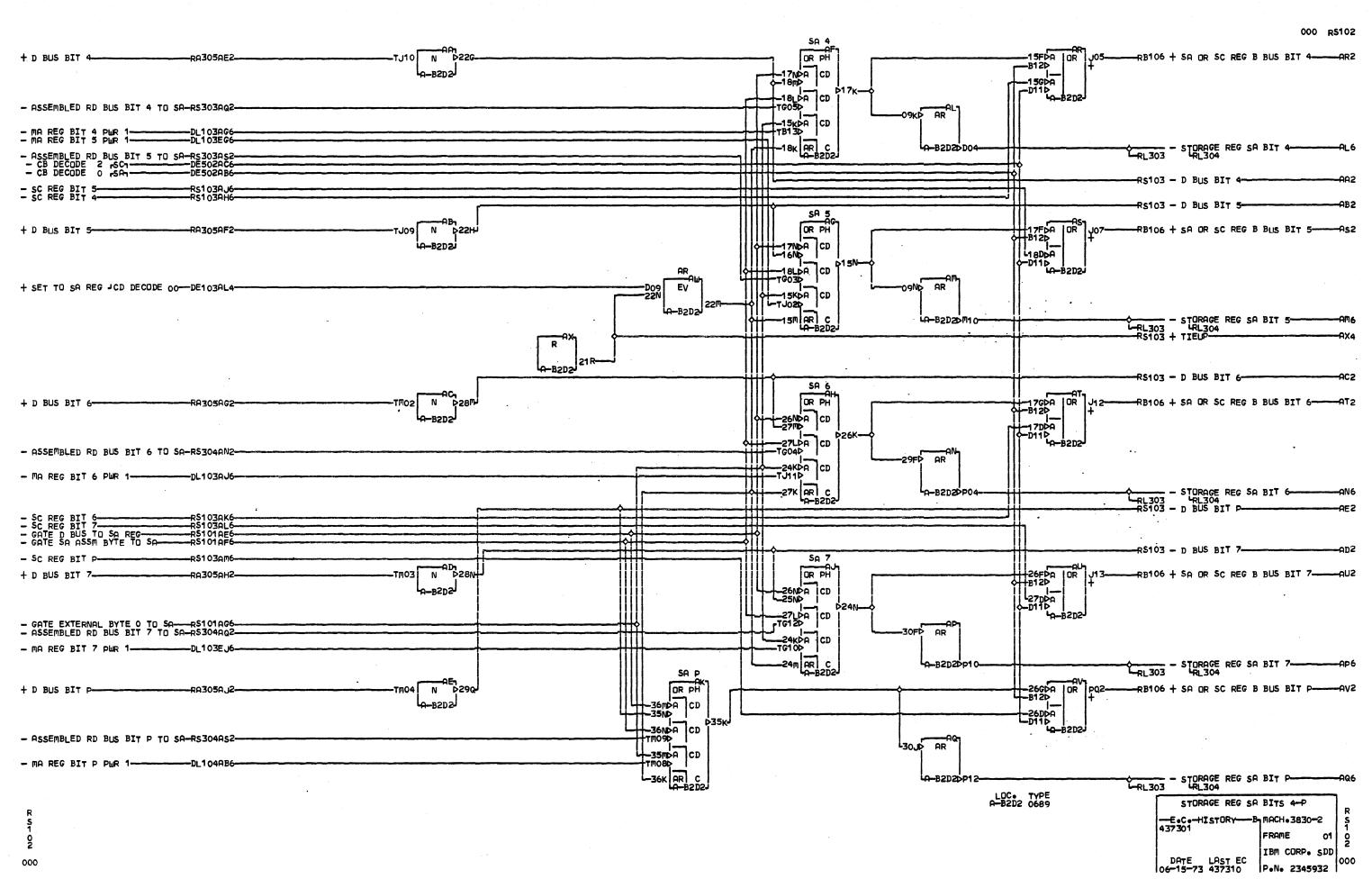
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- (	CHECK	1	REG	BIT	9	-RC103A	Z6	 ·	<b>-</b> 609	90 90 9-8202
- (	CHECK	1	REG	BIT	10	<del>-</del> RC1 <sub>0</sub> 3E	A6	. *	-G08	R 90 9-B2C2
- (	CHECK	1	REG	BIT	11	-RC103E	B6 <del></del>	 	-G10	R 90 -B2C2
- (	CHECK	1	REG	BIT	12	<del>-</del> RC103E	C6	 	-\$08	R 90 B2C2
<del>-</del> (	CHECK	1	REG	BIT	13	<del>-</del> RC103E	D6 <del></del>	 	<b>-</b> 50 <b>7</b>	90 A-B2C2
- 1	MPL NO	TC	RDY	TO (	CHK 1 REG	-LA2040	E6			90 90 9-8202
+ '	TIE U	> 1	ro us	SER-		<u>—</u> LД101£	R2	 	<b>-</b> ⊓04	90 A-B2C2
- (	CHECK	1	REG	BIT	p 8-15	_RC1046	R4		-m03	90

LOC. TYP

TERMINATORS

-E-C-HISTORY-B-MACH-3830-2 L4
437301 FRAME 01 0
1BM CORP- SDD
DATE LAST EC 06-15-73 437310 P-N- 2345930

000 RS101 SA 0 OR PH + D BUS BIT O--RA305AA2-N. -B120 OBGDA CD A-B2D2--09EDA TB10D -06DDA TB09D - ASSEMBLED RD BUS BIT O TO SA-RS302AN2-- MA REG BIT O PWR 1-----DL103AB6-TD09 AR C + SET TO SA REG JCD DECODE 00-DE103AL4 LA-B2D20B05 - STORAGE REG SA BIT O-RL 303 - CB DECODE 2 rsC1-- CB DECODE 0 rsA1--DE502AC6-Ш -Rs103 - D Bus BIT 0--RS103AE6--RS103AD6-- SC REG BIT 1--RS103 - D BUS BIT 1----OR PH -RA305AB2-+ D Bus BIT 1-N نـ96 اخ -RB106 + SA OR SC REG B BUS BIT 1----AS2 OBCDA CD O7GD O9EDA CD TBOSD in-B2D2i - ASSEMBLED RD BUS BIT 1 TO SA-RS302AQ2-AR -08KĎ 06DDA TD07D - MA REG BIT 1 PWR 1----DL103EB6-TDO9 AR C - STORAGE REG SA BIT 1-LA-B2D2DD05--AN6 L-RL303 - SC REG BIT 2--RS103AF6--RS103 - D BUS BIT 2-SA 2 OR PH 12JDA B12D 12FDA + D BUS BIT 2--RA305AC2--RB106 + SA OR SC REG B BUS BIT 2----AT2 03GDA CD 04PD CD TD06D CD -D110 A-B2D2 01MDA CD - ASSEMBLED RD BUS BIT 2 TO SA-RS302AS2-ORND AR - MA REG BIT 2 PWR 1----DL103AD6--TD09 AR C RL303 - STORAGE REG SA BIT 2-AP6
RL304 - RS102 - GATE D BUS TO SA REG-AE6
RS102 - GATE EXTERNAL BYTE 0 TO SA-AG6 LA-B2D20U09 - ASSEMBLED RD BUS BIT 3 TO SA-RS303AN2 - MA REG BIT 3 PWR 1 DL1-03ED6 -RS102 - GATE SA ASSM BYTE TO SA--RS103AG6 - SC REG BIT 3--RS103 - D BUS BIT 3--SA 3 B12D OR PH + D BUS BIT 3--RA305AD2-TG09 N -RB106 + SA OR SC REG B BUS BIT 3-030PA CD ل<u>192024 ما</u> 2460A 0-0110 O4NDA CD GATE D BUS O1MDA CD AR -TD09 AR C - STORAGE REG SA BIT 3-A-B2D2D46H LA-B2D20B07-RL303 GATE READ - DATA CYCLE -TJ-TJ----KK301AK6 A-B2D2548H GATE EXT - GATE MA TO SA REG--GA202AB6-TS050 AR 49G Ev - + CB DECODE o 2 EVEN-G02--RB106 نـ8202ـمـا 49F. LOC. TYPE STORAGE REG SA BITS 0-3 -E.C.-HISTORY--B- MACH - 3830-2 437301 FRAME 01 0 IBM CORP. SDD DATE LAST EC 000 000 P.N. 2345931 06-15-73 437310



000 RS103 SC 1 SC 0 OR PH OR PH 30H℃ -31 FD AR AR AR 35GDA CD 36FD CD 36EDA CD 35GDA CD 44GDA CD 45FD - D BUS BIT O---RS101AA2-45EDA T504b T503b TU13 Ai A-B2D2Dp13 LA-B2D2DG1 A-B2D20104--RL306 - STORAGE REG SC BIT 2--45EDA CD 36EDA CD TMO7D CD TMO5D CD -RS101 - SC REG BIT 2--- READ BYTE 2 BIT 0 -Fm---RL101AA2-33DDA TP06D CD CD - MA REG BIT O PWR 3--DL103CB6-TU13 AR C TU13 AR C TU13 AR C -RL306 - STORAGE REG SC BIT 1---006 ÀH. - D BUS BIT 1
- READ BYTE 2 BIT 1 rKK1- MA REG BIT 1 PWR 3
- D BUS BIT 2
- D BUS BIT 4
- D BUS BIT 5 RS101AB2-RL101AB2--RS101 - SC REG BIT 1 -RS101 - SC REG BIT 0 -RL306 - STORAGE REG SC BIT O AD6 -DL103GB6--RS101AC2--RS102AA2--RL306 - STORAGE REG SC BIT 4-RS102 - SC REG BIT 4-RS101 - SC REG BIT 3-RS101 - SC REG BIT 3-RL306 - STORAGE REG SC BIT 3-RS100 - STORAGE REG B  $\Box \Box$ - MA REG BIT 4 PWR 3-READ BYTE 2 BIT 2 ND--DL103CG6--RL101CA4-SC 3 GR PH

0-4460A CD

-4360

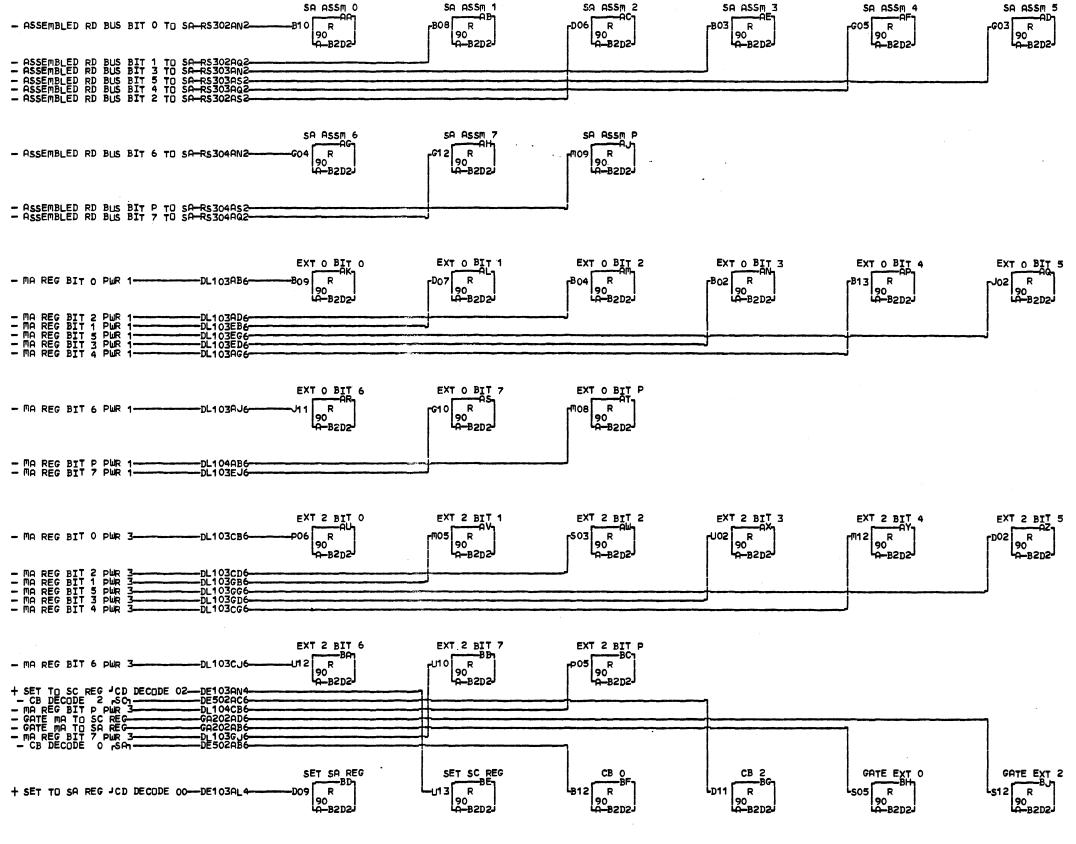
-4580A CD

-4580A CD

-4580A CD

-4200A CD SC 4 SC 5 OR PH AR OR PH AR AR 40HD CD #4C8E CD - D BUS BIT 3--RS101AD2--40GPA CD TP11P in-B2D2buo5i 40GDA CD LA-B2D2DU06 IA-B2D2D507--RL306 - STORAGE REG SC BIT 5-- READ BYTE 2 BIT 3 NB1-RL101AD2--RS102 - SC REG BIT 5-TM125 TDO2D CD - MA REG BIT 3 PWR 3-- MA REG BIT 2 PWR 3--DL103GD6--DL103CD6-40F AR C 37H AR C A-B2D2 - READ BYTE 2 BIT 5 - RL101AF2-+ SET TO SC REG JCD DECODE 02 DE103AN4-- READ BYTE 2 BIT 4 - RL101AE2-- MA REG BIT 7 - RS102AD2-- D BUS BIT 5 DUR 7 - RS102AD2--RL306 - STORAGE REG SC BIT 7--R\$102 - SC REG BIT 7--R\$102 - SC REG BIT 6--RL306 - STORAGE REG SC BIT 6-RL101AH2 -DL1036U6--RL101AU2-SC 6 SC 7 SC P 380PA CD 390P OR PH OR PH AR AR 49 QÞ AR 54/PA CD
54/PA CD
54/PA CD
54/PA CD
54/PA CD
51/KPA CD
71/2D
54/K QR C 53NPA CD 52NP CD 54LPA CD - D BUS BIT 6--RS102AC2--RL306 - STORAGE REG SC BIT P-<sup>L</sup>A—B2D2ĎU07 40NDA CD D51N-LA-B2D2D508 4-B2D2DS09--TU110 | CD ---RL101AG2-- READ BYTE 2 BIT 6--RS102 - SC REG BIT P-37mPn 1-7p05p - MA REG BIT 6 PWR 3--DL103cJ6-51m AR C A-B2D2 -54K AR C -37p AR C A-B2D2-GATE D BUS -027 EV А RS102AX4 + TIEUP 197-La-BSDS] A-B2D2D51F-GATE READ -TS105 A ---KK301AK6-- DATA CYCLE -TJ-TJ-54G+ LA-B2D2D53FJ GATE EXT -GA202AD6-AR - GATE MA TO SC REG-54E-LA-B2D2054D LOC. TYPE A-B2D2 0689 STORAGE REG SC -E.C.-HISTORY--B, MACH. 3830-2 437301 FRAME 01 9 IBM CORP. SDD DATE LAST EC 106-15-73 437310 000 000

P.N. 2345933

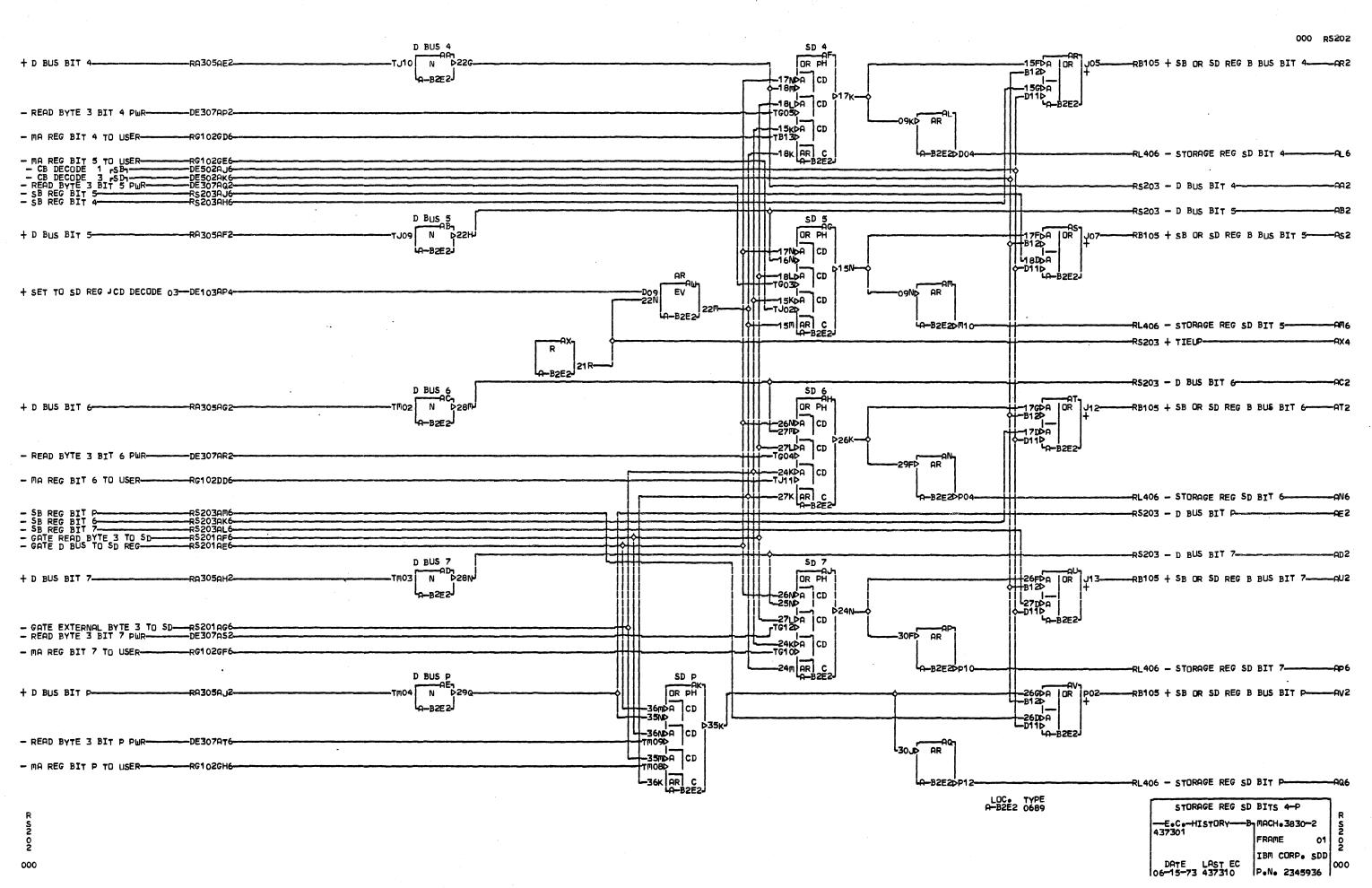


LOC. TYPE

000 RS109

000 RS201 D BUS O SD 0 OR PH + D BUS BIT O--RA305AA2-N RB105 + SB OR SD REG B BUS BIT 0-AR2 OBGDA CD ل9-82E2ما \_\_\_10JDA \_\_D11D 09FD 09EDA CD TB10D 06DDA CD TB09D TD09 AR C - READ BYTE 3 BIT O PUR-DE307AZ4--07KD - MA REG BIT O TO USER-RG102GA6-+ SET TO SD REG JCD DECODE 03-DE103AP4 LA-B2E2DB05 -RL406 - STORAGE REG SD BIT 0-----AM6 - CB DECODE 1 rsB1--DE 502AJ6 -DE 502AK6--RS203 - D BUS BIT O-----RS203AE6--RS203AD6-SD 1 OR PH O86PA CD O96PA CD TB08P CD TD07P CD TD07P CD -RS203 - D BUS BIT 1----D Bus 1 12HDA B12D N 5196 -RA305AB2-+ D Bus BIT 1----RB105 + SB OR SD REG B BUS BIT 1-----AS2 نـ82E2 | 143--|0-0110 | |A-B2E2 - READ BYTE 3 BIT 1 PWR----DE 307BA4-AR 08KD - MA REG BIT 1 TO USER-----RG102GB6-LA-B2E2DD05--RL406 - STORAGE REG SD BIT 1--------RS203AF6-- SB REG BIT 2 -RS203 - D BUS BIT 2-D BUS 2 SD 2 1200A OR 9120 1250A OR PH + D BUS BIT -2--RA305AC2 N -RB105 + SB OR SD REG B BUS BIT 2-AT2 O3QDA CD
O4PD
O4PD
TD06D
TD06D
TD09 AR C
A-B2E2 - READ BYTE 3 BIT 2 PWR-DE307BB4--08/10 AR - MA REG BIT 2 TO USER--RG102DC6--TD09 AR C LA-BSESPU09 RL406 - STURAGE REG SD BIT 2----- MA REG BIT 3 TO USER-- READ BYTE 3 BIT 3 PWR-RS202 — GATE D BUS TO SD REG RS202 — GATE EXTERNAL BYTE 3 TO SD -RG102GC6 -DE307BC4 RS202 - GATE READ BYTE 3 TO SD-- SB REG BIT 3--RS 203AG6 -R\$203 - D BUS BIT 3-D BUS 3 SD 3 24FDA B12D OR PH N + D BUS BIT 3--RA305AD2--RB105 + SB OR SD REG B BUS BIT 3--030PA CD ل<sub>92E24-</sub>ما 24GDA 0-D11D -O4NDA CD GATE D BUS LA-BSES-AR -01MDA CD -TD09 AR C A-B2E2D46H -B2E2DB07--RL406 - STORAGE REG SD BIT 3-GATE READ - DATA CYCLE -TJ-TJ--KK 301 AK 6 -A--B2E2548H GATE EXT B12 D11 - GATE MA TO SD REG--GAZOZAC6-AR Eν 49G+ - + CB DECODE 1 3 EVEN-G02-RB105 LA-B2E2j 19-B2E2049F LOC. TYPE STORAGE REG SD BITS 0-3 -E.C.-HISTORY-B, MACH. 3830-2 437301 437302 FRAME 01 IBM CORP. SDD DATE LAST EC 106-15-73 437310 000 000

PeNe 2345935



000 RS203 SB 1 OR PH OR PH OR PH -30Hb -31Fb AR AR AR 35GÞA CD 44GDA CD 35GDA CD 36EDA CD + TM07D - D BUS BIT O--RS201AA2 A-B2E2Dp1 3 LO-B2E2DG 45EDA CD 1-0-B2E2DU04-ERL403 - STORAGE REG SB BIT 2-----36EDA CD Ď33G-33DDA CD - STOR RD BUS BYTE 1 BIT 0 PWR-DE503AL6 33D>A CD TM055 AR C A-B2E2 42DDA CD TS03D TU13 AR C A-B2E2 - MA REG BIT O PWR 2-----DL103BB6-TU13 AR C STORAGE REG SB BIT 0-RL404 - STORAGE REG SB BIT 1-LRL403 - RS201 - SB REG BIT 1-RS201 - SB REG BIT 0-L-RL403 STORAGE REG SB BIT 4RL403 - SB REG BIT 4RS202 - SB REG BIT 3RS201 - SB REG BIT 3LRL403 - STORAGE REG SB BIT 3LRL404 -AH6 -AG6 L-RL403 SB 3 SB 5 AR AR OR PH OR PH AR AR -38JD CD 446DA CD 426 40HP CD 40GDA CD D39F - D BUS BIT 3----RS201AD2-RL403 - STORAGE REG SB BIT 5-RL403 - RL404 - RS202 - SB REG BIT 5-RS202 14-B2E2DU05iA-B2E2DU06 -A-B2E2DS07 L-RL403 -40GDA CD 45EDA CD TSO2D CD 42DDA CD TU02D CD TU13 AR C - STOR RD BUS BYTE 1 BIT 3 PWR-DE503AP6-- STOR RD BUS BYTE 1 BIT 2 PWR-DE503AN6-TM120 TDO20 - MA REG BIT 3 PWR 2-DL103FD6-AOF AR C A-B2E2 - MA REG BIT 5 PWR 2 DL103FG6+ SET TO SB REG JCD DECODE 01 DE103AM4- MA REG BIT 4 PWR 2 DL103BG6- STOR RD BUS BYTE 1 BIT P PWR-DE503AU6- D BUS BIT 7 R5202AD2- STOR RD BUS BYTE 1 BIT 5 PWR-DE503AR6- MA REG BIT 7 PWR 2 DL103FJ6- STOR RD BUS BYTE 1 BIT 7 PWR-DE503AT6- MA REG BIT P PWR 2 DL104BB6-RL403 - STURAGE REG SB BIT 7-RS202 - SB REG BIT 7-RS202 - SB REG BIT 6-RS203 - STURAGE REG SB BIT 6-RL403 - RL404 1-1-14 SB 6 SB P SB 7 OR PH OR PH OR PH 48PD AR. AR AR 53NPA CD -53NPA CD 3800A CD 54LDA CD ) - D BUS BIT 6--RS202AC2-4-B2E20U07 - STORAGE REG SB BIT P-053K -54LPA CD Ď51 N— LA-82E2DS08 LA-B2E2DS09 -40NPA CD RL403 RS202 - SB REG BIT P - STOR RD BUS BYTE 1 BIT 6 PWR-DE503AS6 -TŬ1ÎÇ'' TP07D 51KDA CD 51KDA CD 37MDA CD - MA REG BIT 6 PWR 2----DL103BJ6 51 m AR C A-BZE2 -37P ARI C -54K AR C GATE D BUS AR EV PX-А + TIEUP RS202AX4 19m. LA-B2E2J A-B2E2D51F-GATE READ A AB -KK301AK6 - DATA CYCLE -TJ-TJ--LA-B2E2D53FJ GATE EXT - GATE MA TO SB REG----GA202AE6-AR 54EJ LA-B2E2D54D-LOC. TYPE STORAGE REG SB -E.C.-HISTORY-ByMACH.3830-2 01 0 FRAME IBM CORP. SDD 000 DATE LAST EC 000

06-15-73 437310

PeNe 2345937

000 RS209 BYTE 3 BIT 0 BYTE 3 BIT 1 BYTE 3 BIT 2 BYTE 3 BIT 3 BYTE 3 BIT 4 BYTE 3 BIT 5 D06 R 90 PABZE2 B03 R 90 A-B2E2 GO3 R 90 A-B2E2 90 A-B2E2 - READ BYTE 3 BIT O PWR--DE 307AZ4 90 DEE

> LOC. TYPE A-B2E2 0689

SB SD REGS TERMINATORS

01

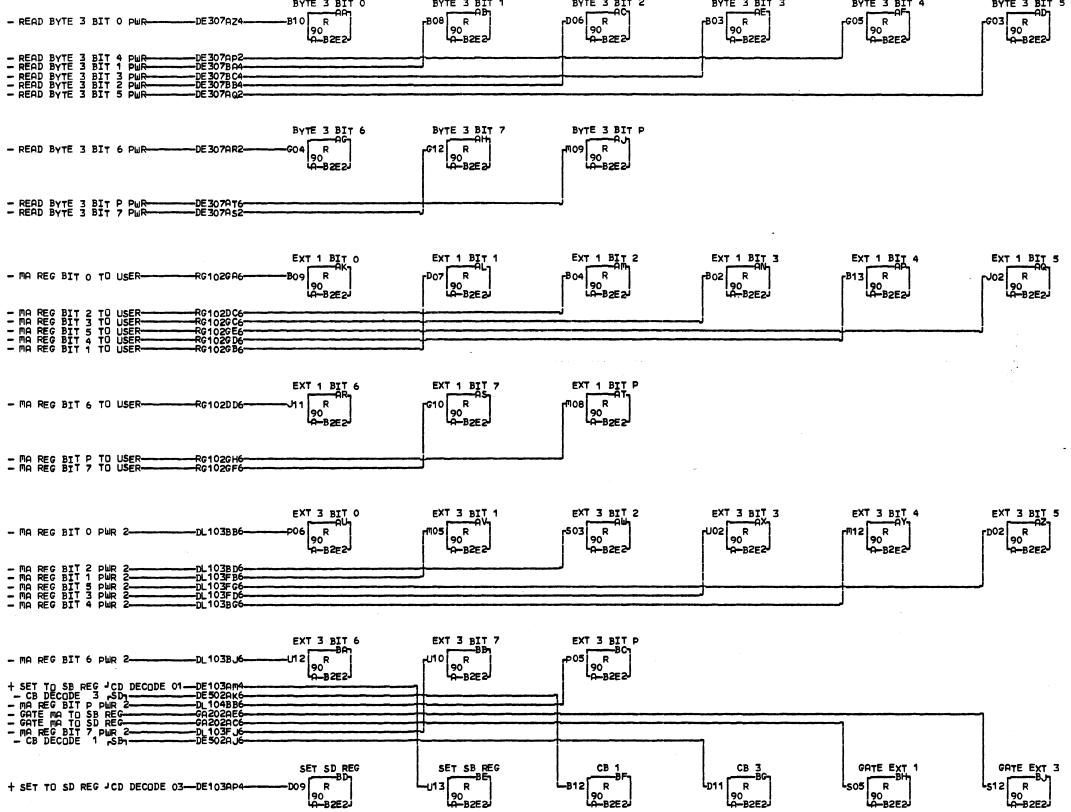
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IBM CORP. SSD

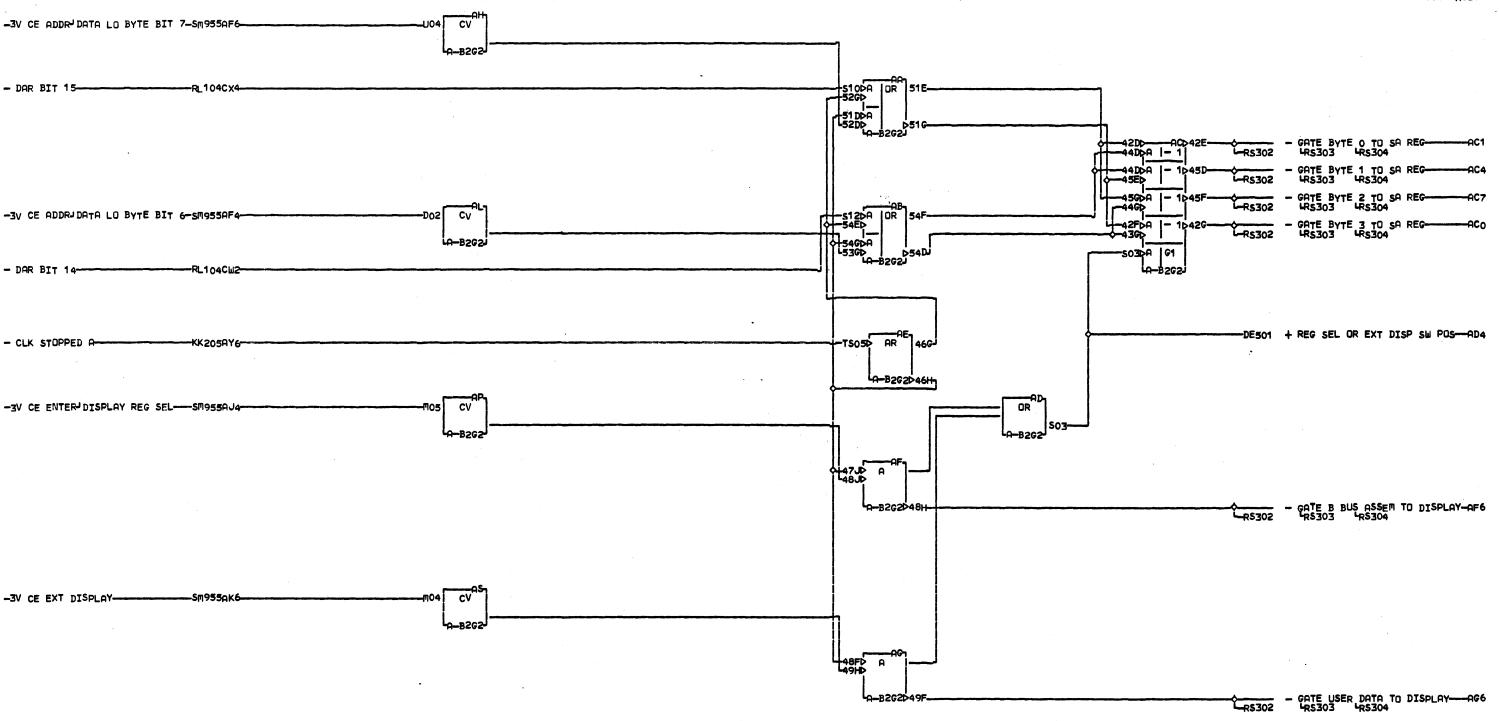
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--E.C.-HISTORY-437301 437302

DATE LAST EC 06-15-73 437310



000 R5301



LOC. TYPE A-B2G2 0695

SA AND DISPLAY ASSEM CONTROL

-E.C.O.-HISTORY----B\_MACH.3830-2
437301

FRAME 01

IBM CORP. SDD

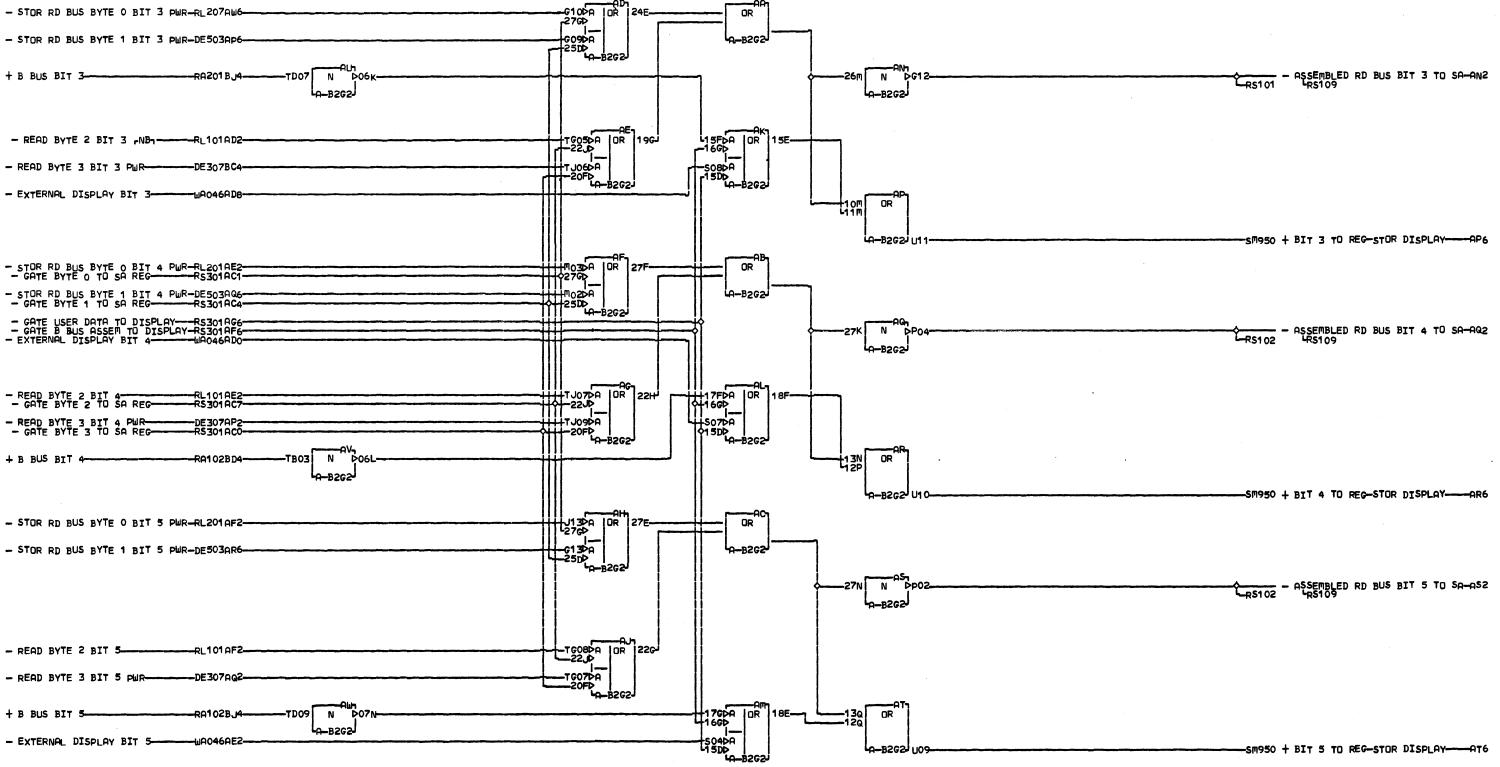
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06-15-73 437310

P.N. 2345939

000 RS302 -∏10ΣA -40JÞ - STOR RD BUS BYTE 0 BIT 0 PWR-RL207AT6-OR - STOR RD BUS BYTE 1 BIT 0 PWR-DE503AL6 p100A A-B2G2 -RA202BD4 - ASSEMBLED RD BUS BIT 0 TO SA-AN2 + B BUS BIT O-N نههجهــما A-B2G2J TP070A | OR OR 1466 TP060A 33DD 14-B262J - READ BYTE 3 BIT 0 PWR-DE307AZ4 -U020A r28FD OR in-B2G2i B02--SM950 + BIT O TO REG-STOR DISPLAY----AP6 - STOR RD BUS BYTE 0 BIT 1 PWR-RL207AU6-- GATE BYTE 0 TO SA REG-------RS301AC1-OR - STOR RD BUS BYTE 1 BIT 1 PWR-DE503AM6-- GATE BYTE 1 TO SA REG-RS301AC4ia-B2G2i 38FD LA-B262 - ASSEMBLED RD BUS BIT 1 TO SA-AQ2 4RS109 N in-B2G2j TPO9DA OR 30HPA | OR - READ BYTE 2 BIT 1 KK--------RL101AB2-----RS301AC7-- READ BYTE 3 BIT 1 PWR---- GATE BYTE 3 TO SA REG-\$130A \$28FD A-B2G2 + B BUS BIT 1--RA202BJ4-OR 3-10 ل<u>B2G2 سم</u>ا -SM950 + BIT 1 TO REG-STOR DISPLAY----AR6 - STOR RD BUS BYTE 0 BIT 2 PWR-RL207AV6-OR 1-120A 38FD - STOR RD BUS BYTE 1 BIT 2 PWR-DE503AN6-La-B2G2 ASSEMBLED RD BUS BIT 2 TO SA-AS2 N - READ BYTE 3 BIT 2 PWR-DE307BB4 30.0A + B BUS BIT 2--RA201 BD4-OR ل<sub>B2G2</sub>ما - EXTERNAL DISPLAY BIT 2-WA046AD6 -SM950 + BIT 2 TO REG-STOR DISPLAY-AT6 LOC. TYPE A-B2G2 0695 SA AND DISPLAY ASSEM BIT 06162 ---E.C.--HISTORY---437301 437302 -B- MACH-3830-2 IBM CORP. SDD 000 DATE LAST EC

06-15-73 437310

P.N. 2345940

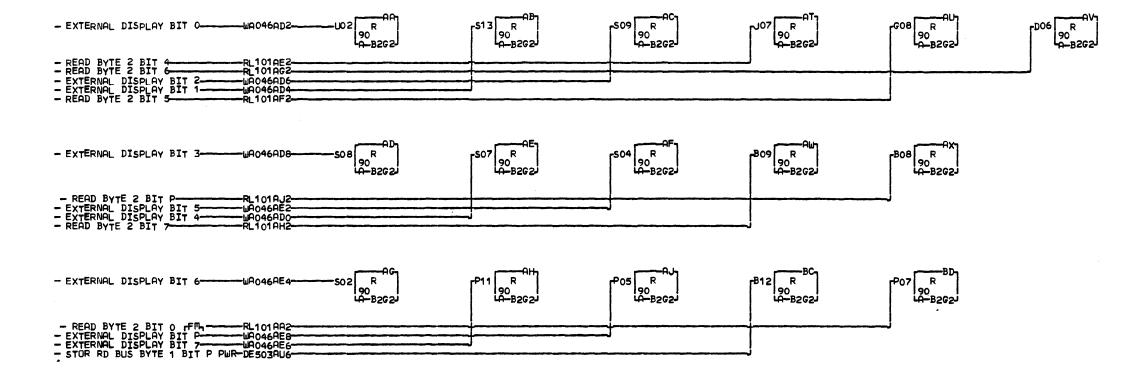


LOC. TYPE

000 RS304 - STOR RD BUS BYTE 0 BIT 6 PWR-RL201AG2-OR 13JÞ 10110A 11FD 10-B2G2 - STOR RD BUS BYTE 1 BIT 6 PWR-DE503AS6 A-B2G2--ANT DG02-- ASSEMBLED RD BUS BIT 6 TO SA-AN2 4RS109 + B BUS BIT 6--RA101BD4-A-B2G2 لB2G2لم TD065A | DR 06E - READ BYTE 3 BIT 6 PWR------DE307AR2-- EXTERNAL DISPLAY BIT 6-WA046AE4-OR to1m LA-B2G2 1007 -SM950 + BIT 6 TO REG-STOR DISPLAY----AP6 0130A 13Jb IOR OR D04DA 11FD 10-B2G2 - STUR RD BUS BYTE 1 BIT 7 PWR-DE503AT6-- GATE BYTE 1 TO SA REG-RS301AC4-H-B2G2 -- ASSEMBLED RD BUS BIT 7 TO SA-AQ2 2 485109 -RS102 LA-B2G2 T B090 A | DR 03HDA 02JD P11DA 01FD - READ BYTE 2 BIT 7- GATE BYTE 2 TO SA REG-OR -RL101AH2--RS301AC7--DE 307AS2--RS 301 AC0--RA101BJ4-OR N + B BUS BIT 7--togn togn Ď09L لـ826عـما -SM950 + BIT 7 TO REG-STOR DISPLAY----AR6 LA--B2G2-J U06 B13DA OR OR - STOR RD BUS BYTE 0 BIT P PWR-RL201AJ2-11 12DA A-B2G2 - STOR RD BUS BYTE 1 BIT P PWR-DE503AU6-- ASSEMBLED RD BUS BIT P TO SA-AS2 R\$109 N R5102 A-B2G2 - READ BYTE 2 BIT P----RL101AJ2. - READ BYTE 3 BIT P PWR-----DE307AT6-103JDA | OR + B BUS BIT P--RA301 AH4-N OR PSCS-P05DA 101FD 10-B2G2 -SM950 + BIT P TO REG-STOR DISPLAY----AT6 LOC. TYPE A-B2G2 0695 SA AND DISPLAY ASSEM BIT 6070P --E.C.-HISTORY-01 IBM CORP. SDD DATE LAST EC 106-15-73 437310 000

P.N. 2345942

000 RS309



LOC. TYPE A-B2G2 0695

000 Sm950

+ ADDRESS COMPARE IND ACRLA205AR6*	#EXIT*	-SM951 + ADDR COMP IND ACRAA1
+ CLOCK STOPPED SCRID DRIVE-LA205AQ6*	TO	-SM951 + MACHINE STOP INDAA3
+ CHECK 1 TO INDICATORRC105BE6*		-SM951 + MACHINE CHECK 1 IND
+ CHECK 2 TO INDICATOR		—SM951 + MACHINE CHECK 2 IND————————————————————————————————————
+ ALL IFC DISABLED IND	#EXIT#	
+ ADDR-CHK-PROG DISP BIT P HI-RL305BC6*	LAMPS	-sm951 + ADDRJERR HI BYTE BIT P INDAB5
+ ADDR-CHK-PROG DISP BIT 0-RL305AU6*		-SM951 + ADDRJERR HI BYTE BIT 0 IND
+ ADDR-CHK-PROG DISP BIT 1-RE305AV6*	- AC	-SM951 + ADDRJERR HI BYTE BIT 1 IND-AC1
+ ADDR-CHK-PROG DISP BIT 2RL305AW6*	ILAMPS	-SM951 + ADDRJERR HI BYTE BIT 2 INDAC3
+ ADDR-CHK-PROG DISP BIT 3RL305AX6*		-SM951 + ADDRJERR HI BYTE BIT 3 INDAC5
+ ADDR-CHK-PROG DISP BIT 4RL305AY6*		SM951 + ADDRJERR HI BYTE BIT 4 IND
+ ADDR-CHK-PROG DISP BIT 6RL305BA6*	*EXIT*	
+ ADDR-CHK-PROG DISP BIT 7RL305BB6*	LĂMPS	
+ ADDR-CHK-PROG-DISP BIT P LO-RL405BC6*		-SM951 + ADDRJERR LO BYTE BIT P IND-AD7
+ ADDR-CHK-PROG-DISP BIT 8RL405AU6*	- AE-   *EXIT*	-SM951 + ADDRJERR LO BYTE BIT 0 INDAE1
+ ADDR-CHK-PROG-DISP BIT 9RL405AV6*	TO LAMPS	-SM951 + ADDRJERR LO BYTE BIT 1 IND
+ ADDR-CHK-PROG-DISP BIT 10RL405AW6*		-SM951 + ADDRJERR LO BYTE BIT 2 INDAE5
+ ADDR-CHK-PROG-DISP BIT 11RL405AX6*	AF3	SM951 + ADDRJERR LO BYTE BIT 3 INDAE7 SM951 + ADDRJERR LO BYTE BIT 4 INDAF1
+ ADDR-CHK-PROG-DISP BIT 13RL405AZ6*	*EXIT*	-SM951 + ADDR'ERR LO BYTE BIT 5 INDAF3
+ ADDR-CHK-PROG-DISP BIT 14RL405BA6*	LAMPS	-SM951 + ADDRJERR LO BYTE BIT 6 INDAF5
+ ADDR-CHK-PROG-DISP BIT 15RL405BB6*		-SM951 + ADDRJERR LO BYTE BIT 7 IND-AF7
	#EXIT#	SM951 + REGUSTOR DISP BIT P
+ BIT 0 TO REG-STOR DISPLAY-RS302AP6*	LAMPS	-SM951 + REG'STOR DISP BIT 0AG3
+ BIT 1 TO REG-STOR DISPLAY——R\$302AR6*		
+ BIT 2 TO REG-STOR DISPLAY-R\$302AT6+ + BIT 3 TO REG-STOR DISPLAY-R\$303AP6+	AEXIT#	SM951 + REGISTOR DISP BIT 2
+ BIT 4 TO REG-STOR DISPLAY-RS303AR6*		-SM951 + REGUSTOR DISP BIT 4
+ BIT 5 TO REG-STOR DISPLAY-R\$303AT6*	LHIPS	SM951 + REGISTOR DISP BIT 5-AH5
+ BIT 6 TO REG-STOR DISPLAYRS304AP6*		—SM951 + REG STOR DISP BIT 6
+ DC RDY SCRID DRIVE-La205AM6*	#EXIT#	SM951 + POWER ON IND
+ ADDR COMP IND SWDE505AC6*	LAMPS	SM951 + ADDR COMP IND SW
I FIRM COLUMN AND COLU		
- ADDR COMPARE SYNC ACR-KK304AR6*		_SM954 _ ADDR COMP SYNC JACK 1AK1
	TO CE PNL	
- ADDR COMP SYNC 2		-SM954 - ADDR COMP SYNC JACK 2

AR101GN6 LA205AQ6 RL305Au6 RL305BC6 RL405AZ6 RS302AT6 RS304AT6

2 01A-B2V2D02 1 01A-B2V2B04 1 01A-B2V3B05 1 01A-B2V3B02 1 01A-B2V2D07 1 01A-B2V2D04

DE505AC6 LA205AR6 RL305AX6 RL405AU6 RS303AP6

1 01A-B2V2B08 1 01A-B2V2B03 1 01A-B2V3B07 1 01A-B2V3D04 RL405BB6 RS303AP6

1 01A-B2V2D09 RC105BB6 RL305AX6 RL405AU6 RL405BB6 RS303AP6

1 01A-B2V2D13 1 01A-B2V2B07 1 01A-B2V3B08 1 01A-B2V3D05 1 01A-B2V3D05 1 01A-B2V2D10 RS303AP6

1 01A-B2V2D13 1 01A-B2V2B07 1 01A-B2V3B08 1 01A-B2V3D05 1 01A-B2V3D05 1 01A-B2V2D11 RS303AP6

1 01A-B2V2B01 1 01A-B2V2B05 1 01A-B2V3B06 RL405BC6 RS303AP6

1 01A-B2V2B01 1 01A-B2V2B05 1 01A-B2V3B06 RL405BC6 RS303AP6

1 01A-B2V2B01 1 01A-B2V2B05 1 01A-B2V3B06 RS303AP6

1 01A-B2V2B01 1 01A-B2V2B05 1 01A-B2V3B06 RS303AP6

1 01A-B2V2B01 1 01A-B2V2B05 1 01A-B2V3B06 RS303AP6

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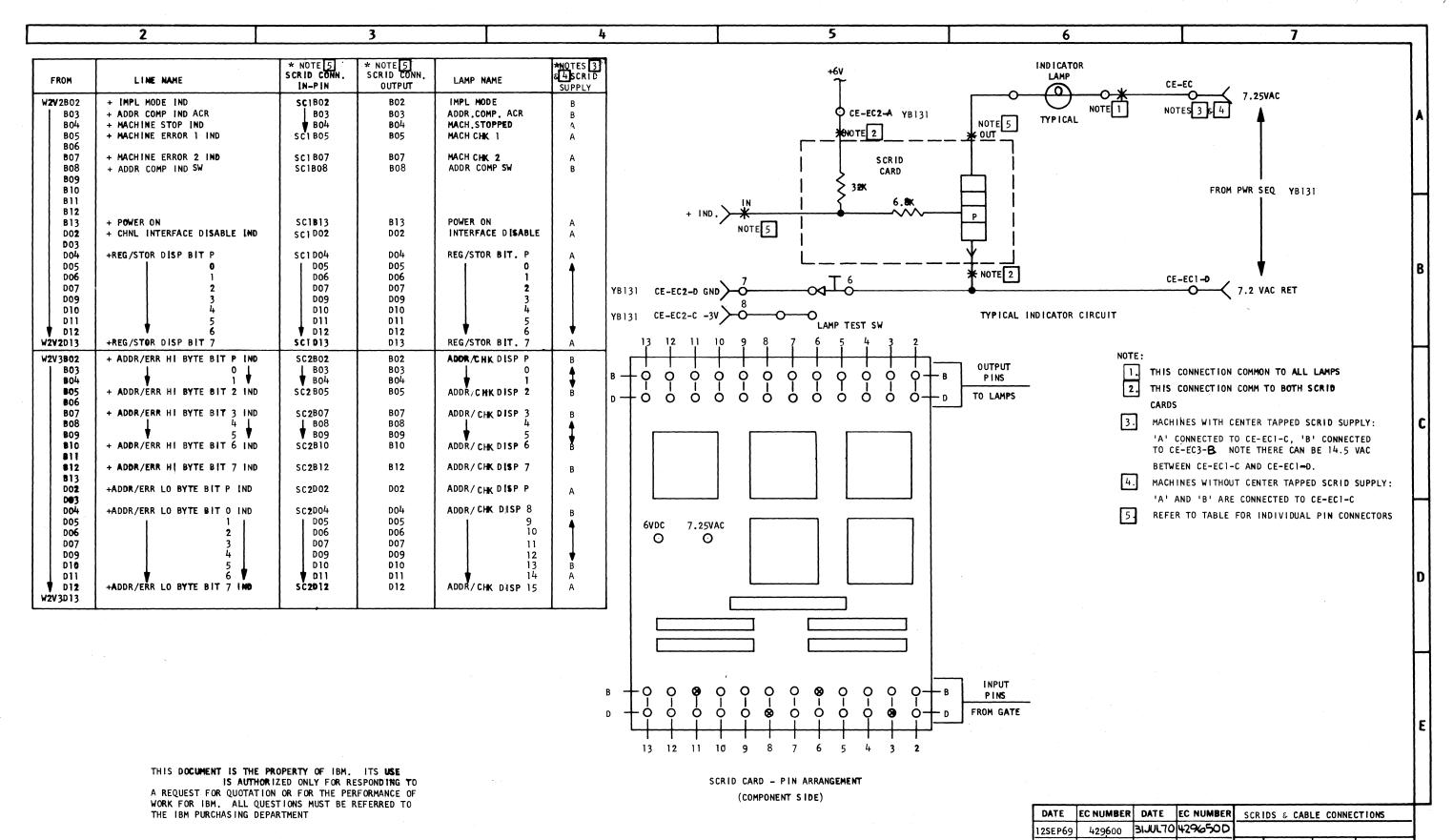
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1 01A-B2V2B03 1 01A-B2V3B06 RS30AP6

1 01A-B2V2B01 1 01A-B2V2D05 1 01A-B2V2D12 RS30AP6

1 01A-B2V2B03 1 01A-B2V3B09 1 01A-B2V2D05 1 01A-B2V2D13

LOC• TYPE



DATE JUN69

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**4296**53

429657

8DEC 70 21APR 71

5 JUL 73

429600B

429650

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4N0V69

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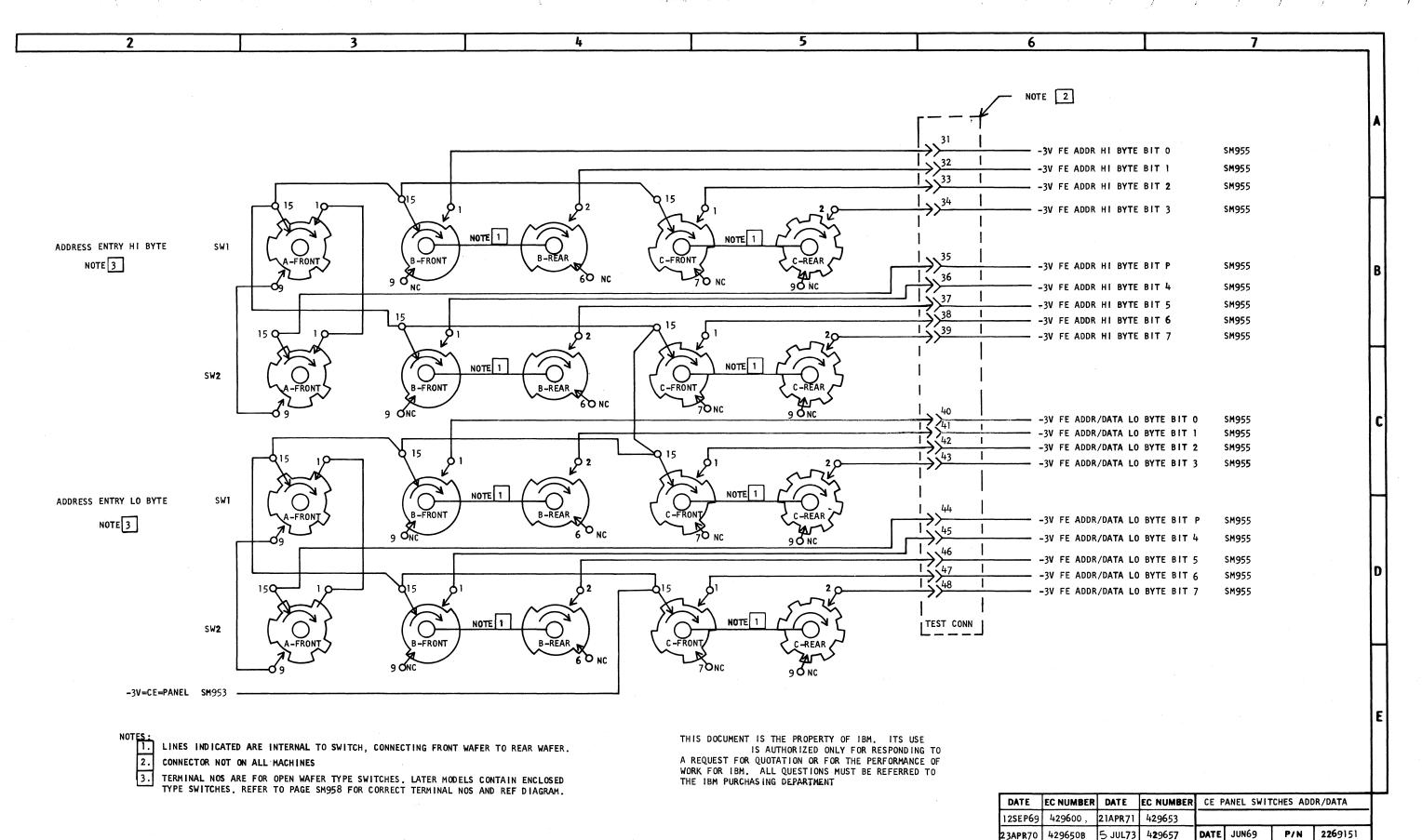
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23APR70 429650B

P/N

SM951

TYPE



TYPE

IBM

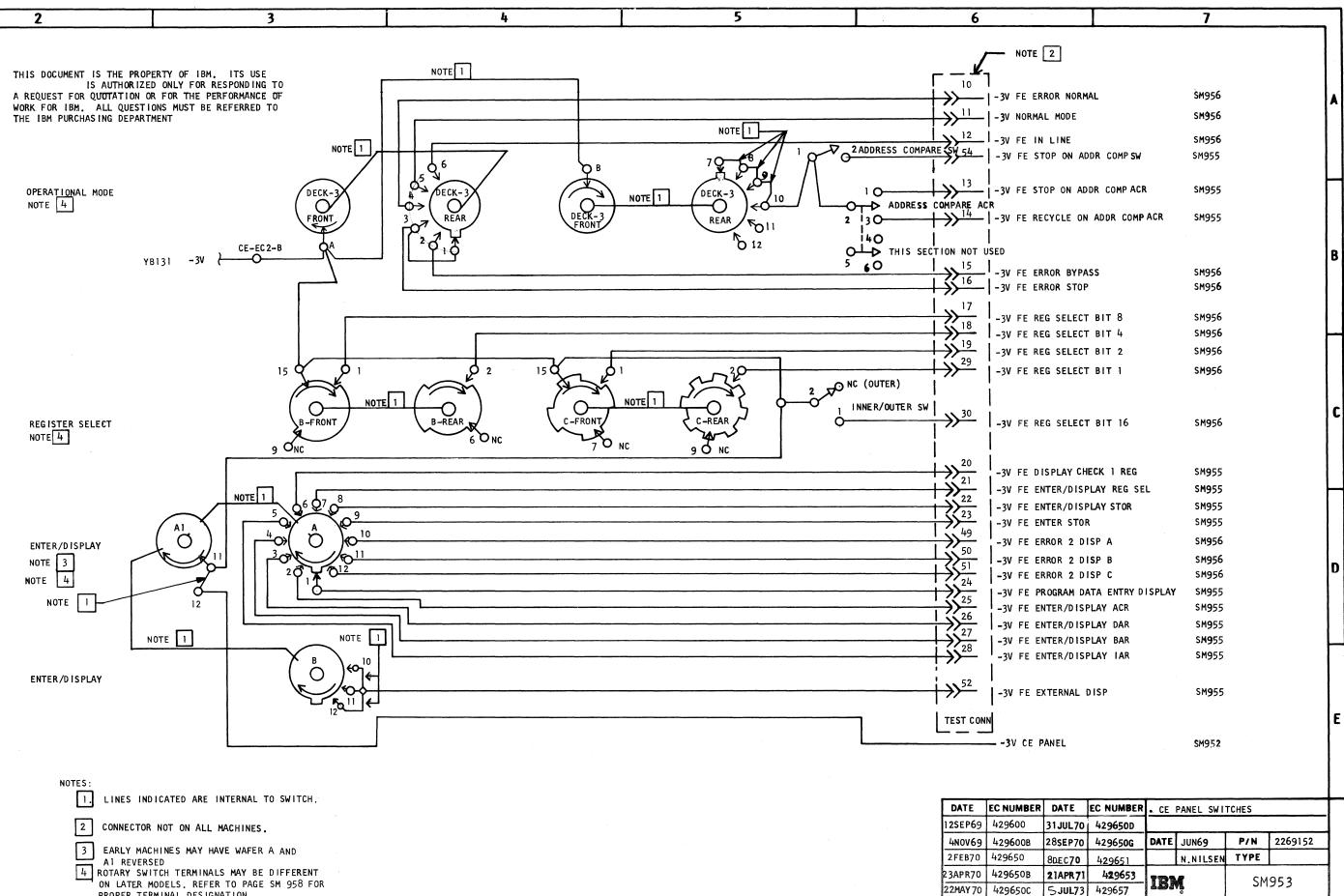
SM952

429650C

3111170 4296500

8DEC 70 429651

22MAY 70



PROPER TERMINAL DESIGNATION.

5 6 2 3 4 4 O THIS SECTION NOT USED -3V FE MPL SK OUT/LOAD HD MPL CONTROL SM956 -3V FE MPL SK IN/LOAD HD YB131 -3V SM956 FE S/I STOP N/C CE-EC2-C S/1 STOP NOTE 2 SM956 FE P/B SW DEGLITCH (3) 5 ℃ FE RESET N/C RESET NOTE 2 (3) 5 € SM956 FE START N/C START NOTE 2 (3) 5 ℃ SM956 FE IMPL/MPL PWR ON N/C NOTE 2 FE IMPL P/B DEGLITCH (3) 50 FE EXECUTE N/C SM956 EXECUTE NOTE 2 (3) 5 C FE CHECK RESET N/C SM956 CHECK RESET / LAMP TEST TEST CONN NOTE 2 NOTES 1. CONNECTOR NOT ON ALL MACHINES **I** (3) CE-EC2-D 2. LATER MACHINES CONTAIN TOGGLE SWITCHES RATHER THAN PUSH BUTTON TYPE. **-DO** 7 (4) -SYNC 1 TERMINAL NUMBERS IN PARENTHESIS ARE FOR TEST JACK YB131 7.25V AC 2 CE-EC1-D (6) 8 O-TOGGLE TYPE SWITCHES. (Q) LATER MACHINES HAVE ONLY 2 EDGE CONNECTORS. EDGE CONNECTOR LOCATIONS IN PARENTHESIS ARE FOR LATER MACHINES. OCE-EC4-E YB131 DC-GND THIS DOCUMENT IS THE PROPERTY OF IBM. ITS USE IS AUTHORIZED ONLY FOR RESPONDING TO (CE-EC1-E) 55 A REQUEST FOR QUOTATION OR FOR THE PERFORMANCE OF ADDR COM SYNC ACR SM950 AK1 NOTE 3 WORK FOR IBM. ALL QUESTIONS MUST BE REFERRED TO CE-EC2-H THE IBM PURCHASING DEPARTMENT **TEST** CONN 910 (O) TEST JACK DATE EC NUMBER DATE EC NUMBER CE PANEL 34296500 12SEP69 429600 ADDR COM SYNC SW SM950AK5 } 8DEC70 DATE JUN69 P/N 2269153 4N0V69 429600B **4296**51 CE-EC4-H NOTE 1 TYPE 2FEB70 429650 21APR71 429653 (CE-ECI-H) 23APR70 4296508 5 JUL 73 429657 IBM SM954 NOTE 3 22MAY 70 429650C

000 Sm955

	AA_	
-3V FE ADDR HI BYTE BIT P	#ENTR*	-30 CE ADDR HI BYTE BIT P-AA2  KP102 RI 304  RI 304 -30 CE ADDR HI BYTE BIT 0-AA4
-3V FE ADDR HI BYTE BIT 0 OO2 OO2	CE PNL	THE OF ORDER HE DIST. THE A CO.
-3V FE ADDR HI BYTE BIT 1-003-3	OB	KP102 -3V CE ADDR HI BYTE BIT 1AA6
-3v FE ADDR HI BYTE BIT 2	#ENTR*	
-3V FE ADDR HI BYTE BIT 3	CE PNL	
-3V FE ADDR HI BYTE BIT 4		
-3v FE ADDR HI BYTE BIT 5		-3V CE ADDR HI BYTE BIT 5-AC2
*SM952 -3V FE ADDR HI BYTE BIT 6		L-KP102 41304
*SM952 008		—————————————————————————————————————
-3V FE ADDRIDATA LO BYTE BIT P	——————————————————————————————————————	
*SM992 O10-1	#ENTR*	L-KP103 UR304 UR404
*SM952 -3V FE ADDRIDATA LO BYTE BIT 1		-TV CE ADDRIDATA LO BYTE BIT 1-ADA
*SM952 012J	AE .	
-3V FE ADDRIDATA LO BYTE BIT 2 O13-1	#ENTR# #	-WP103 -WR304 -WR404 - WR504 -
-3V FE ADDRIDATA LO BYTE BIT 3  *SM952  -3V FE ADDRIDATA LO BYTE BIT 4	CE PNL	-3V CE ADDRJ DATA LO BYTE BIT 3-AE4 L-KP103
*\$M952 015		-KP103 48A304 48L404
-3V FE ADDRJ DATA LO BYTE BIT 5	#ENTR*	
-3V FE ADDRIDATA LO BYTE BIT 6	CE PNL -	-3V CE ADDRIDATA LO BYTE BIT 6-AF4 -RA304
-3V FE ADDR DATA LO BYTE BIT 7		RA304 -3V CE ADDRI DATA LO BYTE BIT 7-AF6
-3V FE STOP ON ADDR COMP	—————————————————————————————————————	KK304 -3V CE STOP ON ADDR COMP 1AG2
#\$M953 -3V FE RECYCLE ON ADDR COMP	FROM CE PNL	KK304 -3V CE RECYCLE ON ADDR COMPAG4
-3V FE IN LINE DATA ENTRY		KP104 -3V CE IN LINE DATA ENTRY-AG6
-3V FF ENTER DISPLAY ACR		KP104 -3V CE ENTER DISPLAY ACR
#\$M953 022	FROM CE PNL -	LAZOZ -3V CE ENTER'DISPLAY DAR-AH4
#SM953 O23		KP104 -3V CE ENTER DISPLAY BAR-AH6
<b>₩S</b> M953 024——		KK203 -3V CE ENTER DISPLAY IAR
-3V FE ENTER DISPLAY IAR	#ENTR#	
#SMP53 -3V FE ENTER DISPLAY REG SEL	LEE PIUL J	
#SM953 027-	<del>г</del> АК¬	West and the manufacture of the second secon
-3V FE DISPLAY ERR 1 REG	#ENTR#	KP104 -3V CE DISPLAY ERR 1 REGAK2
-3V FE ENTER STOR	CE PNL -	KK203 -3V CE ENTER STOR-AK4
-3V FE EXT REG DISPLAY		RS301 -3V CE EXT DISPLAY-AK6

AR2 A-B2V4B02 AD2 A-B1V5B05 AE2 A-B1V5B02 AF2 A-B1V5D06 AG2 A-B2V5B12 AK2 A-B2V5D02 AA4 A-B2V4B03 A 01A-B1U6B04 A 01A-B1U6E04 A 01A-B1U6E02 AG4 A-B2V5B13 AK4 A-B2V5D03 AA6 A-B2V4B04 A 01A-B2U1B11 A 01A-B2U1E11 A 01A-B2U1E13 AG6 A-B2V5B02 AK6 A-B2V5D04 AB2 A-B2V4B05 AD4 A-B1V5B06 AE4 A-B1V5B03 AF4 A-B1V5D07 AH2 A-B2V5B03 AB4 A-B2V4B06 A 01A-B1U6C04 A 01A-B1V6A04 A 01A-B2V4B04 A 01A-B2V5B04 AB6 A-B2V4B02 A 01A-B1U6C04 A 01A-B2V1A11 A 01A-B2V1B13 AH6 A-B2V5B05 AC2 A-B2V4D03 AD6 A-B1V5B07 AE6 A-B1V5D05 AF6 A-B1V5D04 AJ2 A-B2V5B06 AC4 A-B2V4D04 A 01A-B1U6D04 A 01A-B1V6B04 A 01A-B1V6B05 AJ4 A-B2V5B07 AC6 A-B2V4D05 A 01A-B2U1D11 A 01A-B2V1B11 A 01A-B2V1B13 AJ6 A-B2V5B06

LOC. TYPE

CE PANEL ENTRY

--E.C.-HISTORY---B MACH.3830-2

437301

FRAME

01

1BM CORP. SDD

06-15-73 437310

P.N. 2345951

000 Sm956

-3v FE REG SELECT BIT 1-		#ENTR# #	
*SM953 -3V FE REG SELECT BIT 2-	001—	FROM CE PAIL	DE504 -3V CE REG SELECT BIT 2BA4
<b>☆S</b> ⋔953	002	CE PINL	DESOT TAY OF REG SELECT BIT 4
-3V FE REG SELECT BIT 4	003	BB-	DESON -30 CE REG SELECT BIT
-3V FE REG SELECT BIT 8-		#ENTR*	DE504 -3V CE REG SELECT BIT 8-BB2
*SM953 -3V FE REG SELECT BIT 16-	004—		DE504 -3V CE REG SELECT BIT 16-BB4
*SM953 -3v FE IN LINE	005		KP104 -3V CE IN LINE-BB6
*SM953	00 <del>6—J</del>	вС-	
-3V NORMAL MODE	007—	#ENTR*	KP104 -3V NORMAL MODE-BC2
-3V FE ERROR NORMAL	008——	CE PNL	-3V CE ERROR NORMALBC4
-3V FE ERROR STOP	009		
-3V FE ERROR BYPASS	009	#ENTR#	
#SN053	010	(FROM )	L-LA204 LRC104
-3 FE PJB SW DEGLITCH *SM954	011	CE PNL -	KP101 -3V CE PJB SW DEGLITCH-BD4
-3 FE START Nº C	012—3		KP101 −3V CE START NU C BD6
-3 FE RESET NO C-		#ENTR# FROM	KP101 -3V CE RESET NV C
*SM954 -3 FE SJI STOP NJC	013	FROM   CE PNL	KP101 -3V CE SJI STOP NJCBE4
*SM954 -3 FE CHECK RESET Nº C	014		KP101 -3V CE CHECK RESET NV C-BE6
*SM954	015—أ	BF-	
-3 FE EXECUTE NJ C	016	*ENTR*	KP101 -3V CE EXECUTE Nº C-BF2
-3 FE STOP ON ADDR COMP SW.	027	CE PNL -	DE505 -3V CE STOP ON ADDR COMP 2-BF4
-3 FE IMPLIMPL PWR ON NIC			LA205 -3V CE IMPLIMPL PWR ON NIC-BF6
	018	#ENTR#	LOUGH THE STATE OF THE COLUMN THE
-3V FE MPL SK IN-LOAD HD	020	(FROM )	LA101 -3V CE MPL SK INJLOAD HDBG2
-3V FE MPL SK OUT LOAD HD *SM954	021-	CE PNL	LA101 -3V CE MPL SK OUT LOAD HDBG4
FE IMPL PJB DEGLITCH NJO	028		LAZOS FE IMPL P'B DEGLITCH N'D-BG6
		#ENTR#	
		FROM CE PNL	
-3V FE ERR 2 DISP A	024		
-DA LE EKK E DION H			TO OF OHE DEAL MANAGEMENT OF THE PROPERTY OF T
-3V FE ERR 2 DISP B	025	*ENTR*	-3V CE CHK 2 DISP BBJ2
-3V FE ERR 2 DISP C-	026		

BA2 A-B2V5B09 2 01A-B2A2D11 BF6 A-B2V4B10 2 01A-B2V6B04
BA4 A-B2V5B10 BD2 A-B2V4B07 BG2 A-B1V5D02
BA6 A-B2V5D09 BD4 A-B2V4B11 BG4 A-B1V5D03
BB2 A-B2V5D10 BD6 A-B2V4B12 BG6 A-B2V5D12
BB4 A-B2V5D11 BE2 A-B2V4B13 BH6 A-B2V5D05
BB6 A-B2V4D06 BE4 A-B2V4B11 2 01A-B2V6A02
BC2 A-B2V4D07 BE6 A-B2V4D12 BJ2 A-B2V5D06
BC4 A-B2V4B09 BF2 A-B2V4D13 2 01A-B2V6B02
BC6 A-B2V4B08 BF4 A-B2V5D08 BJ4 A-B2V5D07

LOC. TYPE

CE PANEL I			١.
	MACH-3830 FRAME IBM CORP- P-N-2345	SDD	SFI 95 6

2 3 4 5 7

NET NUMBER

SM955AG6 W2V5B02

CONNECTION

LINE NAME	NET Number	CONNECTION
-3V FE ADDR/DATA LO BYTE BIT 2	SM955AE2	W1V5B02
-3V FE ADDR/DATA LO BYTE BIT 3	SM955AE4	1 B03
		B04
-3V FE ADDR/DATA LO BYTE BIT P	SM955AD2	B05
-3V FE ADDR/DATA LO BYTE BIT O	SM955AD4	В06
-3V FE ADDR/DATA LO BYTE BIT 1	SM955AD6	B07
		В08
		B09
		B10
	l	B11
		B12
	ļ	B13
-3V FE MPL SK IN/LOAD HD	SM956BG2	D02
-3V FE MPL SK OUT/LOAD HD	SM956BG4	D03
-3V FE ADDR/DATA LO BYTE BIT 7	SM955AF <b>6</b>	D04
-3V FE ADDR/DATA LO BYTE BIT 4	SM955AE6	D05
-3V FE ADDR/DATA LO BYTE BIT 5	SM955AF2	D06
-3V FE ADDR/DATA LO BYTE BIT 6	SM955AF4	▼ D07
		WIV5DI3

3	-3V FE ENTER/DISPLAY ACR	SM955AH2	1 B03
	-3V FE ENTER/DISPLAY DAR	SM955AH4	P04
;	-3V FE ENTER/DISPLAY BAR	SM955AH6	В05
.	-3V FE ENTER/DISPLAY IAR	SM955AJ2	В06
,	-3V FE ENTER/DISPLAY REG SEL	SM955AJ4	В07
3	-3V FE ENTER/DISPLAY STOR	SM955AJ6	800
,	-3V FE REG SELECT BIT 1	SM956BA2	В09
)	-3V FE REG SELECT BIT 2	SM956BA4	B10
			B11
<u> </u>	-3V FE STOP ON ADDR COMPACE	SM955AG2	B12
3	-3V FE RECYCLE ON ADDR COMPACE	SM955AG4	B13
.	-3V FE DISPLAY CHECK REG	SM955AK2	D02
	-3V FE ENTER STOR	SM955AK4	D03
	-3V FE EXT DISPLAY	SM955AK6	D04
;	-3V FE ERR 2 DISP A	SM956BH6	D05
,	-3V FE ERR 2 DISP B	SM9568J2	D06
'	-3" FE ERR 2 DISP C	SM956BJ4	D07
	-3V FE STOP ON ADDR COMP SW	SM956BF4	80d
	-3V FE REG SELECT BIT 4	SM956BA6	D09
	-3V FE REG SELECT BIT 8	SM956BB2	D10
	-3V FE REG SELECT BIT 16	SM956BB4	<b>7</b> D11
	-3V FE IMPL P/B DEGLITCH	SM956BG6	D12
	-ADDR COMP SYNC ACR	SM950AK1	W2V5 D13
10N			

LINE NAME

-3V FE PROGRAM DATA ENTRY DISPLAY

LINE NAME	NET NUMBER	CONNECTION
-3V FE ADDR HI BYTE BIT P	SM955AA2	W2V4B02
-3V FE ADDR HI BYTE BIT O	SM955AA4	1 B03
-3V FE ADDR HI BYTE BIT 1	SM955AA6	B04
-3V FE ADDR HI BYTE BIT 2	SM955AB2	B05
-3V FE ADDR HI BYTE BIT 3	SM955AB4	В06
-3V FE ERROR BYPASS	SM956BD2	B07
-3V FE ERROR STOP	SM956BC6	В08
-3V FE ERROR NORMAL	SM956BC4	B09
FE IMPL/MPL PWR ON N/C	SM956BF6	B10
FE P/B SW DEGLITCH	SM956BD4	B11
FE START N/C	SM956BD6	B12
FE RESET N/C	SM956BE2	B13
-3V FE ADDR HI BYTE BIT 4	SM955AB6	D02
-3V FE ADDR HI BYTE BIT 5	SM955AC2	D03
-3V FE ADDR HI BYTE BIT 6	SM955AC4	D04
-3V FE ADDR HI BYTE BIT 7	SM955AC6	D05
-3V FE IN LINE	SM956BBc	D06
-3V NORMAL MODE	SM956BC2	D07
-ADDR COMP SYNC SW	SM950AK5	D09
FE S/I STOP N/C	SM956BE4	011
FE CHECK RESET N/C	SM956BE6	<b>▼</b> D12
FE EXECUTE N/C	SM956BF2	W2V4D13

DATE	EC NUMBER	DATE	EC NUMBER	CE F	ANEL - CA	ABLE CON	NECTIONS
12SEP69	429600	5 JUL73	429657				
4N0V69	429600B			DATE	NOV69	P/N	2269154
23APR76	429650B					TYPE	
22MAY 70	429650C			IDI	<b>a</b>	SM9	5.7
SJULTO	429650D			- IBW 2MA21			

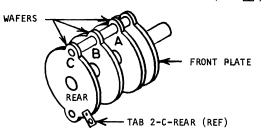
LOGIC TEST TAB TAB SECTION SECTION NAME TYPE PAGE SIGNAL NAME CONN. ENTER SM953 PROGRAM DATA 24 A-REAR DISPLAY 2 ACR 25 2 DAR 26 3 3 4 BAR 27 4 28 5 IAR 5 CHECK 1 REG 20 6 6 REG SELECT 21 7 7 22 8 8 DISP STORAGE ENT STORAGE 23 9 9 10 ERROR 2-A 49 10 11 11 ERROR 2-B 50 ERROR 2-C 51 12 12 11 11 EXT DISPLAY 52 B-REAR В PIN 2 OF -3VDC SERV. 11 12 A-FRNT Αl INNER/ -3VDC SERV. 12 B-FRNT 11 Αl OUT. SW. YB131 OPERATIONAL CE-EC1-F Α PIN-6 5 A 1 Α ON/OFF SW CE-EC2-G 7 Α 6 A 1 CE-EC1-H 8 Α Α PIN-2 12 Α 10 A 1 ON/OFF SW CE-EC3-C 14 Α 12 Δ1 CE-EC1-A В 1 В CE-EC1-G 2 В Вl 8 CE-EC4-A В 7 В CE-EC4-B 9 В В1 SM953 -3VDC SERV CE-EC2-B Α DECK-3 С JMPR TO C1 ERR. STOP ERROR BYPASS 15 2 2 ERROR STOP 16 3 3 4 ERROR NORM. 10 4 NO RMAL 11 5 5 IN-LINE 12 6 6 PIN-1 FE MODE 10 10 ACR SW. YB131 CE-EC1-E 1 D D 1 CE-EC3-D 2 DI CE-EC3-E 8 D CE-EC3-G

NOTE 5

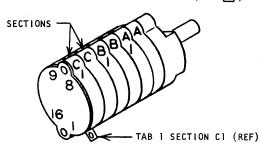
EARLY MOD.

LATE MOD.

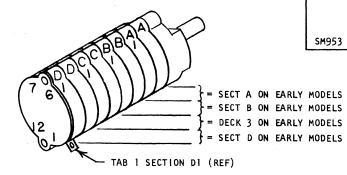
TYPE A OPEN WAFER 16 POSITION (NOTE 1)



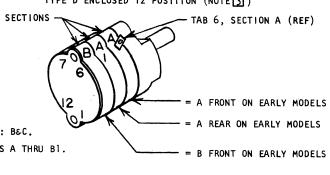
TYPE B ENCLOSED 16 POSITION (NOTE [2])



TYPE C ENCLOSED 6 POSITION



TYPE D ENCLOSED 12 POSITION (NOTE 3)



NOTES:

OPEN WAFER ADDRESS-DATA ENTRY SWITCHES HAVE 3 WAFERS: A,B&C. REGISTER SELECT SWITCH HAS 2 WAFERS: B&C.

2 ENCLOSED ADDRESS-DATA ENTRY SWITCHES HAVE SECTIONS A THRU C1, REGISTER SELECT SWITCH HAS SECTIONS A THRU B1.

3 EARLY MACHINES - SECTIONS A&A1 ARE REVERSED.

4 OPEN WAFER SWITCH: -3VDC SERVICE ON TAB 15 OF SW1, WAFER A-FRONT, B-FRONT, C-FRONT,
TAB 15 OF SW2, WAFER B-FRONT, C-FRONT
ENCLOSED SWITCH: -3VDC SERVICE ON TAB 14 OF SW1 SECTION A, SECTION C
TAB 14 OF SW2 SECTION C

5 TEST CONNECTOR NOT ON ALL MACHINES.

TAB 9 OF WAFER A FRONT, SW1, CONNECTED TO TAB 9 OF WAFER A FRONT, SW2, ON OPEN WAFER SWITCH TAB 9 OF SECTION A1, SW1, CONNECTED TO TAB 9 OF SECTION A1, SW2, ON ENCLOSED SWITCH

IBM CONFIDENTIAL UNTIL JUNE 1971, UNCLASSIFIED THEREAFTER

NOTE 5

TEST

CONN

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CE-EC2B-

JMPR-

TYPE SIGNAL NAME

BIT 0

BIT 1

BIT 2

BIT 3

BIT 4

BIT 5

BIT 6

BIT 7

BIT P

BIT 0

BIT 1

BIT 2

BIT 3

BIT 4

BIT 5

BIT 6

BIT 7

BIT P

SELECT BIT 8

SELECT BIT 4

SELECT BIT 2

SELECT BIT 1

-3VDC SERV

-3VDC SERV

NAME/SW. NO

ADDR. ENTRY

HI BYTE SWI

(NOTE 4)

(NOTE 6)

ADDR ENTRY

(NOTE 4)

ADDR/DATA

(NOTE 4) (NOTE 6) SM952

ADDR/DATA

(NOTE 4)

REGISTER

(NOTE 6)

SM952

SELECT

LO BYTE SW2

FNTRY

LO BYTE SW1

(NOTE 6)

SM952

ENTRY

HI BYTE SW2

SM952

OPEN WAFER

WAFER

B-FRNT

C-FRNT

C-REAR

B-FRNT

B-REAR

C-FRNT

C-REAR

A-FRNT

B-FRNT

B-REAR

C-FRNT

C-REAR

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B-REAR

C-FRNT

C-REAR

A-FRNT

B-FRNT

B-REAR

C-FRNT

C-REAR

B-FRNT

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14

2

14

2 B-REAR

TAB

1

2

1

2

2

15

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ENCLOSED

TAB | SECTION

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	EC HIS	TORY	DRAWING TITLE		
	9 MARTI	429651	CE PANEL SWITCH CONVERSION CHART		
	21 APR 71	4 <b>29</b> 653	MACH		
			PART NO 2269156		
			CLASSIFICATION		
C				<b>IDM</b> CORP	

620-0131-0 MRO# 780522201 ALD DRAWING FORMAT ASTROCLOTH N650T 8273

**FLEXIBLE** DISK CONTROL CABLE PADDLE PADDLE EC1-F  $\circ \circ \circ$ CONN SS-LD EC1-G -SECTOR PULSE BOTTOM TOP 3 2 1 -SS011 SOCKET SOCKET LIGHT PHOT0 3 2 1 BULB TRANS ISTOR SLT CONTROL CARD PADDLE DRV HUB GND PADDLE SERV CONN TOP +24V 13Ω D03 CONN B03 SOCKET TOP SOCKET EC1-A CONTROL CABLE 3 2 1 HEAD ENGAGE SOL. DR PADDLE PADDLE PRESSURE PAD + HEAD ENGAGE CONN CONN RELAY COIL B05 EC3-B TOP BOTTOM SS011 B02 SOCKET J02 B02 SOCKET  $\circ \circ \circ$ EC3-A 3 2 1 STEPPING MOTOR OUT COIL CONE INTERLOCK SW PADDLE SEEK OUT CONN EC3-G DC GND TOP SS-MD 000D02 D02 SOCKET **—** DO3 G03 1 2 3 EC2 257 (T1)  $\int_{0}^{2}$ PADDLE FRAME GND STEP MTR CONN EC3-E D03 TOP O EC2-E SOCKET CONTROL CABLE SEEK IN PADDLE D04 + OUT COMMAND SS-MD CONN **O** 3 BOTTOM **G**04 SS011 257 (T2) + IN COMMAND SOCKET 2 Q **PADDLE** DC CABLE STEPPING MOTOR EC3-F C ONN . D06 BOTTOM DO6 SS011 SOCKET BO8 CONN TP +240 0 J12 READ + 24V GND CONTROL CABLE HEAD READ AMP PADDLE 600 NS SS-LD CONN DET AR-DF + FILE DATA BOTTOM SS011 SOCKET TP **EC HISTORY** DRAWING TITLE NOTES: TP 1. POSITION OF TEST POINTS ARE SHOWN ON SLT CARD AND 427076A CONTROL CIRCUITS 230CT70 SIGNAL INFORMATION LABEL LOCATED ON BASE PLATE IN MACH 23FD-11 THE 23FD-11. 10MAR71 431602 DRIVE MOTOR AC CABLE PART NO 2280703 20JUL71 431602A 2. ACCEPTABLE CARD OPTIONS MOTOR ENTR 5862500 24FEB72 431630 CLASSIFICATION IBM CORP 208-SS 011 5862712 31JUL73 431640 230V 5864073 (NOISE HARDENED LINE DRIVER) CONN

ASTROCLOTH N650T

ALD DRAWING FORMAT

620-0131-0 MRO# 780522201

DRAFTING MEDIA GRAPHIC CONTROLS CORPORATION Buffelo, New York Printed in U.S.A.

000 55011

+ 3V MOVE MPL 1 TRK IN	-LA102AV2	*EXIT* TO MPL FILE ssoio	+ IN COMMAND———————————————————————————————————
†6 VOLTS YB180	- 001 - 002 - 003 - 004 - 005 - 006	#EXITA TO MPL FILE SSO10	16 VOLTS
208 VOLT AC YB180	- 007 - 008 - 009	#EXIT* TO MPL FILE SSO10	208 VOLT AC————————————————————————————————————
		#ENTRAD FROM MPL FILE SSO10	+ 3 MPL FILE DATA——AD2  LA101 - 0X+3X MPL FILE SECTOR——AD4  LA109 - 4A109

D2 A-B174B09

IBM CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED THEREAFTER 006 SW410 + WRITE BUS BYTE 0 BIT 0-LSW521 LSW561 LSW **~\_\_**S⊌509 **S**⊌565 LSW563 + WRITE BUS BYTE O BIT O-RL303AA2\* \$—Sш509 —Sш565 \*ENTR + WRITE BUS BYTE O BIT 1-RL303AB2\* + WRITE BUS BYTE O BIT 2-LSW521 LSW561 LSW FEED LSW563 —Sы509 —Sы565 + WRITE BUS BYTE O BIT 2-RL303AC2\* + WRITE BUS BYTE O BIT 3-RL303AF2\* \$—SШ509 —SШ565 + WRITE BUS BYTE 0 BIT 5 -AC3 \$₩509 \$₩565 WRITE BUS BYTE O BIT 4-+ WRITE BUS BYTE O BIT 4-RL303AG2\* **ў—**SЫ509 **1—**SЫ565 LSW563 \*ENTR\* LSh1521 LSW561 + WRITE BUS BYTE O BIT 5-----RL303AH2\* FEED + WRITE BUS BYTE O BIT 6-**-**SW509 LSW563 + WRITE BUS BYTE O BIT 6-RL303AL2\* LSW521 LSW561 -SW565 + WRITE BUS BYTE O BIT 7 + WRITE BUS BYTE O BIT 7----RL303AM2\* **←**S⊌509 **←**S⊌565 LŚW563 ADDRESS BIT 13--AE1 **С−**SШ503 С−SШ563 LSW558 LSW561 LSW564 LSW565 ADDRESS BIT 12 LSW558 LSW561 LSW562 LSW565 LSW 566 - STORAGE ADDRESS BIT BUS 13---RL104CV4\*-\*ENTR -SU503 **L**S₩562 - STORAGE ADDRESS BUS BIT 12-RL103DE4\*-LSW564 LSW564
ADDRESS BIT 11LSW558 LSW564
LSW564 LSW569 FEED -SW563 LSW565 LSW566 AE 5 - STORAGE ADDRESS BUS BIT 11-RL103DD4\*-—S₩503 S₩563 LSW561 LSW565 LSW562 LSW566 - STORAGE ADDRESS BUS BIT 10-RL103DC4\*-ADDRESS BIT 10-AE7 —SЫ503 —SЫ563 LSW561 LSW565 LSW 562 LSW 566 LSW558 LSW564 ADDRESS BIT 9-LSW558 LSW5 LSW564 LSW5 -001 SU503 SU563 SW561 SW565 LSW 566 ADDRESS BIT 8-58558 SW561 SW564 SW565 - STURAGE ADDRESS BUS BIT 9----RL103DB4\*-SW503 SW563 \*ENTR LSW562 LSW566 - STORAGE ADDRESS BUS BIT 8-RL103DA4\*-FEED ADDRESS BIT 7-1555 ---SW503 ----SW563 LSW562 - STURAGE ADDRESS BUS BIT 7-RL206AM2\* LSW564 LSW565 LSW566 - STURAGE ADDRESS BUS BIT 6-RL206AL2\* SW503 SW563 LSW562 LSW566 -SW501 LSW 558 ADDRESS BIT 5-SW503 LSW562 —SW502 —SW561 LSW553 LSW558 - STORAGE ADDRESS BUS BIT 5-RL206AK2\* \*ENTR\* ADDRESS BIT 4-- STORAGE ADDRESS BUS BIT 4-RL206AJ2\* <sup>L</sup>S⊎561 -S⊎565 FEED **Ç**—S⊌503 LSW558 \$-\$₩562 -\$₩566 - STORAGE ADDRESS BUS BIT 3-RL206AH2\* ADDRESS BIT 3 LSW553 LSW558 LSW563 LSW564 - STORAGE ADDRESS BUS BIT 2-RL205AG2\* \$—\$⊌503 \$—\$⊌562 \$⊌566 <sup>L</sup>Տ⊍561 LՏ⊎565 ADDRESS BIT 2-LSW552 LSW5 ბ—S⊌501 -S⊌562 LSW558 LSW561 - STORAGE ADDRESS BUS BIT 1----RL205AF2\* \*ENTR\* **--**S⊌501 ADDRESS PARITY 1-7-- STORAGE ADDR BUS BIT P 1-7-RL205AE2\* Sw553 FEED LSU558 ADDRESS PARITY 8-13-- STORAGE ADDRESS BIT P 8-13-RL104BB2\* <del>---</del>Sы553 LSW558 GO TO STORAGE-- GO TO STORAGE----KK204AN6\* -SiJ501 LS11558 LOC. TYPE WRITE DATA + ADDRESS BITS -E.C.-HISTORY-B MACH.3830-2 01 IBM CORP. SDD DATE LAST EC 006COPYRIGHT IBM CORP 1976 SIM TO PN 2345955 EC PRECOO1 PeNe 2290501

CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED THEREAFTER 005 SW420 IBM - WRITE INSTRUCTION-LSW535 LSW552 - + MACHINE RESET LSW552 LSW558 ---KK203BB2\* - WRITE INTO STORAGE LSU558 -SW501 \*ENTR\* + MACHINE RESET--KK303AH2\* -SW501 FEED -SW535 + STORAGE DIAGNOSTIC MODE-+ STORAGE DIAGNOSTIC MODE-LA303AP2\* - BC CLK T020-T060--KK205AB2# - BC CLK FT020-T0601--SU552 -SW522 + WRITE BUS BYTE 1 BIT P-----AB1 + WRITE BUS BYTE 1 BIT P------RL403AN2\* \*ENTR\* -SW521 + WRITE BUS BYTE 2 BIT P-----AB3 + WRITE BUS BYTE 2 BIT P-RL306AN2\* FEED + WRITE BUS BYTE 3 BIT P------RL406AN2\* SW535 + WRITE BUS BYTE 3 BIT P-----SW535 + WRITE BUS BYTE O BIT P-----AB7 + WRITE BUS BYTE O BIT P-RL303AN2\* + SET READ LATCHES + SET STORAGE READ LATCHES-KK205BD6\* \*ENTR\* SW531 <sup>L</sup>SW534 + SET STORAGE ERROR REG-RC105AS2\* + PERMIT PATTERN LATCHES-AC3 FEED + RESET STORAGE ERROR REG-LA303AN2\* + CLK STOPPED B-+ CLK STOPPED B-KK205AZ2\* - + IMPL LATCH-0 LSW552

#ENTR# FEED

+ IMPL LATCH-

--LA205AH6\*-

KK203BB2 A 01A-B2N6C04 RL303AN2 AA3 A-B3A2D05 AC7 A-B3A2D02

S A 01A-B2N6D04 LA205AH6 A 01A-B2C6B04 AB5 A-B3E1E13 AD1 A-B3R1B13

KK205AB2 1 01A-B2U6A04 RL305AN2 AA7 A-B3A2D05

A 01A-B2N6B04 LA303AN2 A 01A-B2E6E04 AB1 A-B3J1D13

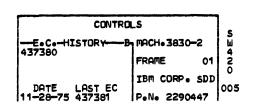
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O A 01A-B2M6E04 LA303AP2 A 01A-B2F6C02 AB5 A-B3K1B11

KK205BD6 1 01A-B1V3D12 RL406AN2 AB7 A-B3R1E11

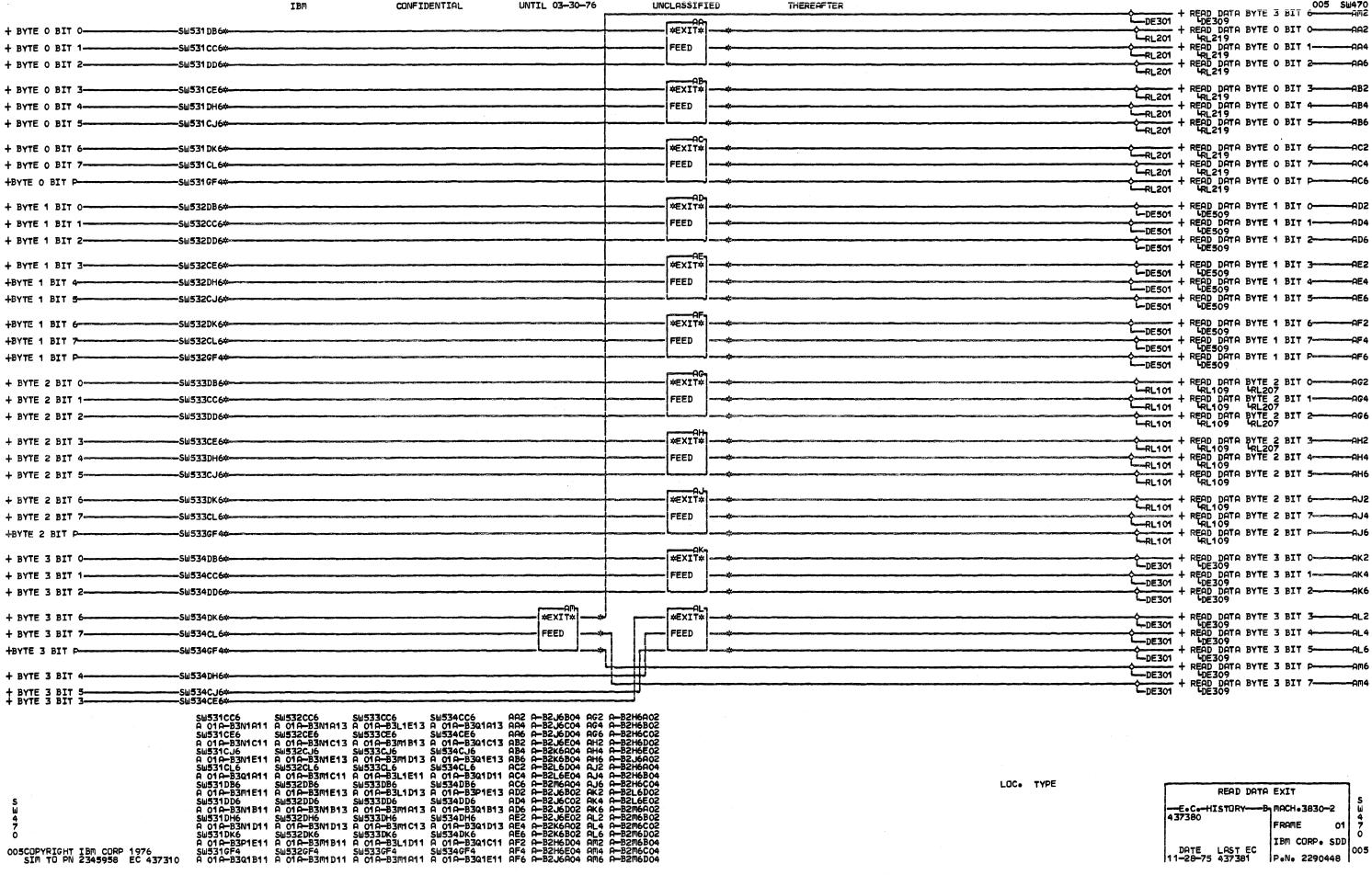
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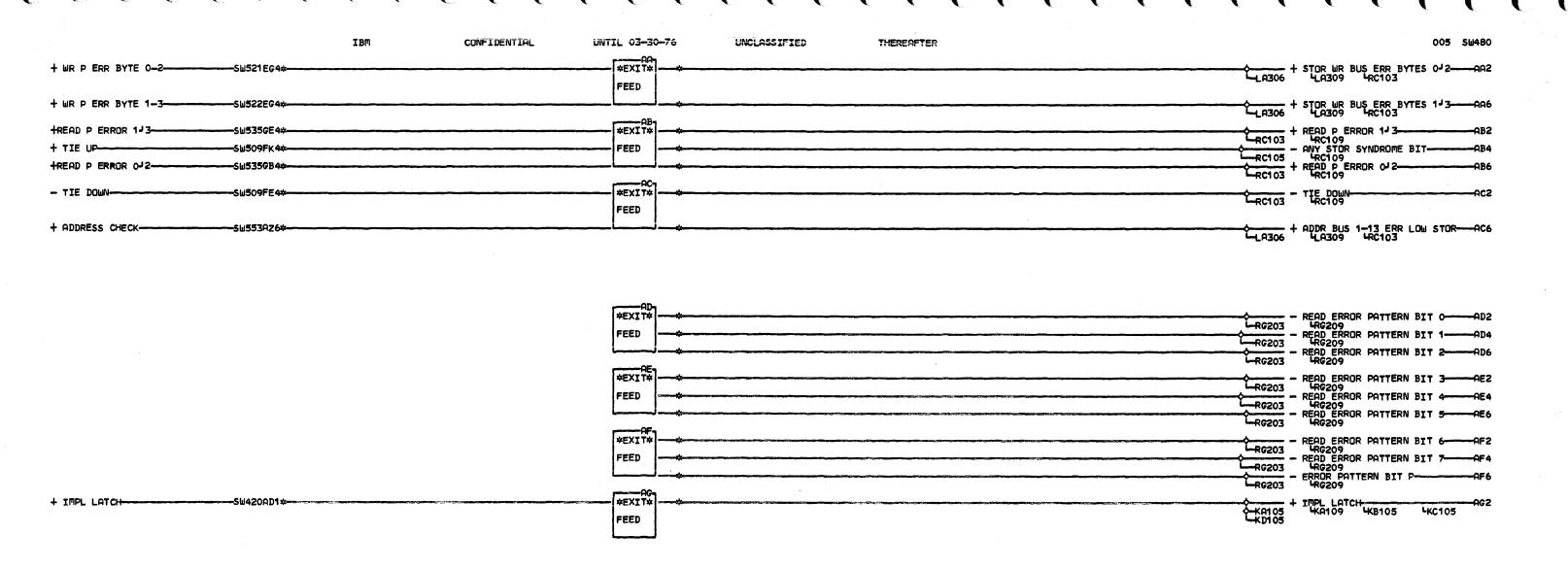
LOC. TYPE



SW480

IBM CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED THEREAFTER - + READ DATA BYTE 3 BIT 6-DE309 · + READ DATA BYTE 0 BIT 0--DE301 \*EXIT\* ---S⊌531 DB6\* + BYTE O BIT O-RL201 - + READ DATA BYTE O BIT 1-+ BYTE 0 BIT 1-----SW531 CC6 FEED RL201 + READ DATA BYTE O BIT 2-–Տա531 DD6⇔ + BYTE O BIT 2--RL201 \*EXIT + READ DATA BYTE O BIT 3--SW531 CE6# -RL201 FEED + BYTE O BIT 4--Sы531 DH6# -RL201 -SW531 CJ6\* + BYTE O BIT 5-





LOC. TYPE

ERROR + PATTERN EXIT --E.C.-HISTORY-B, MACH.3830-2 4 4 4 37380 FRAME 01 8 0 0 DATE LAST EC 11-28-75 437381 P.N. 2290449

UNCLASSIFIED

THEREAFTER

UNTIL 03-30-76

CONFIDENTIAL

IBM

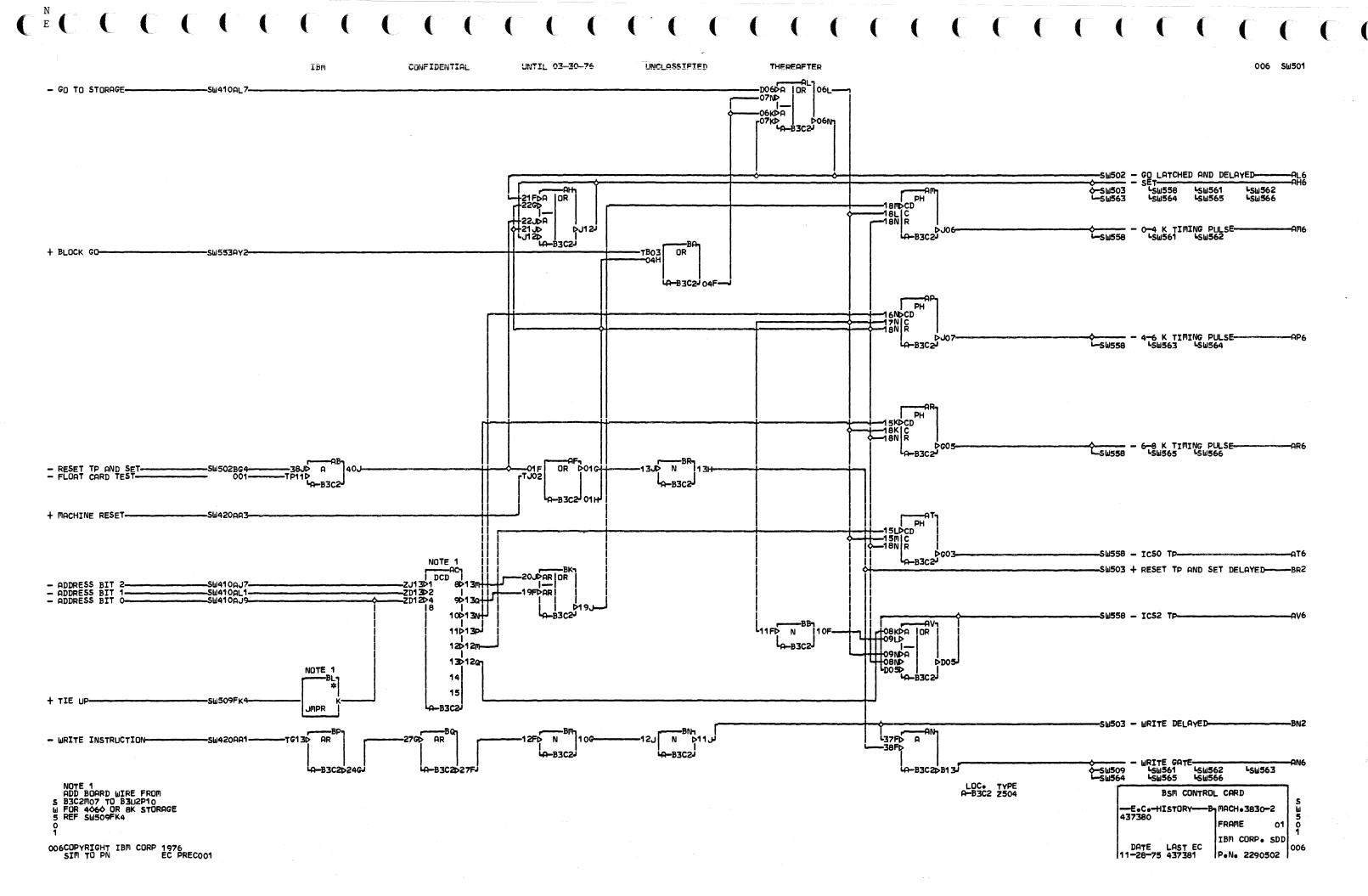
EXIT

-E.C.-HISTORY-B MACH.3830-2
437380 FRAME 01

DATE LAST EC
11-28-75 437381 P.N. 2290450

005 SW490

0 0 005COPYRIGHT IBM CORP 1976 SIM TO PN 2345960 EC 437310



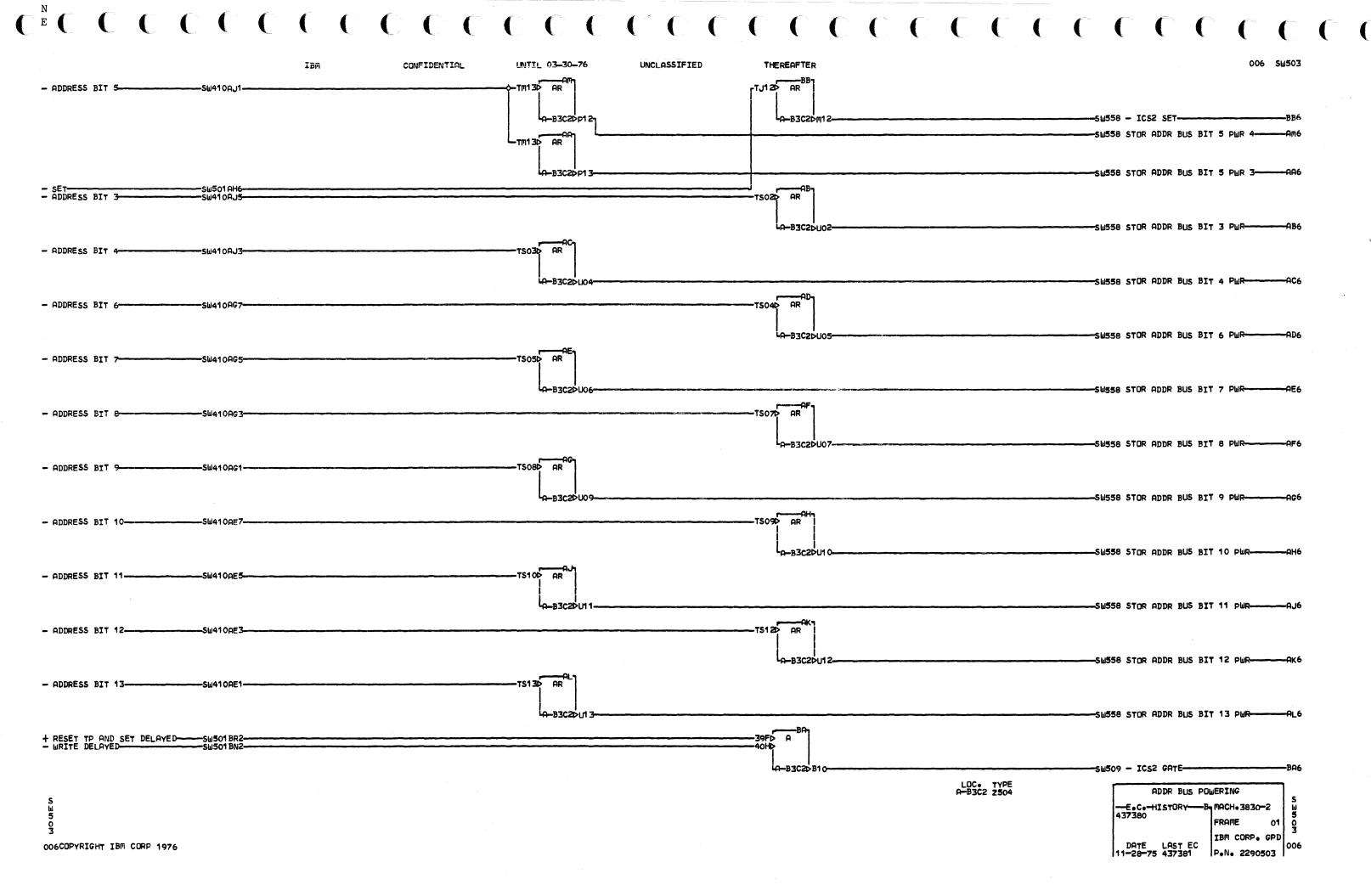
IBM CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED THEREAFTER 005 SW502 - ADDRESS BIT 5--SW410AJ1--B3C25-J09 TM13P AR 4A-B3C25G04 30NS H \*\*\*\*\*\*\*\* PIN - GO LATCHED AND DELAYED-SW501AL6-TD P4PIN A-B3C2 29KJ 28K-27K-35NS HAV PIN P4PIN A-B3C2 40NS AK \*\*\*\*\*\*\*\* PIN NOTE 1 L295 -SW501 - RESET TP AND SET-P4PIN A-B3C2 P4PIN A-B3C2 **L**275 P4PIN R-B3C2

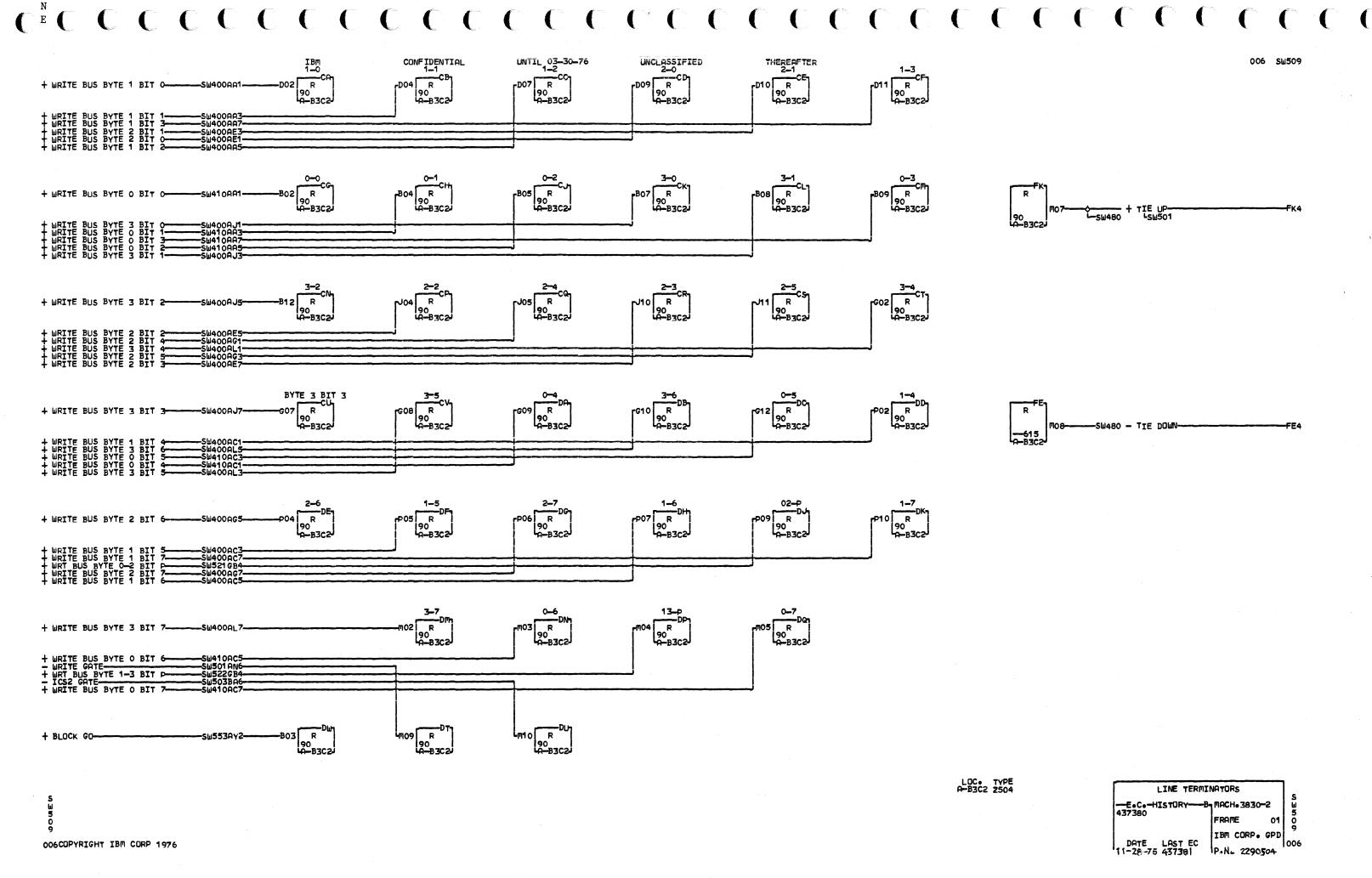
NOTE 1 PLUG FOR 55NS +8 -ONS S PULSE AT 01A-B3C2J06 S SW501AM6 5 0

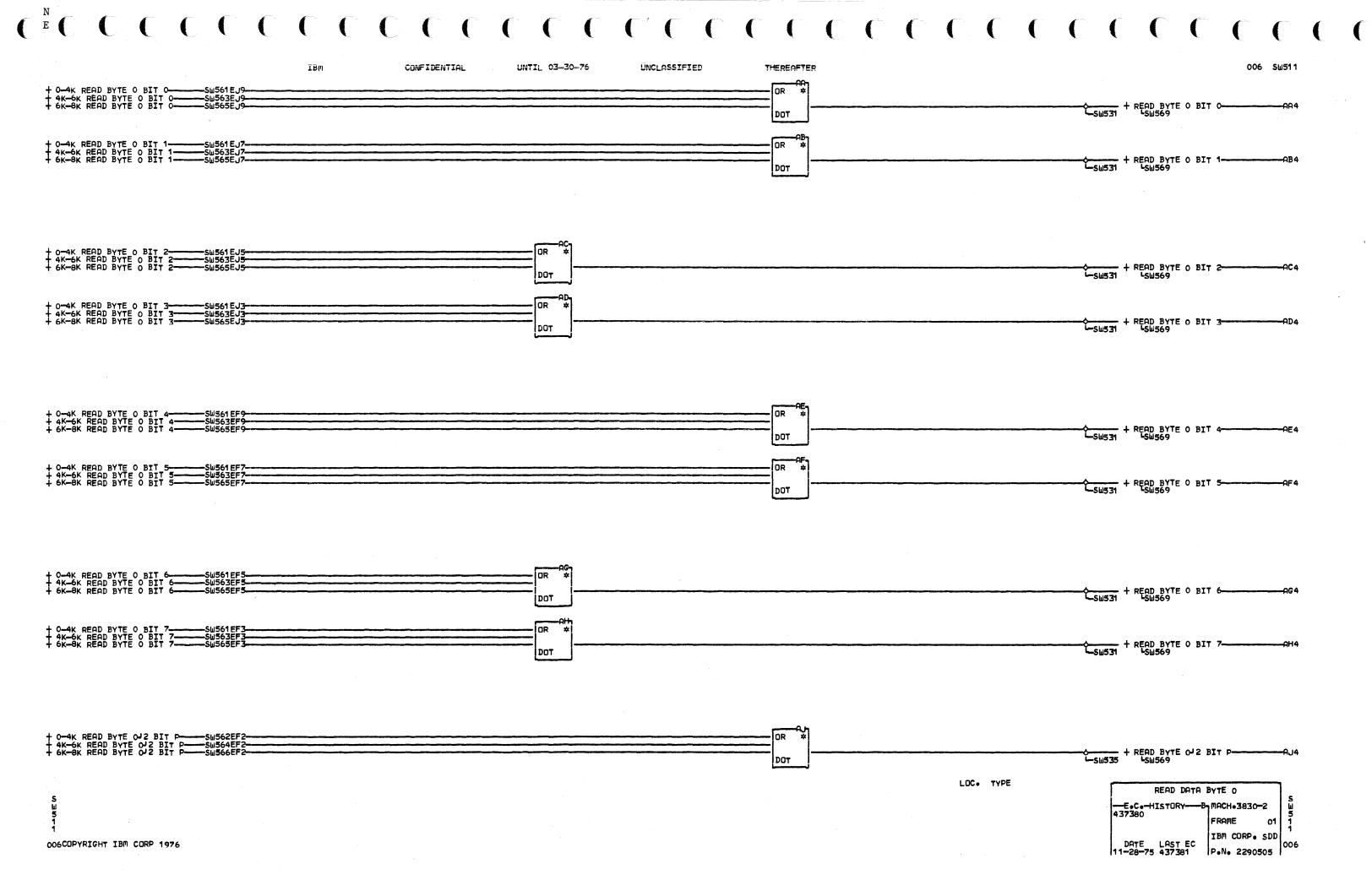
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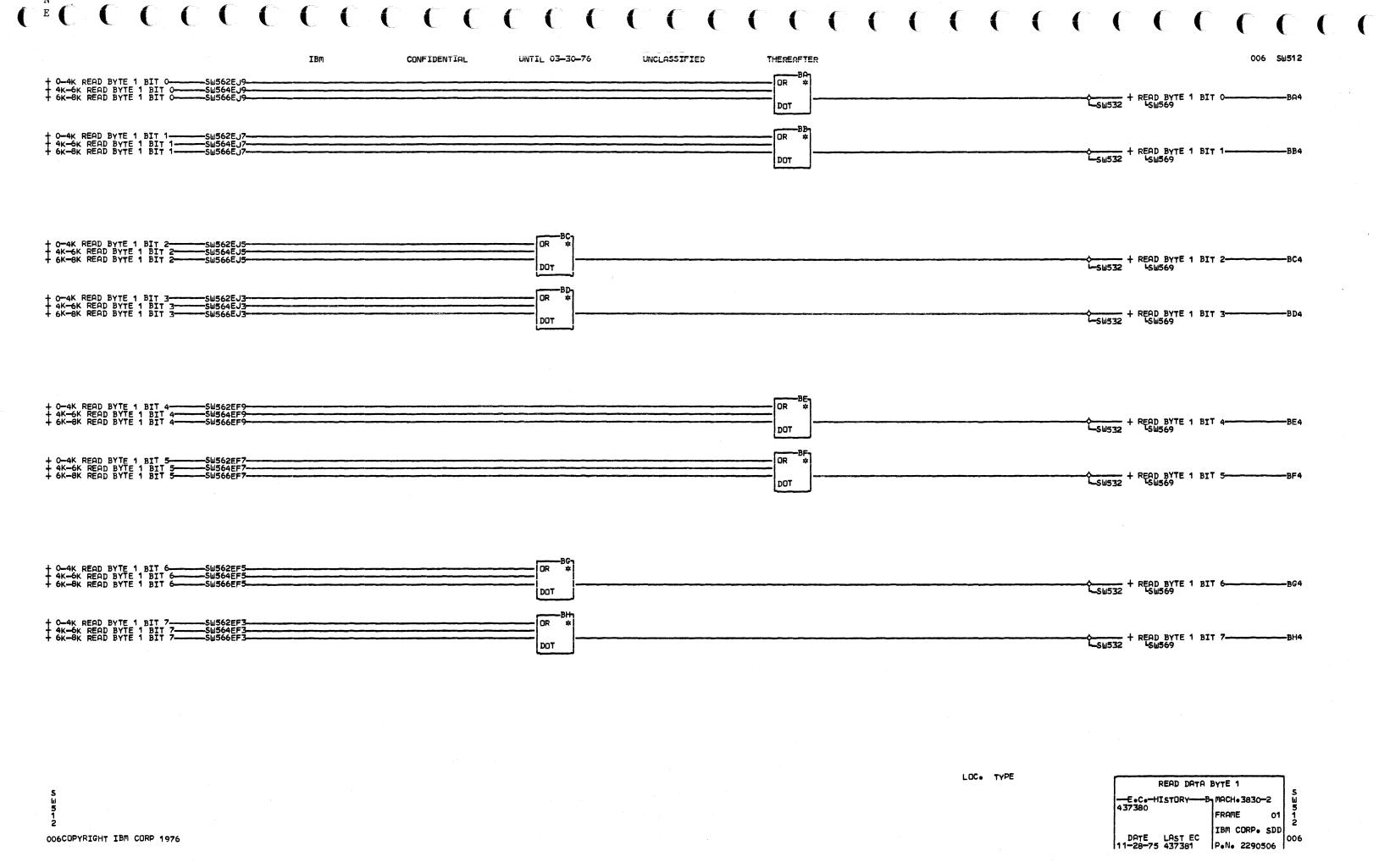
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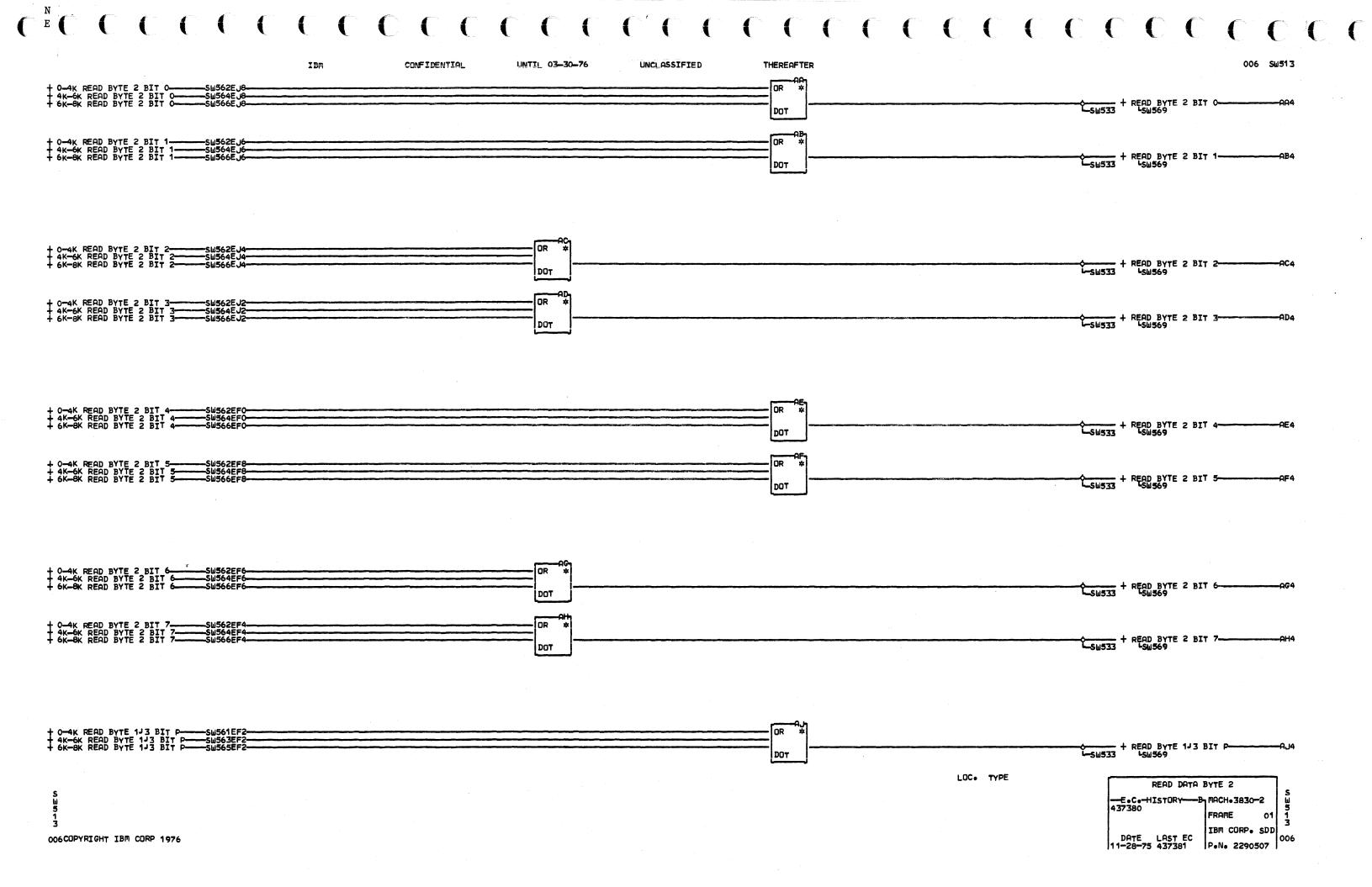
SNIPE CONTR	OL CARD	٦.
E.CHISTORYB7 MACH.3830-2		
437360	FRAME	S W 5
DATE LAST EC	IBM CORP. S	DD 2
	P.N. 229045	2 005

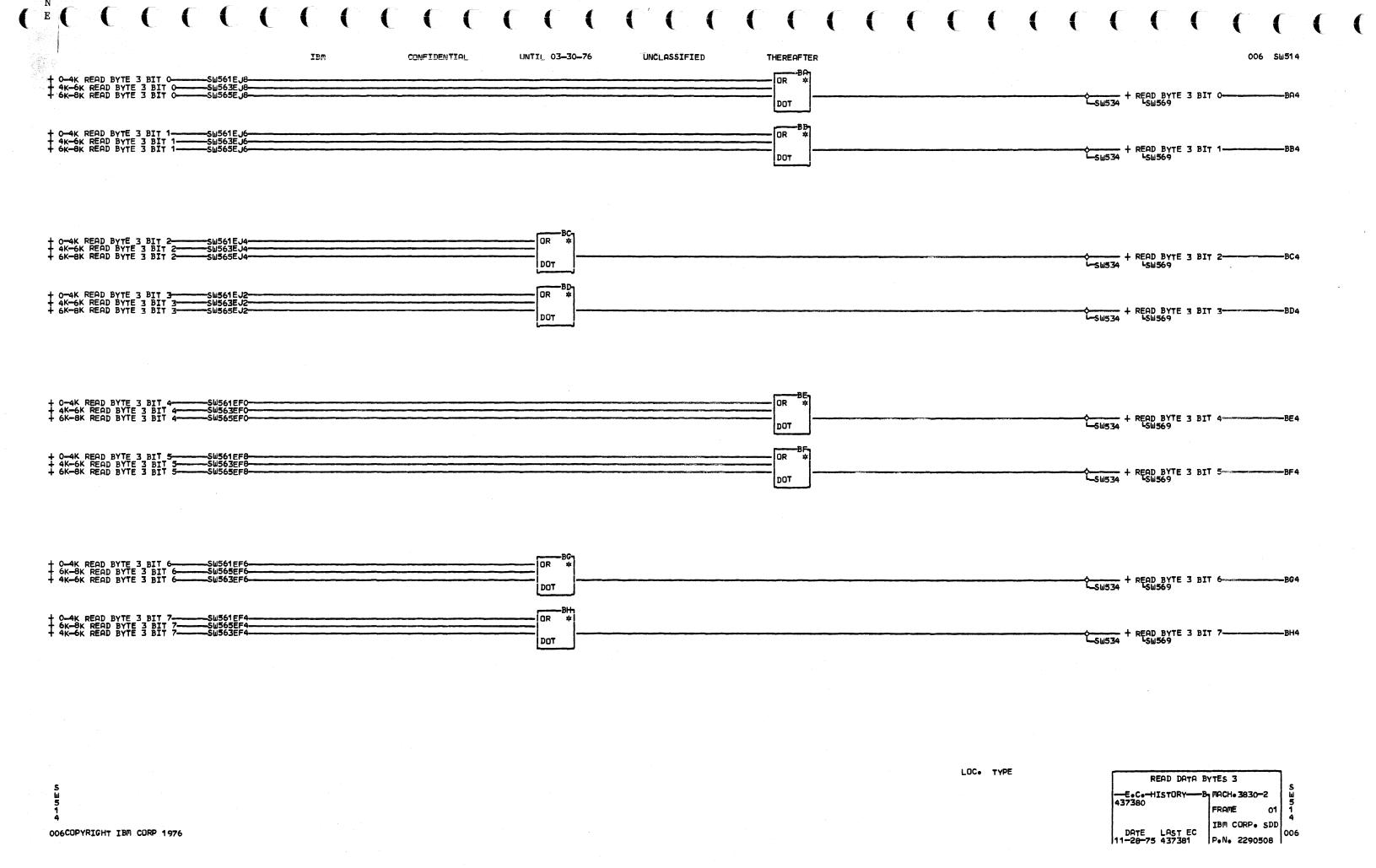


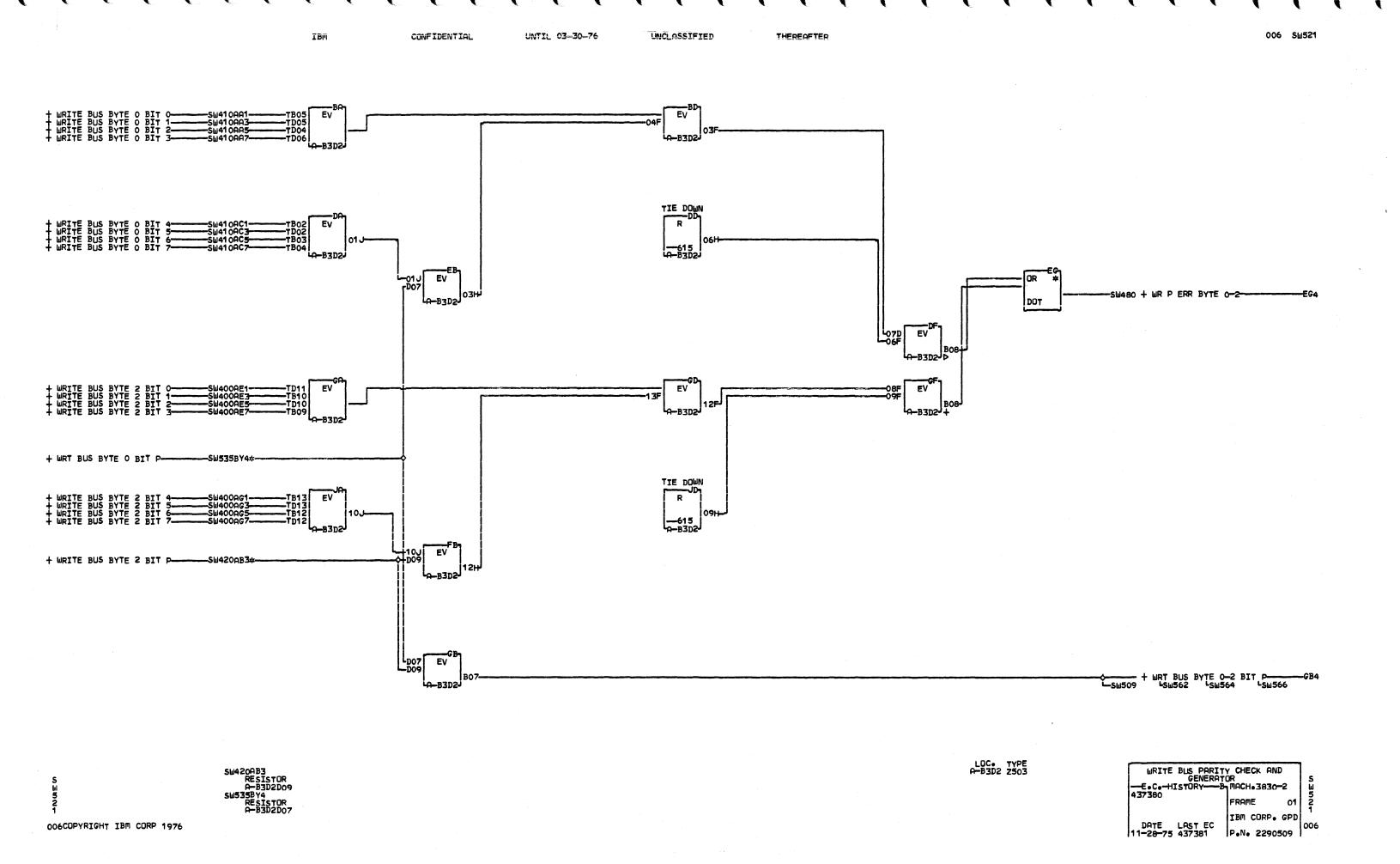


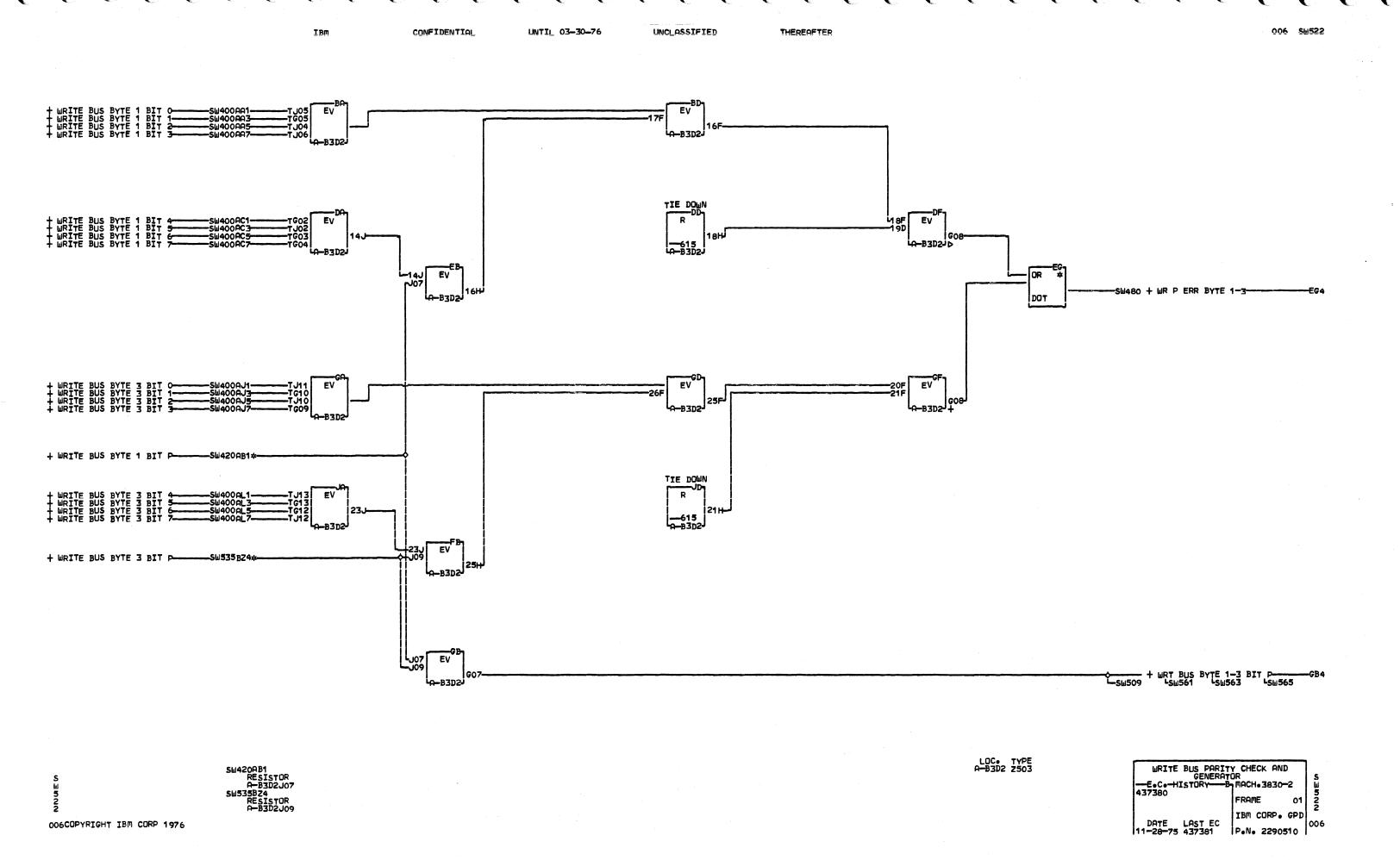


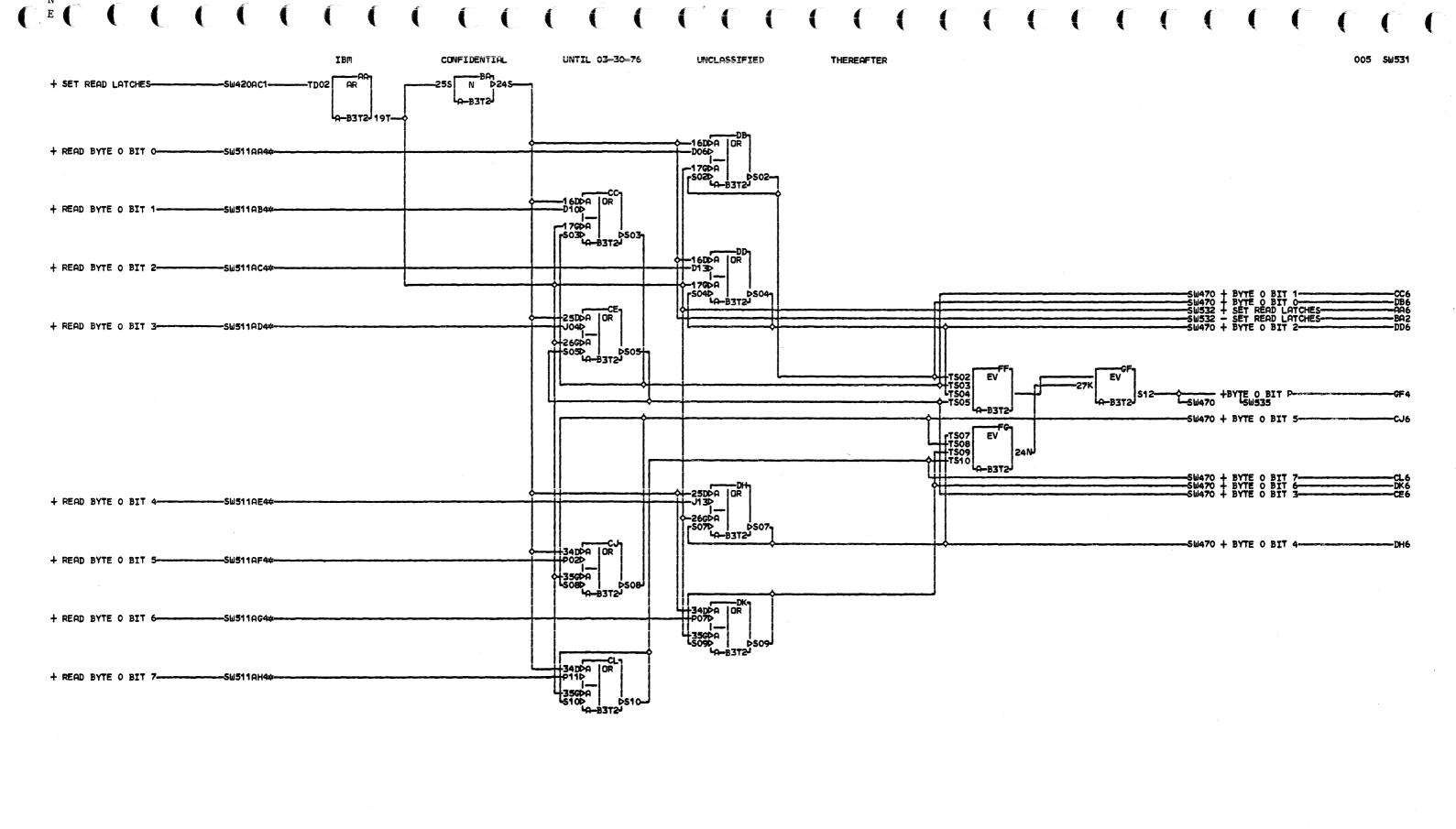


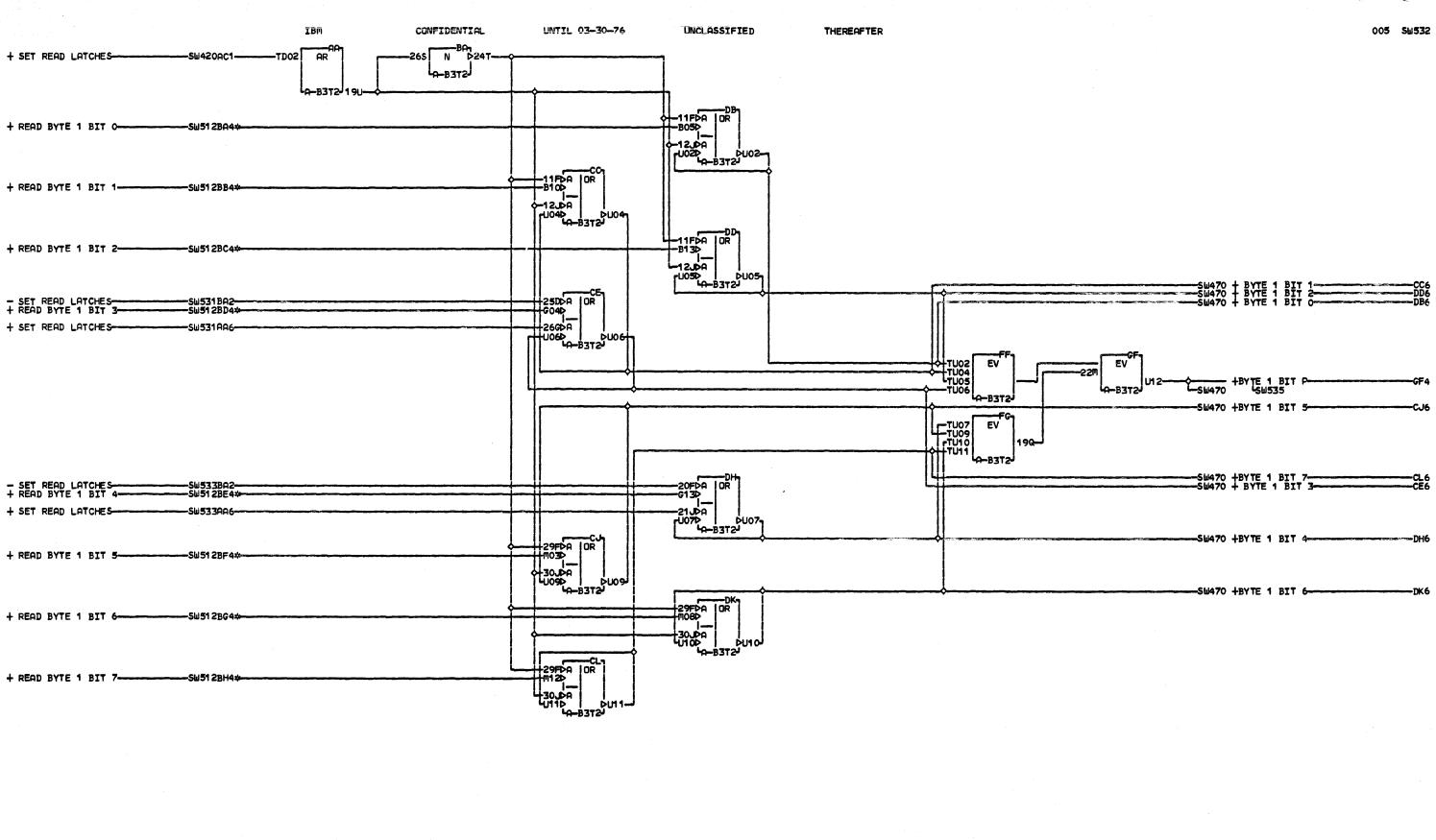








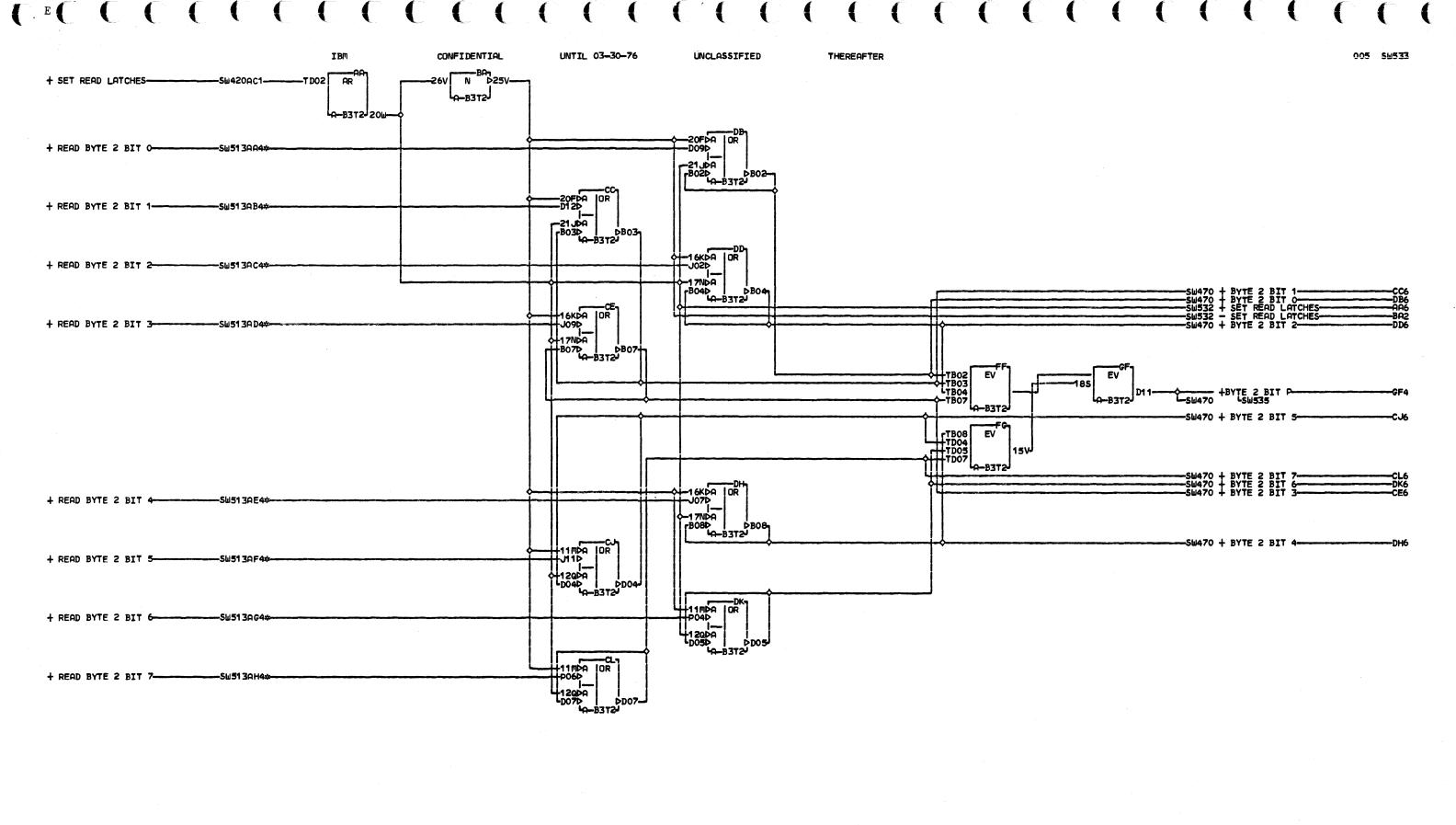




S SW51 2BA4 RESISTOR A-B3T2G13 A-B3T2MO8
RESISTOR A-B3T2B13 A-B3T2G13 SW51 2BH4
A-B3T2B05 A-B3T2B13 SW51 2BF4 RESISTOR
A-B3T2B05 SW51 2BD4 RESISTOR A-B3T2M03
RESISTOR A-B3T2G04 A-B3T2M08
RESISTOR A-B3T2G04 A-B3T2M08
RESISTOR A-B3T2G04 RESISTOR RESISTOR RESISTOR A-B3T2M08

2 Z502 —E • 4373

BYTE 1 READ LATO READ LATO A37380	CHEC	5 <u>d</u>
437380	FRAME 01	5 3
DATE LAST EC 11-28-75 437381	IBM CORP. GPD	55
11-28-75 437381	P.N. 2290461	



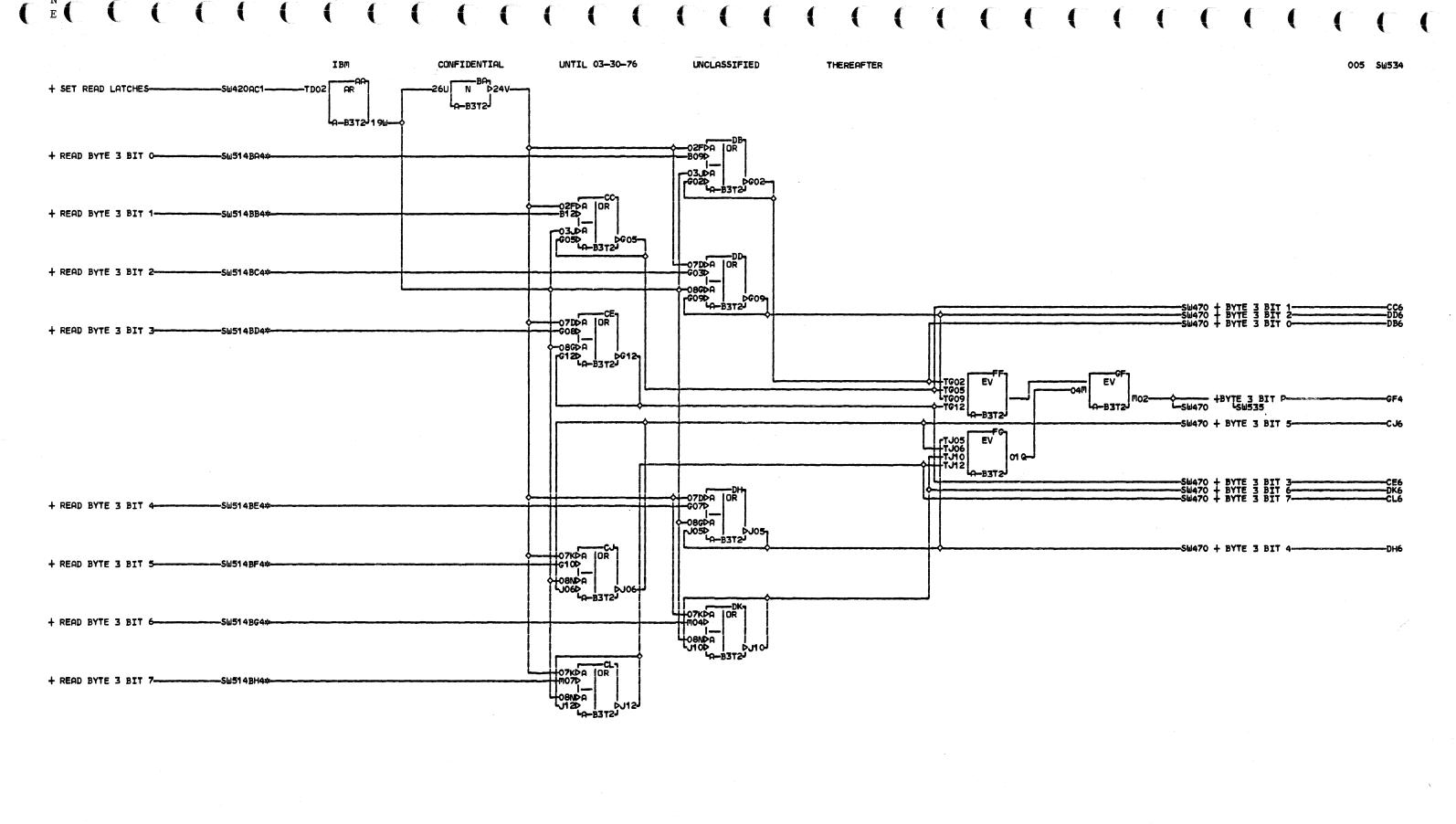
S SW513AA4 RESISTOR A-B3T2JO7 A-B3T2PO4
RESISTOR A-B3T2JO2 A-B3T2JO7 SW513AH4
A-B3T2D09 A-B3T2JO2 SW513AF4 RESISTOR
A-B3T2D09 SW513AD4 RESISTOR A-B3T2JO1
RESISTOR A-B3T2JO2 A-B3T2JO1
RESISTOR A-B3T2JO1
A-B3T2D12 A-B3T2JO9 A-B3T2JO1
A-B3T2D12 A-B3T2JO9 A-B3T2JO1
A-B3T2D12 A-B3T2JO9 A-B3T2JO1
A-B3T2D12 SW513AG4
RESISTOR A-B3T2JO1
RESISTOR A-B3T2JO1
RESISTOR A-B3T2JO2
A-B3T2D12 SW513AG4
RESISTOR A-B3T2PO4

LOC. TYPE
A-B3T2 Z502

BYTE 2
READ LATCHES
--E.C.-HISTORY--B MACH.3830-2
437380

PATE LAST EC
11-28-75 437381

BYTE 2
READ LATCHES
5
5
1
1BM CORP. GPD
005



LOC. TYPE
A-B3T2 Z502

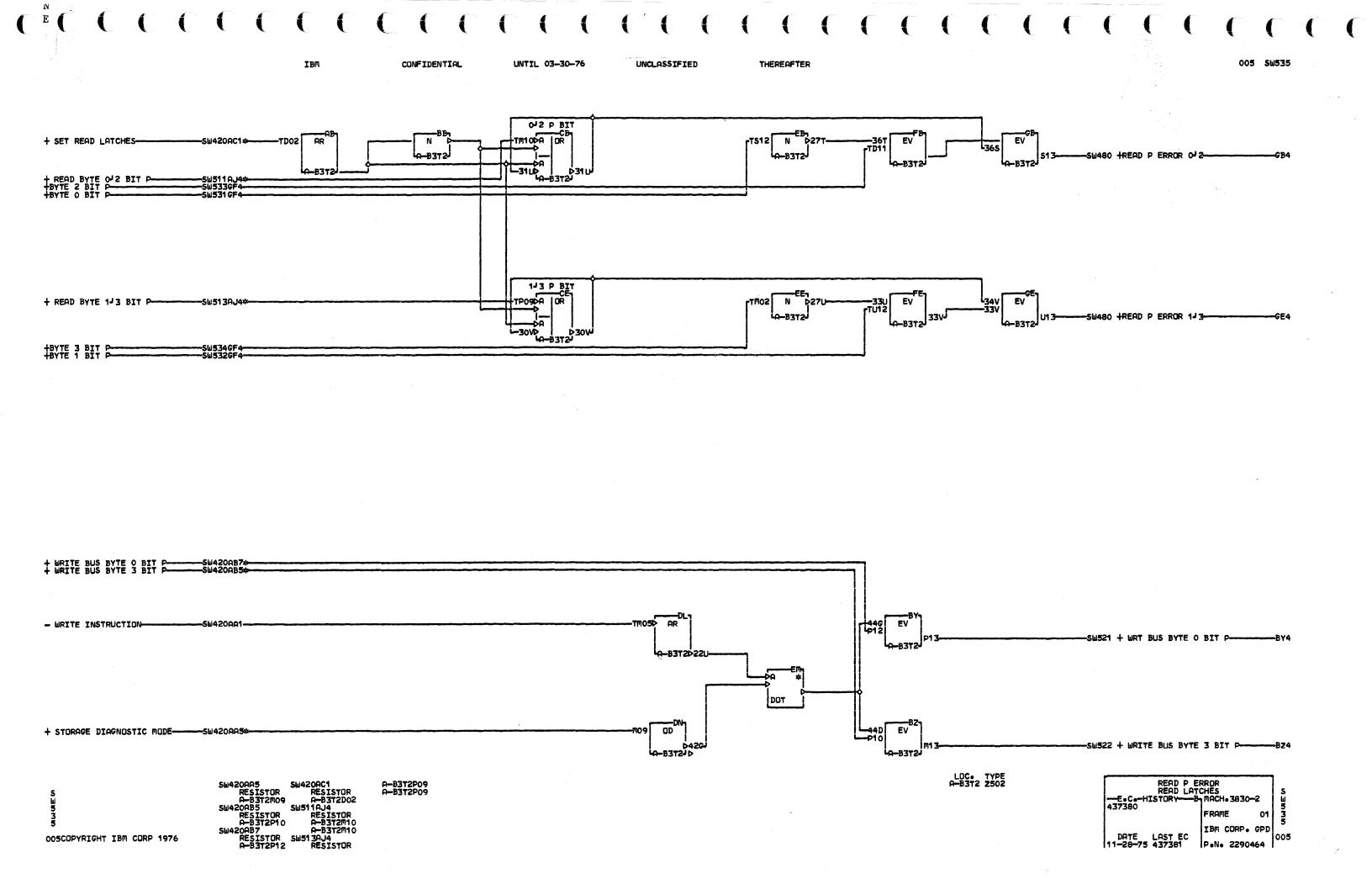
READ LATCHES
--E.C.-HISTORY---B, MACH. 3830-2
437380

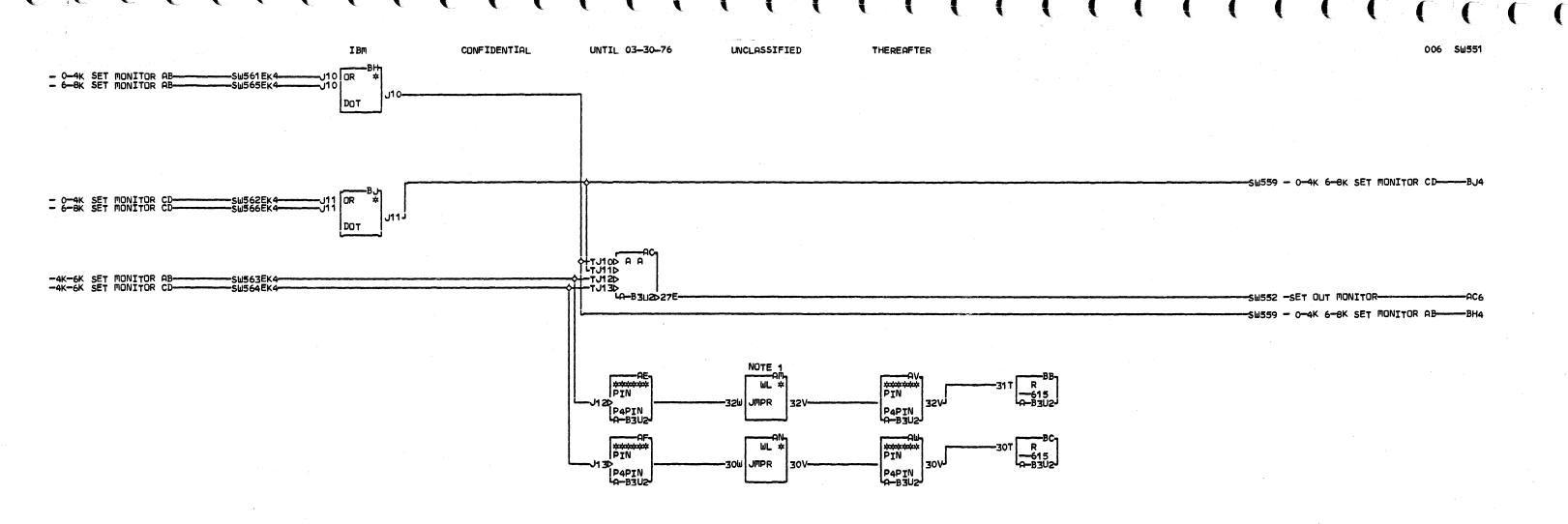
FRAME
O1

IBM CORP. GPD

DATE LAST EC
11-28-75 437381

P.N. 2290463





NOTE 1 FOR 4K ONLY. JUMPER PIN A TO S PIN B 324-32V AND PIN C TO W PIN D 304-30V.

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LOC. TYPE

BSM ADDRESS CHECK

-E.C.-HISTORY-B MACH.3830-2
437380
FRAME 01
1BM CORP. SDD

DATE LAST EC
11-28-75 437381
P.N. 2290511

IBM CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED THEREAFTER 006 SW552 + IMPL LATCH -SW420AD1-TD13D N -SW553 + WRITE CYCLE PWRD--SW553 - WRITE CYCLE PWRD----LA-B3U2D45K - WRITE INSTRUCTION--SW420AA1-AR AR -SW553 + ADDRESS CHECK STROBE 2--SW553 + CU CLK STOPPED B PWRD-LA-83U2D03H LA-B3U2D37F 25VD OR 26U— ---BB + CLK STOPPED B--SW420AC7-AR 036E 26SDA 27SD 27TD ia--B3U2j36Dj -SW553 + ADDRESS CHECK STROBE 3-BB6 LA-B3U2D370 AR -24SDA -SET OUT MONITOR-**-**S₩551 AC6-LA-B3U2D38J -SW553 + ADDRESS CHECK STROBE 4--SW553 + ADDRESS CHECK STROBE 1---SW553 + SET MONITOR CHECK----P06DAR | OR -28FDA | 129FD -30 MDAR OR 31P--31 00A -P0 90 -30P0 D31 M - GO TO STORAGE--SW410AL7-لـ286 10-B3U2 0287 - BC CLUCK T020-T060--SW420AA7-AR LA-B3U2035F 1000A | OR | 31H -SW553 -GATE ICS2---33DD N -SW553 -GATE 0-8K-+ MACHINE RESET----SW420AA3-- ADDRESS BIT O--SW41 0AJ9-- ADDRESS BIT 2--SW410AJ7-ىلB3U2D37 -SW553 - ADDRESS BUS BIT 2---

> LOC. TYPE A-B3U2 Z505

ADDRESS CHECK CONTROL LOGIC
-E.C.-HISTORY-B7 MACH.3830-2
437380

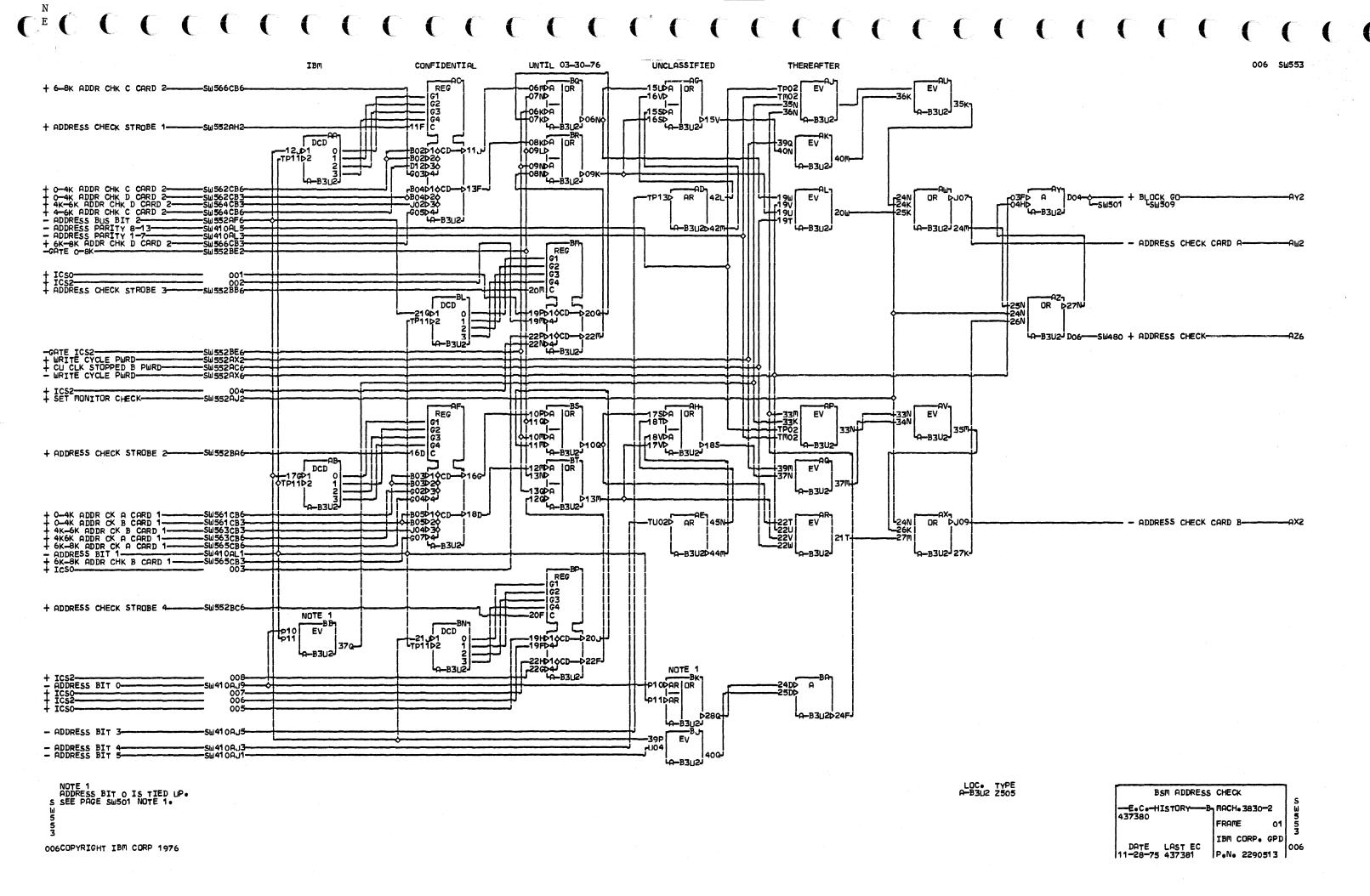
DATE LAST EC

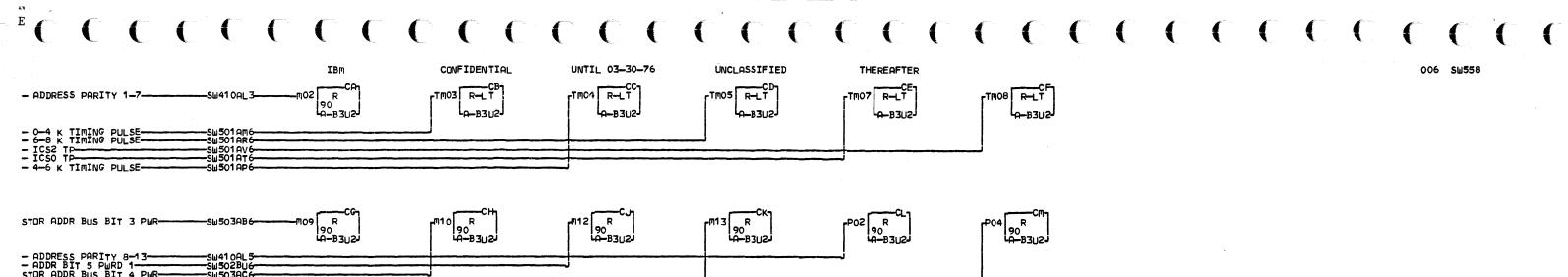
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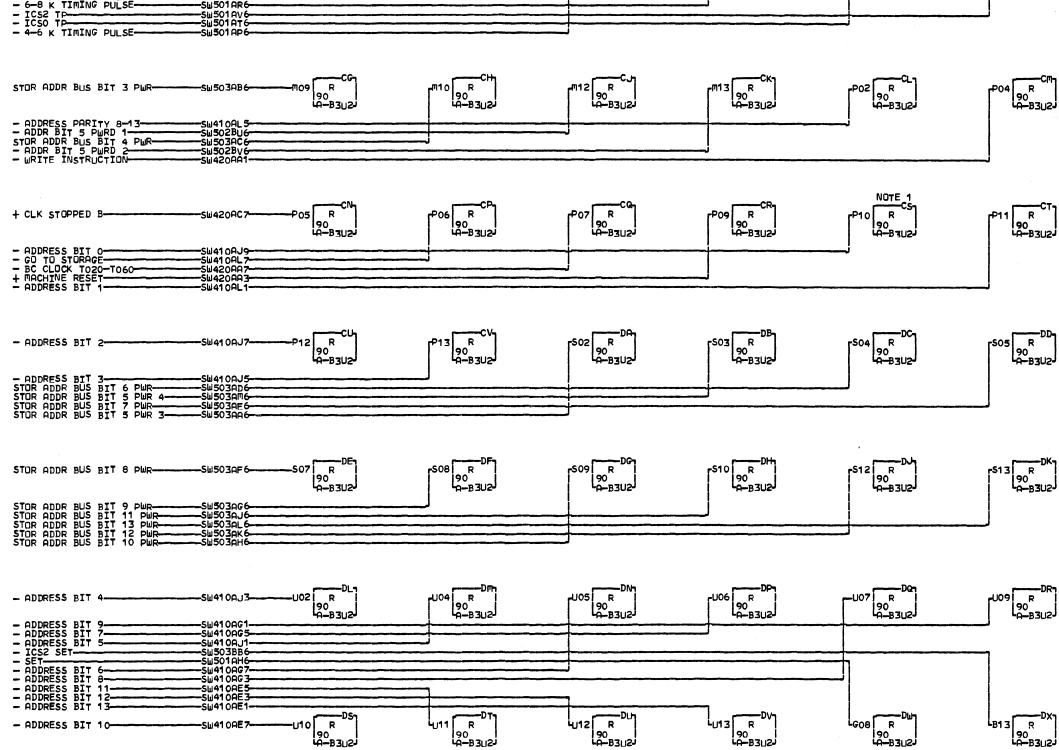
P-N- 2290512

NOTE 1 ADDRESS BIT O IS TIED UP. S SEE PAGE SW501 NOTE 1.

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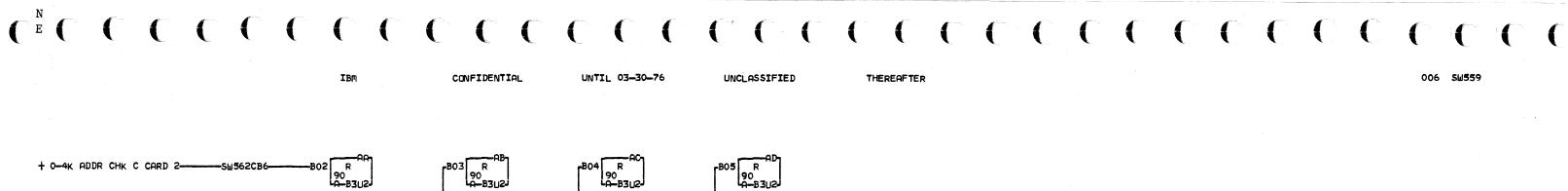


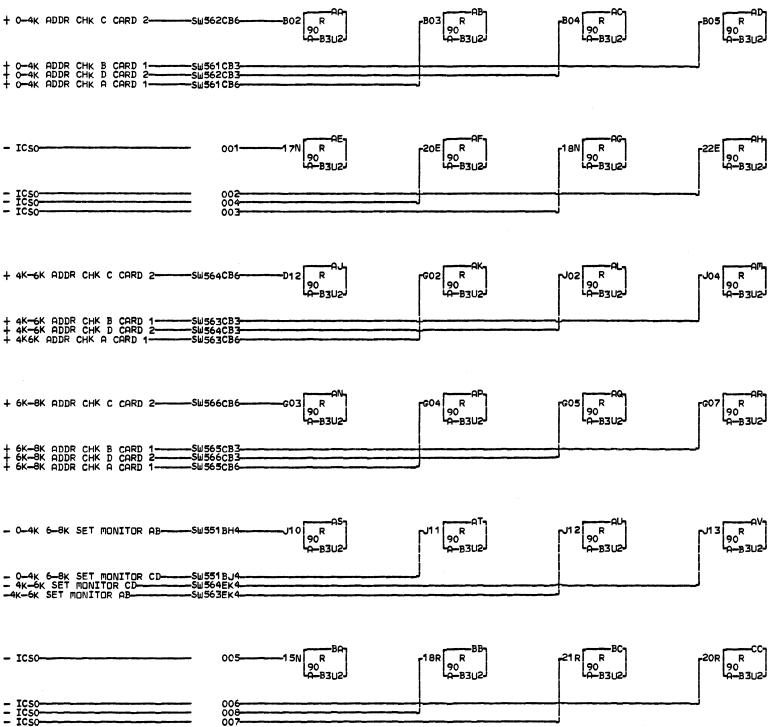
NOTE 1 ADDRESS BIT 0 IS TIED UP. S SEE PAGE SW501 NOTE 1. 5 5

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LOC. TYPE A-B3U2 Z505

TERMINATOR		
E.CHISTORYB MACH.3830-2		
437380	FRAME 01	E 55
DATE LAST EC 11-28-75 437381	IBM CORP. GPD P.N. 2290514	8
	P.N. 2290514	006



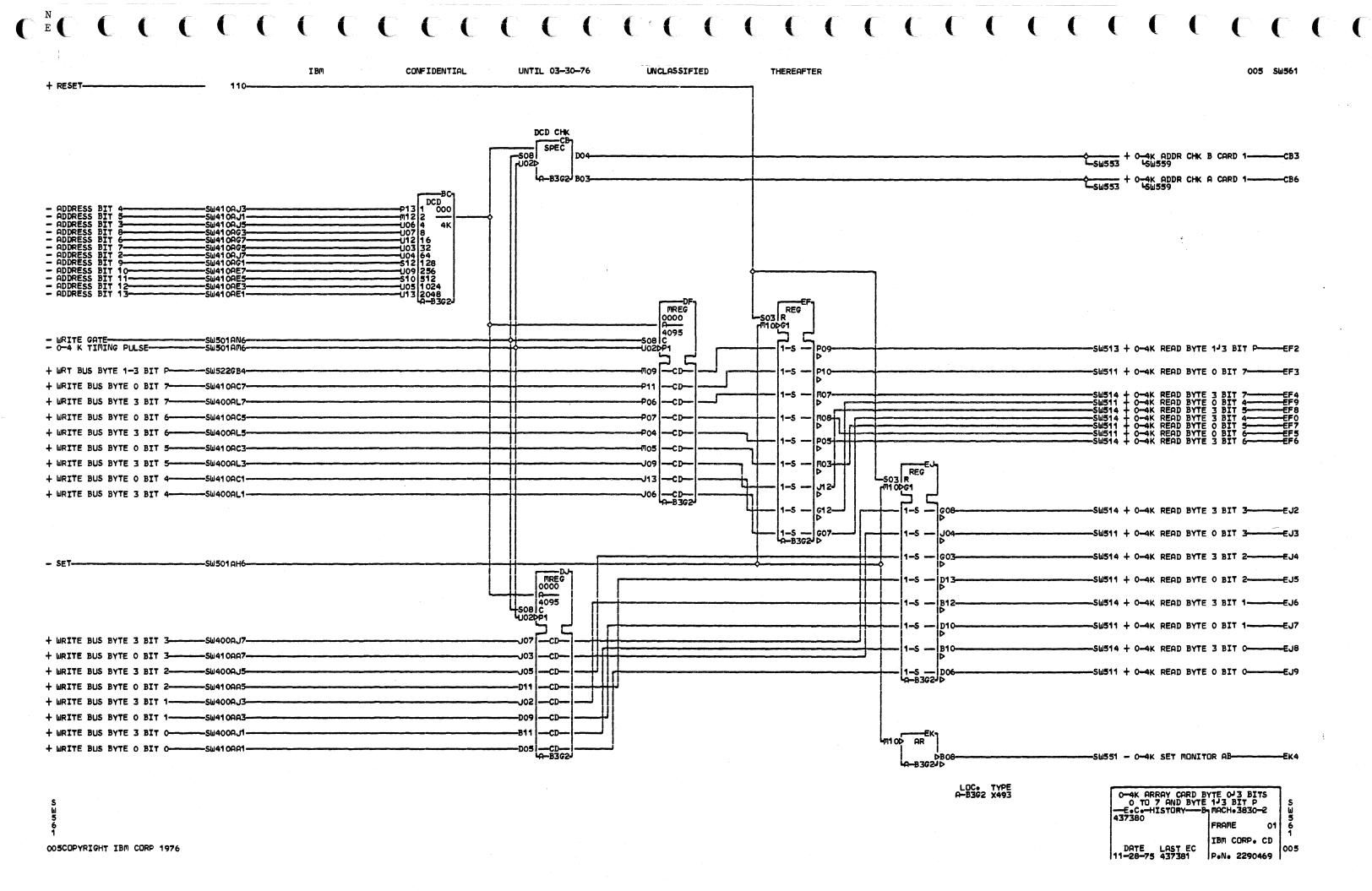


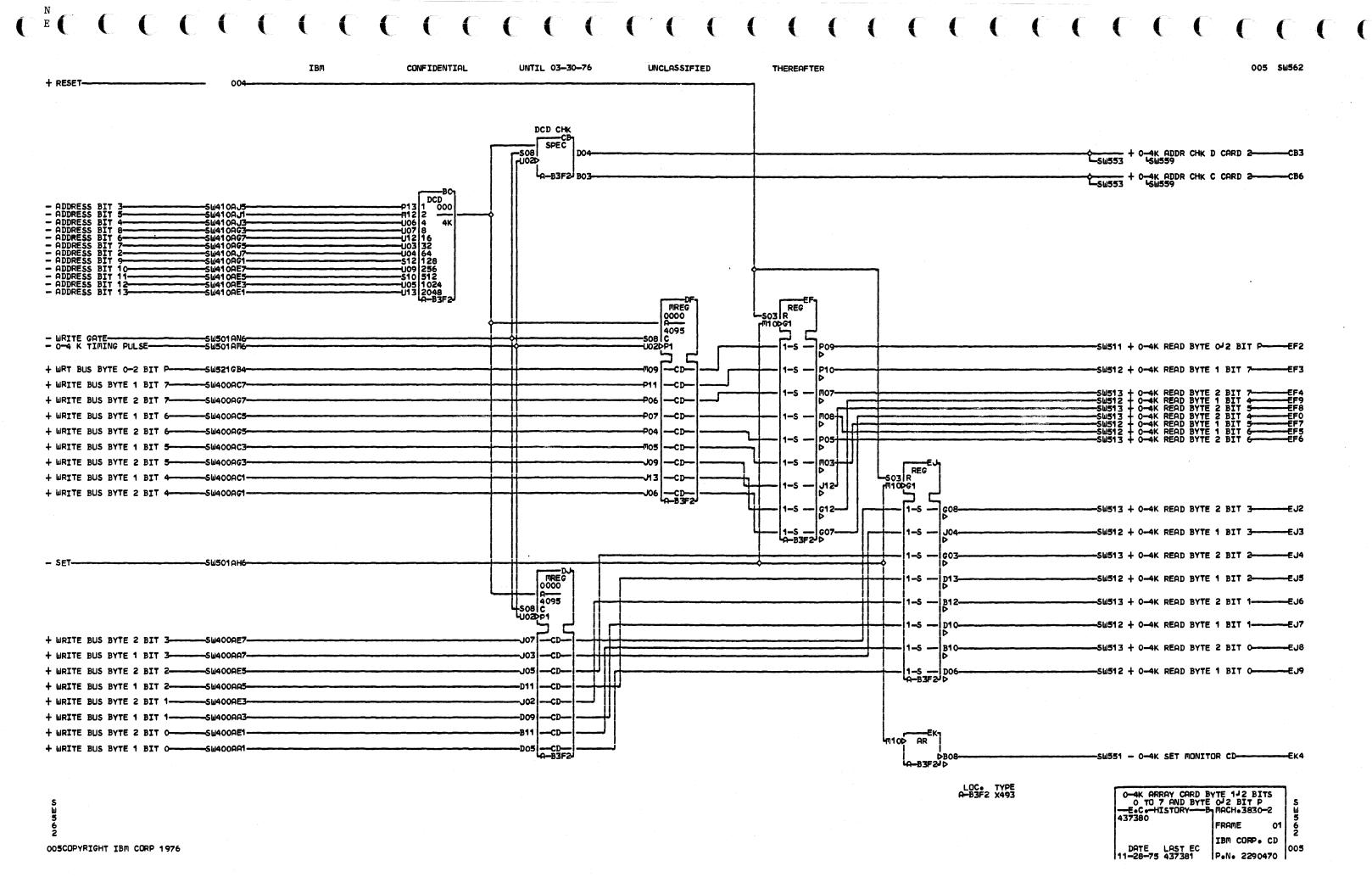
LOC+ TYPE A-B3U2 Z505

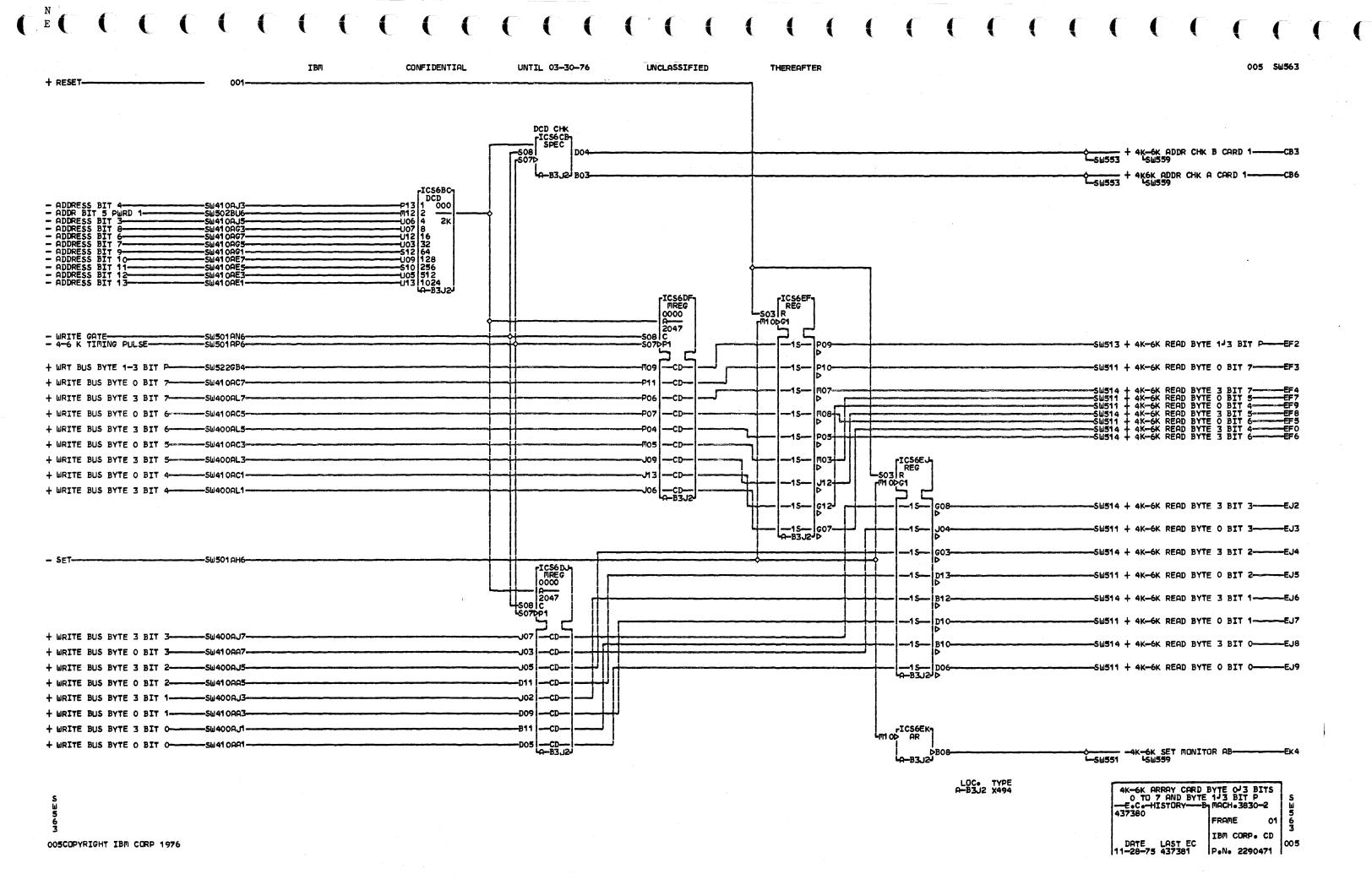
BSM ADDRESS CHECK

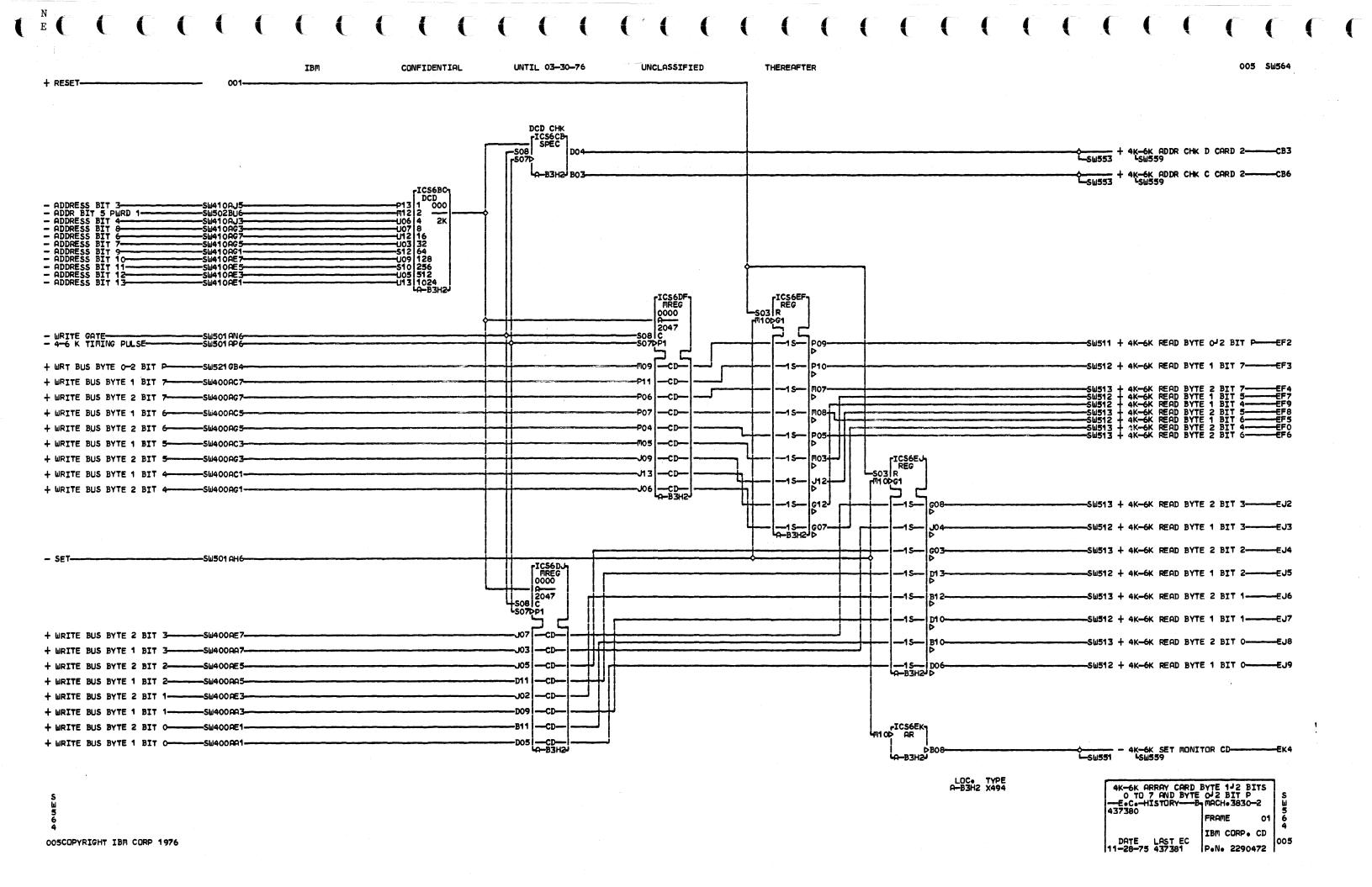
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437380 FRAME 01
5
9
DATE LAST EC 11-28-75 437381 P.N. 2290515

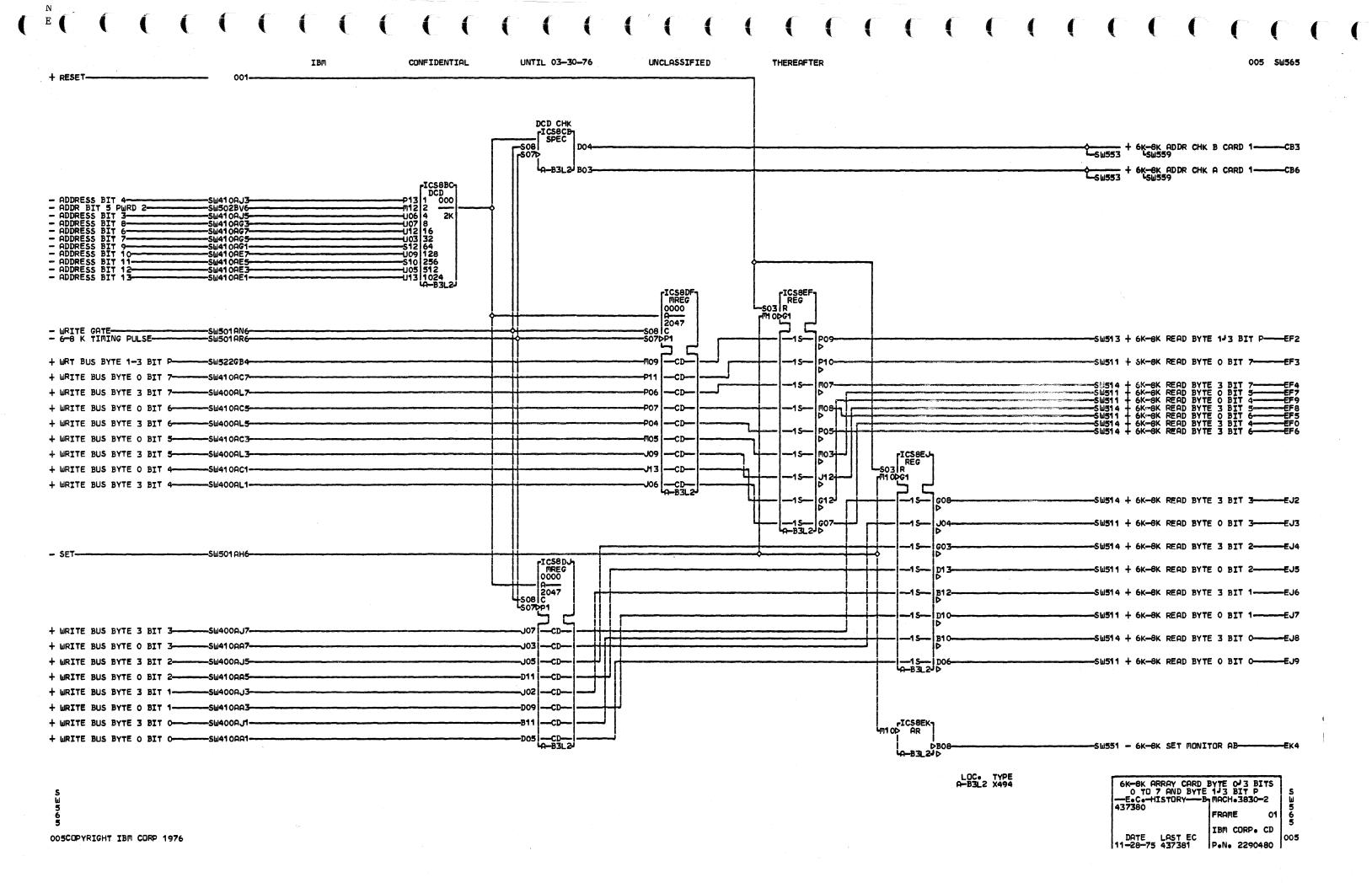
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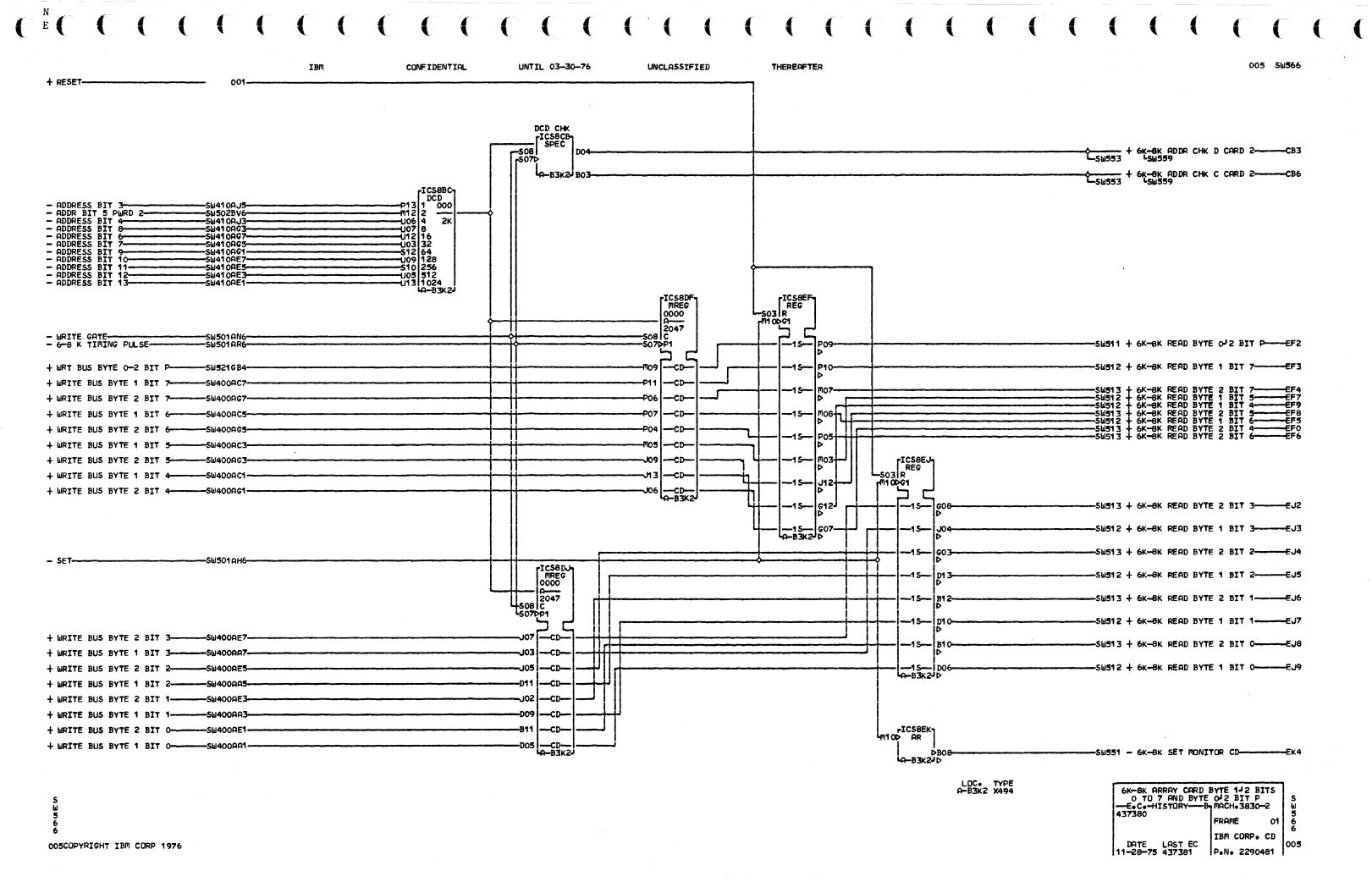


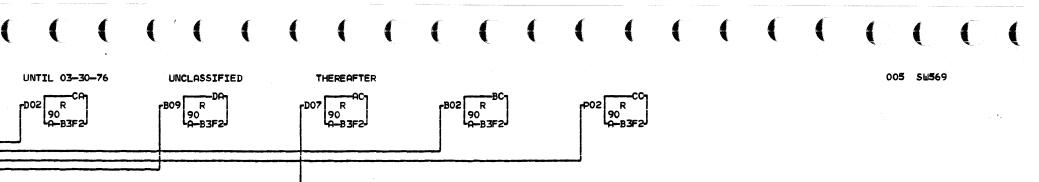


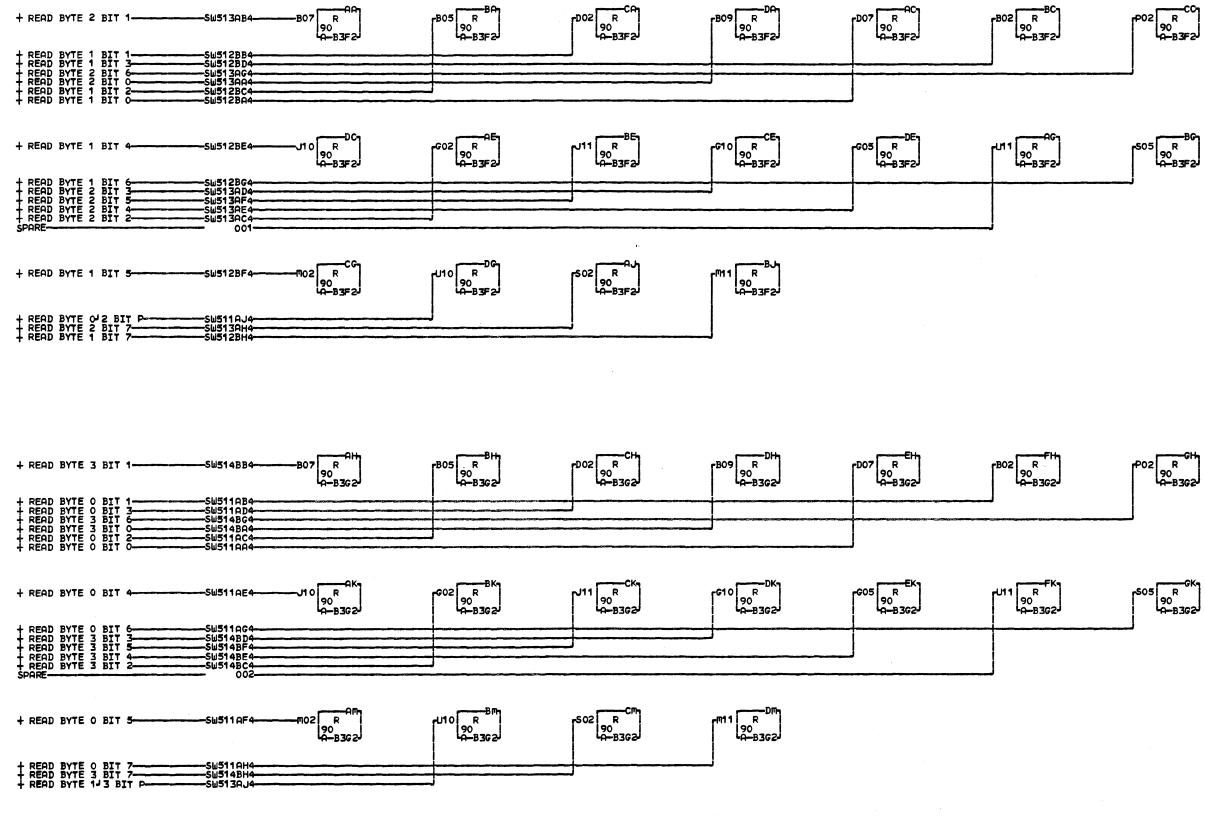












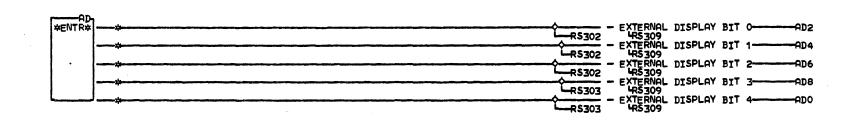
IBM

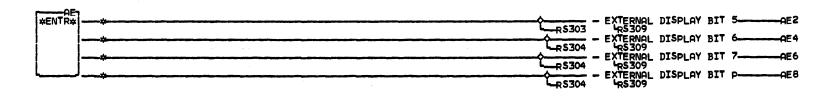
CONFIDENTIAL

LOC. TYPE A-B3F2 X493 A-B3G2 X493 W 5 6 9 005COPYRIGHT IBM CORP 1976

THEREAFTER

000 шд046





LOC. TYPE

AD10A-B2A; AD2 A-B2A; AD4 A-B2A; AD6 A-B2A; AD8 A-B2A; AE2 A-B2A; AE4 A-B2A; AE6 A-B2A; AE8 A-B2A;

000 шд070 THEREAFTER \*\* ENTER\* FROM CTLR WC001 + 3V CTL-I BUS IN BIT 0---GRAGO9
+ 3V CTL-I BUS IN BIT 1---GRAGO9 GA601 GA601 + 3V CTL-I BUS IN BIT 2-GR601 ENTER\* FROM CTLR WC001 + 3V CTL-I BUS IN BIT 3--AB2 GA601 + 3V CTL-I BUS IN BIT 4-GA609 + 3V CTL-I BUS IN BIT 5--AB4 -GA601 €GA601 ENTER\* FROM CTLR WCOO1 + 3V CTL-I BUS IN BIT 6-GA701 + 3V CTL-I BUS IN BIT 7-LGA709 + 3V CTL-I BUS IN BIT P-LGA709 -GA701 €-GA701 ENTER\* FROM CTLR WCOO1 C-GA701 GA701 L-GA701 ENTER\* FROM CTLR WC001 ENTERX FROM CTLR - + 3V CTL-I SELECTED ALERT 2---AF2 €-GA501 WC001 - + 3V CTL-I UNSELECTED ALERT 1-AF6 L-GA401 #ENTR\* FROM CTLR UCOO2 - + 3V UNSELECTED ALERT 2 UNUSED-AG2 - SPARE-SPARE-#ENTR# FROM CTLR WC002 SPARE + 3V SELECTED ALERT 3 UNUSED-AK4 - CE 2 ALERT UNUSED-#ENTR# FROM CTLR WC002 SPARE

AR2 D-P1\_J04 1 01A-B1A4D09 AD2 D-P1\_J13 1 01A-B1A3D06 AG6 D-P2\_J11 1 01A-B1A4D04 AB6 D-P1G-G10 1 01A-B1A4D13 AE6 D-P2G-G08 AK2 D-P1\_J02 AP4 D-P1G-G05 1 01A-B1A4D10 AD4 D-P2G-G03 1 01A-B1A3D07 AK4 D-P2G-G10 1 01A-B1A4D05 AC2 D-P1\_J11 1 01A-B1A3D03 AF2 D-P2\_J09 AK6 D-P1\_G-G06 AP6 D-P1\_J06 1 01A-B1A4D11 AD6 D-P2\_J04 1 01A-B1A3D09 AL2 D-P2\_J02 1 01A-B1A4D06 AC4 D-P1G-G12 1 01A-B1A3D04 AF6 D-P2G-G12 AB2 D-P1G-G08 1 01A-B1A4D12 AE2 D-P2G-G05 1 01A-B1A3D12 1 01A-B1A4D07 AC6 D-P1G-G03 1 01A-B1A3D05 AG2 D-P2\_J13 AB4 D-P1\_J09 1 01A-B1A4D03 AE4 D-P2\_J06 AG4 D-P2G-G06

LOC. TYPE

THEREAFTER \*\*

000 ШД071

SPARE	
+ 3V CTL-I TAG BUS BIT 3GA402BL4*	CTLR WAGOO
+3V CTL-I CE COMMUNICATION DUT-GA402FK4*	#EXI
+ 3V CTL-I RESPONSEGA502CJ4*	the state of the s
+ 3V CTL-I TAG GATEGA502EH4*	CTLR WAROO
+ 3V CTL-I SELECT HOLDGA502BG4*-	TOXI
+ 3V CTL-I TAG BUS BIT P-GA602EM4*	
+ 3V CTL-I TAG BUS BIT 7-GA602BL4#	CTLR WAOO
+ 3V CTL-I TAG BUS BIT 6-GA602FK4*	#EXI
+ 3V CTL-I TAG BUS BIT 5	CTLR WAOO
+ 3V CTL-I TAG BUS BIT 4 GA602EH4*	*EXI
+ 3V CTL-I TAG BUS BIT 0	
SPARE————————————————————————————————————	[myoo.
+ 3v CTL-I RECYCLEGA502BL4*	
+ 3v CTL-I SYNC-Out	*EXI
+ 3v CTL-I BUS OUT BIT PGA402CJ4*	<u></u>
+ 3v CTL-I Bus Out BIT 7-GA402EH4*	CTLR
+ 3v CTL-I BUS OUT BIT 6-GA402BG4*	*ExI
+ 3v CTL-I BUS OUT BIT 5-GA702EM4*	
+ 3v CTL-I BUS OUT BIT 4GA702BL4*	ČŤLR WAGO
+ 3v CTL-I BUS OUT BIT 3GA702FK4*	*EXI
+ 3V CTL-I BUS OUT BIT 2GA702CJ4*	Maoo.
+ 3V CTL-I BUS OUT BIT 1	TO CTLR
+ 3V CTL-I BUS DUT BIT 0GA702BG4*	*EXI

LOC. TYPE

CTL-I EXIT TO CONTRO -E.CHISTORY-B 437301 437305 437302 437306 437303 437307 437304 DATE LAST EC 06-15-73 437310	LLER	90 O O O

IBM

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UNTIL 08-01-73

UNCLASSIFIED THEREAFTER

002 WA076 -KA105 - 3 METER SW A ENABLED--KB105 - 3 METER SW B ENABLED-

\*ENTR\* -GK701 - 3 MULTI TAG SW DN-LYB131 J

+ 3 DC READY LA101 LA109 KC105 - 3 METER SW C ENABLED \*ENTR\* FROM PWRSEQ -KD105 - 3 METER SW D ENABLED-AD5 LYB131

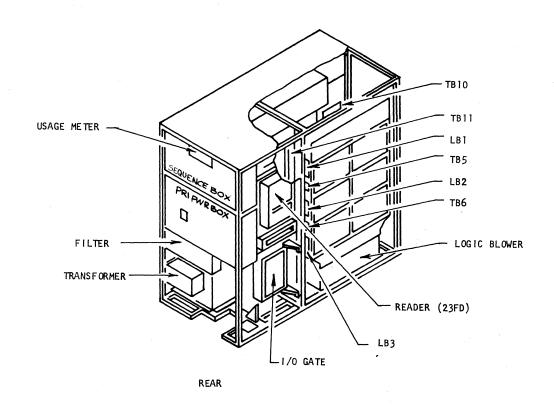
#EXIT# TO PWRSEQ - PICK METER RELAY A+B+C+D-AR101HB4-LA102AX2#-+ 3V MPL POWER ON-LYB149 J

LOC. TYPE

POWER SEQ ENTER AND EXIT
-E.C.-HISTORY-B-MACH.3830-2
437357
437310 FRAME 0 2 A 0 0 7 IBM CORP. SDD PeNe 2346284 002 DATE LAST EC 104-16-73 437311

002 SIM TO PN

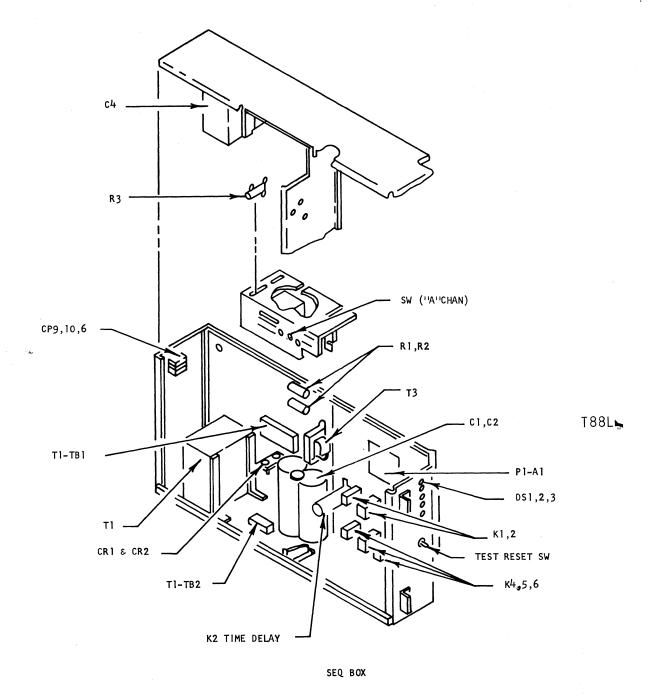
LA102AX2
B 01A-B1V2D0B
1 01A-B1V5D09 B 01A-A1R1A11
AA1 A-B1V5D12 AD1 A-B1V5D10
B 01A-B1V2D09 AD3 A-A1K1B11
B 01A-A1R1B11 AD5 A-A1K1A11
AA3 A-B1V5D13
B 01A-B1V5D13
B 01A-A1R1C11
EC 437310 AA5 A-B1V5D11

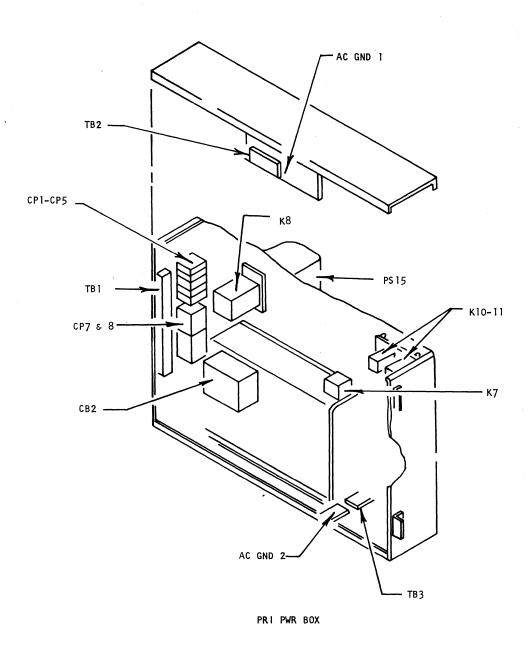


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UNTIL OCT 1972, UNCLASSIFIED THEREAFTER

		EC HISTORY		DRAWING TITLE		
		20DEC71	437502	COMPONENT LOCATION		
		25JUL72	437529	MACH 3830-2		
		·		PART NO 2199850		
ſ	С			CLASSIFICATION		
L	C		-	<b>LDIV</b> CORP		

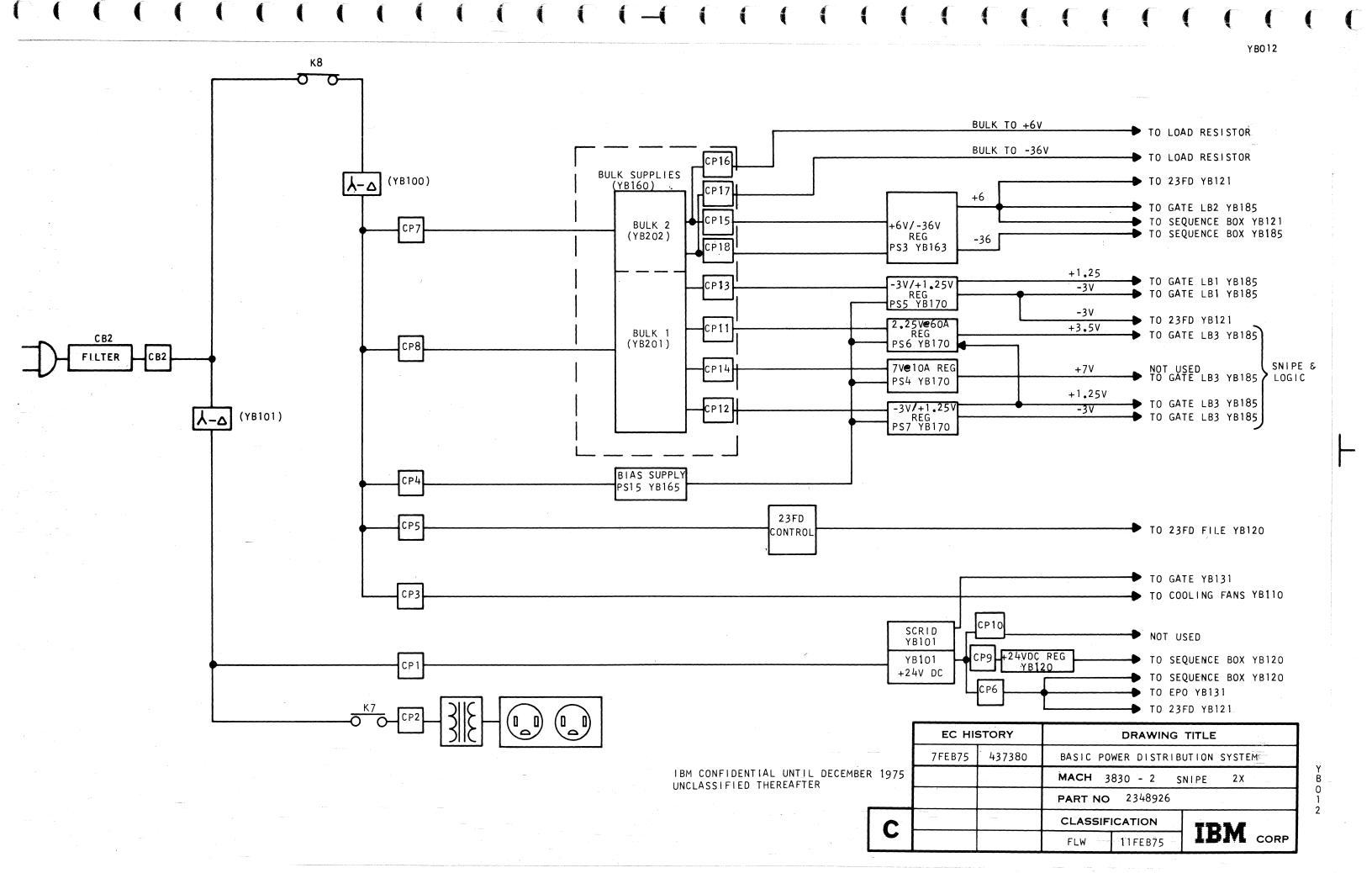
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UNTIL OCT 1972, UNCLASSIFIED THEREAFTER

		EC HIS	STORY	DRAWING TITLE			
		20DEC71	4 <b>37</b> 50 <b>2</b>	COMPONENT	LOCATION		
		6MAR 72	437517	MACH	3830-8	2	
		25 JUL72	437529	PART NO	2199851		,
	C			CLASSIFIC	CATION	TD1/	
	C					TDIM	CORP



### CONTACTORS

K	COIL	1	2	3	4		
K1	YB120	YB120	YB120				
K2	YB120	YB120	YB120	YB120			
KBA	YBIZI	<b>YBISI</b>	YBIZI	YB121			
К4	YB120	YB120	YB120		YB120		
K5	YB120	YB120	YB120	YB120			
к6	YB120	YB121	YB121	YB121			
К7	YB120	YB101	YB101				
к8	YB120	YB100	YB100	YB100			
K10	YB120	YB120		YB120			
KII	YB120	YB120					
K13	YB121	YB121					

# LOCATED IN SEQ BOX)

### MAIN CKT BOARD

1	YB121
2	YB121
3	YB121
4	YB121
5	YB120
6	YB120
7	YB120
8	YB120
9	YB120
10	YB120
11	YB121
12	YB121
13	YB121
14	YB121
15	YB120
16	YB120
17	YB120
18	YB120
19	YB120
20	YB120
21	YB120

MAIN CKT BOARD						
22	YB120					
<b>2</b> 3	YB121					
24	YB121					
<b>2</b> 5	YB121					
26	YB121					
27	YB121					
<b>2</b> 8	YB121					
<b>2</b> 9	YB121					
30	YB121					
31	YB121					
32	YB121					
33	YB121					
34	YB121					

# 23FD TRIAC BOARD

1	YB120
2	YB120
3	<b>YB12</b> 0
4	YB120

LOCATED IN PRIMARY POWER BOX

# CIRCUIT BREAKERS

CB2	YB100

### CONNECTORS

FAN CONN A,B,C,D	YB110
MAIN CKT BD NO.1	YB121
MAIN CKT BD NO.2	YB120
SYSTEM E P 0 1,2,3 €4	YB131
23FD J1 & J2	YB180
SEQ START SI & S2	YB135
SUBSYS EPO 5 & 6	YB135
FILE CONTROL	YB210
PS THERM E	YB110
POWER CONTROL 1 & 2	YB131

# TERMINAL BLOCKS

TB-1	YB100		
TB-2	YB100		
TB-3	YB101		
TB-5	YB185		
тв-6	YB185		
TB-10	YB200		
TB-11	YB163		
BULK 1 TB	YB160		
BULK 2 TB	YB160		
50H <b>Z – TB</b>	YB160		
T1-TB1	YB101		
T1-TB2	YB101		
	· · · · · · · · · · · · · · · · · · ·		

## CIRCUIT PROTECTORS

CP	PAGE	DC	60 HZ	50 HZ
CP1	YB101		6A	6A
CP2	YB101		6A	6A
CP3	YB100		4A	4A
CP4	YB100		1A	1A
CP5	YB100		1A	1A
CP6	YB101	5A		
CP7	YB100		8A	6A
CP8	YB100		15A	10A
CP9	YB101	1A		
CP10	YB101	10A		
CP11	YB 160	60A		
CP12	YB160	70A		
CP13	YB160	70A		
CP14	YB160	12A		
CP15	YB160	8A		
CP16	YB160	45A		
CP17	YB160	16A		
CP18	YB160	1A		

### EDGE CONNECTORS

EC	А	В	С	D	E	F	G	Н
CE EC-1	YB131							
CE EC-2	YB131	YB131	YB131	YB131	YB110	YB110	YB131	SM954
CE EC-3	_	YB131	YB131	YB131	YB131	_	YB131	YB131
CE EC-4	YB131	YB131	_		SM954	YB131	YB131	SM954
CE EG5	YBI3I	YBI3I	YB131	YBI3I	YBIBI	YBIBI	YB131	YBIBI

	EC HISTORY		DRAWING TITLE		
	20DEC71 437502		LOCATION CHART		
	6MAR72 437517		MACH	3830-2	
	25 JUL72	437529	PART NO	2199853	
	25APR73	437552	CLASSIF	ICATION	TD1#
C	·				LDIN CORP

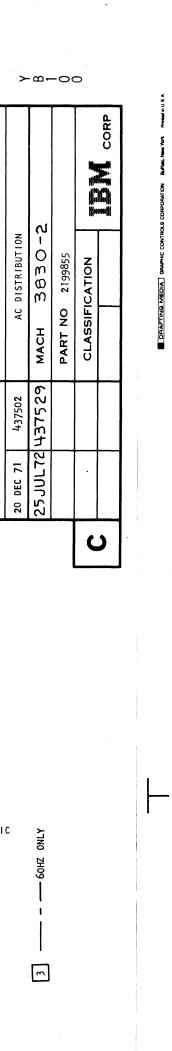
	THEREAFTER
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IBM CONFI	1972,
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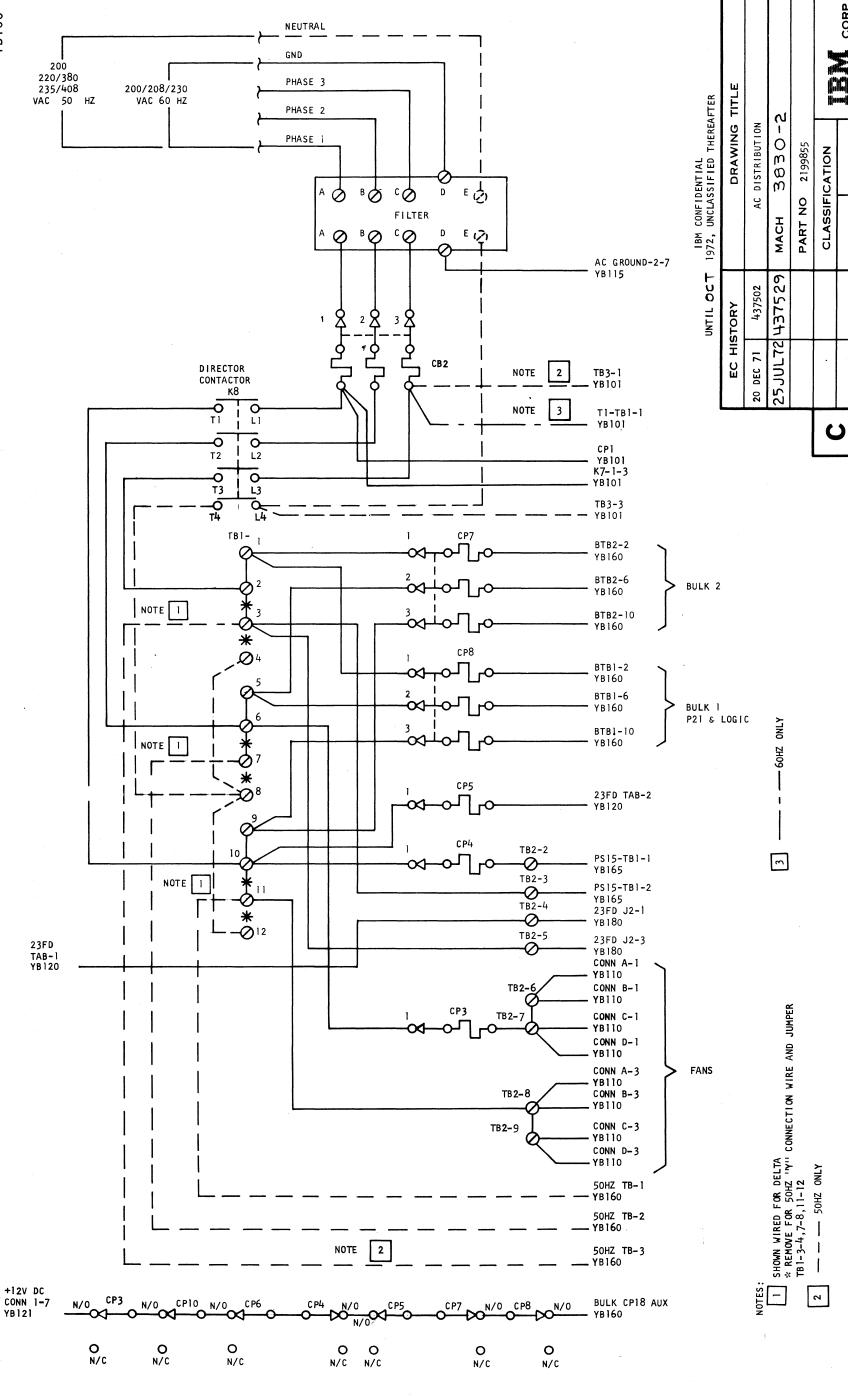
IED THEREAFTER	TITLE			,			
	DRAWING TITLE	WRT 0102010	MACH 3830-2	PART NO 2199854	CLASSIFICATION		
972, UNCLASSI		AC INPUT CHART	l	PART NO	CLASSIF		
UNTIL OÇ⊤ 1972, UNCLASSIFIED THEREAFTER	STORY	437502	25JUL72 437529				
	EC HISTORY	20DEC71 437502	25JUL72				
					Ç	ر	

×9000€

		DELTA INPUT VOLTA		1
REM	OVE	ADD		LOGIC PAGE
TB1-3 TO	TB1-4	TB1-3 TO	TB 1-2	YB100
TB1-7 TO	TB1-8	TB1-7 TO	TB1-6	YB100
тві-11 то	TB1-12	TB1-11 TO	TB1-10	YB100
TB3-2 TO	TB3-3	TB3-2 TO	TB 3- 1	YB101
CONNECT	200V	220V	235V	LOGIC PAGE
CPI	TI-TB1-2	T1-TB1-3	T1-TB1-4	YB101
50HZ TB-3	BTB1-4	BTB1-3	BTB1-1	YB160
50HZ TB-3	BTB2-4	BTB2-3	BTB2-1	YB160
50HZ TB-2	втв1-8	BTB1-7	BTB1-5	YB160
50HZ TB-2	BTB2-8	BTB2-7	BTB2-5	YB160
50HZ TB-1	BTB1-12	BTB1-11	BTB1-9	YB160
50HZ TB-1	BTB2-12	BTB2-11	BTB2-9	YB160
TB 2-3	PS 15 TB1-2	PS 15 TB 1-3	PS15 TB1-4	YB165
		1	1	1

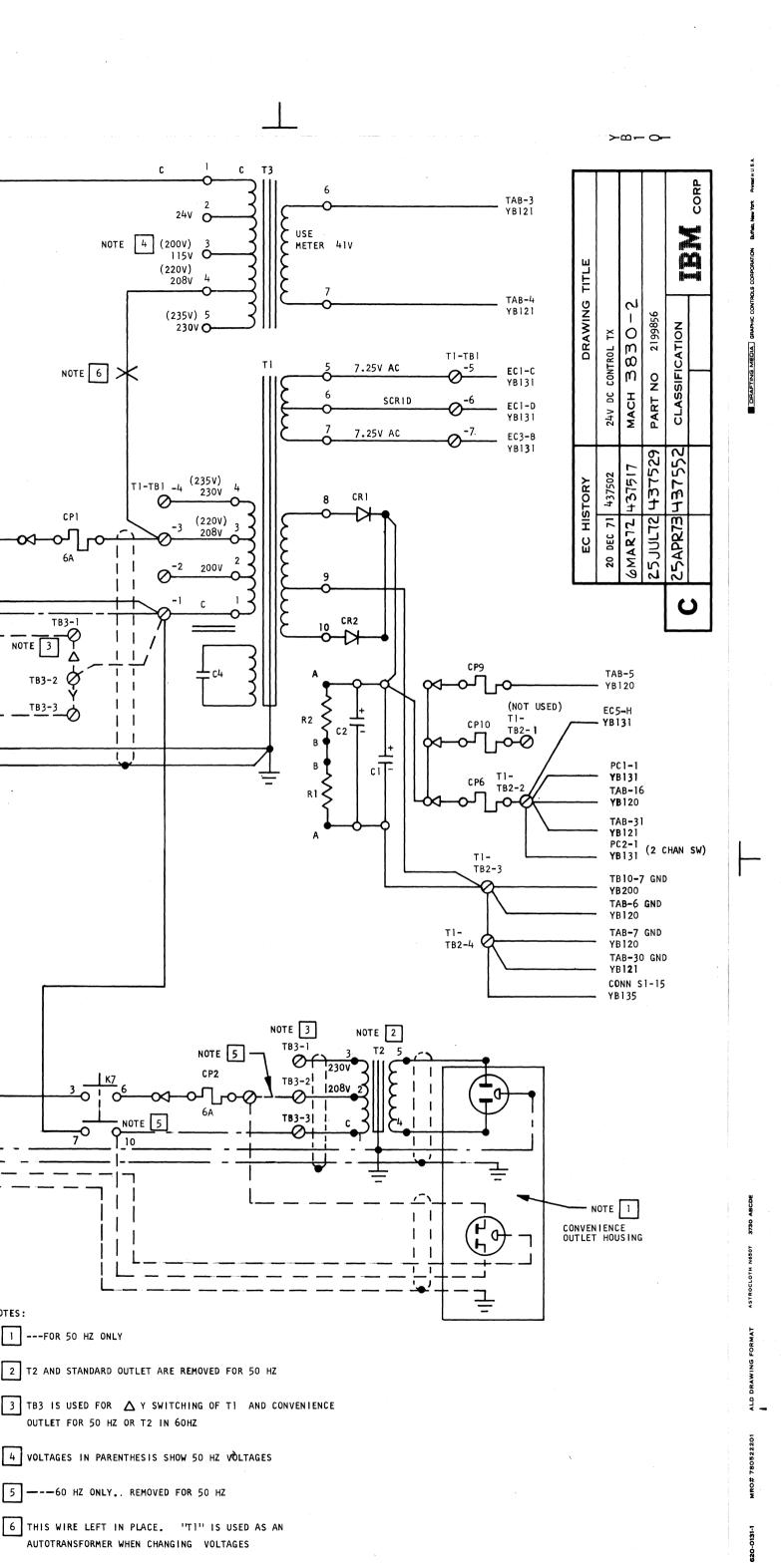
REMOVE		ADI	)	LOGIC PAGE
TB1-3 TO TB1-2		TB1-3 TO	TB1-4	YB100
TB1-7 TO TB1-6		TB1-7 TO	TB1-8	YB100
TB1-11 TO TB1-1	0	TB1-11 TO	TB1-12	YB100
TB3-2 TO TB3-1		ТВ3-2 ТО	TB 3-3	YB101
CONNECT		38ov	408v	LOGIC PAGE
CPI		T1-TB1-3	T1-TB1-4	YB101
50HZ TB-3		BTB1-3	BTB1-1	YB160
50HZ TB-3		BTB2-3	BTB2-I	YB160
50HZ TB-2		BTB1-7	BTB1-5	YB160
50HZ TB-2		BTB2-7	BTB2-5	YB160
50HZ TB-1		BTB I - I I	BTB1-9	YB160
50HZ TB-1		BTB2-11	BTB2-9	YB160
TB2-3		PS15 TB1-3	PS15 TB1-4	YB165





YB100

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NOTE 5 10

NOTE 6

CPI

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TB3-1

твз-2 🧳

TB3-3

NOTES:

1 ---FOR 50 HZ ONLY

NOTE 3

YB101

CB2-1

YB100

CB2-3 YB100

K8-L4

YB 100

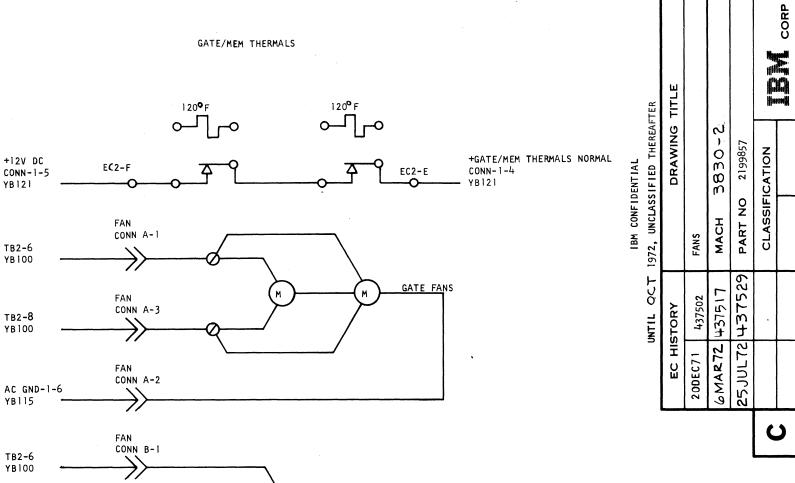
CB2-1 YB100

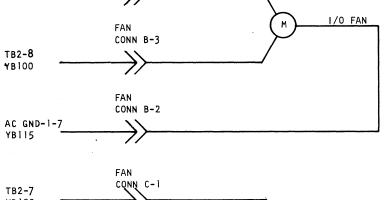
AC GND-2-5 YB115

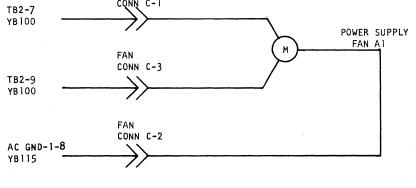
AC GND-1-1 YB115

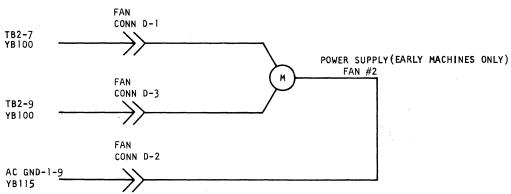
NOTE 5

NOTE 1



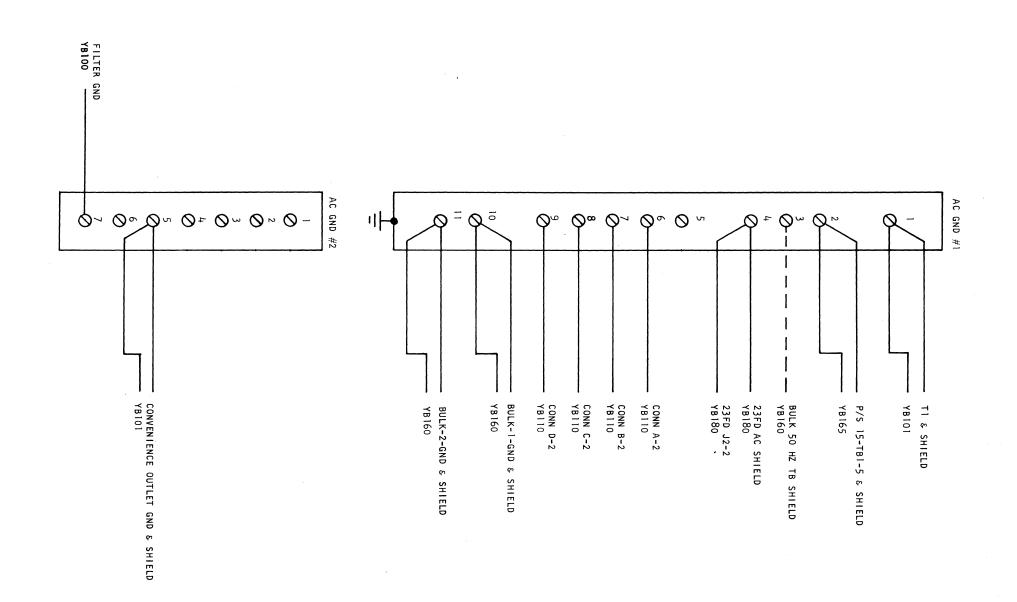






+12V DC CONN 1-3	0-165°F	P/S THERMALS	165 <b>°</b> F	+PS THERMALS NORMAL CONN 1-2
YB121	PS THERM CONN E-2		NOTE 1 PS THE	YB121

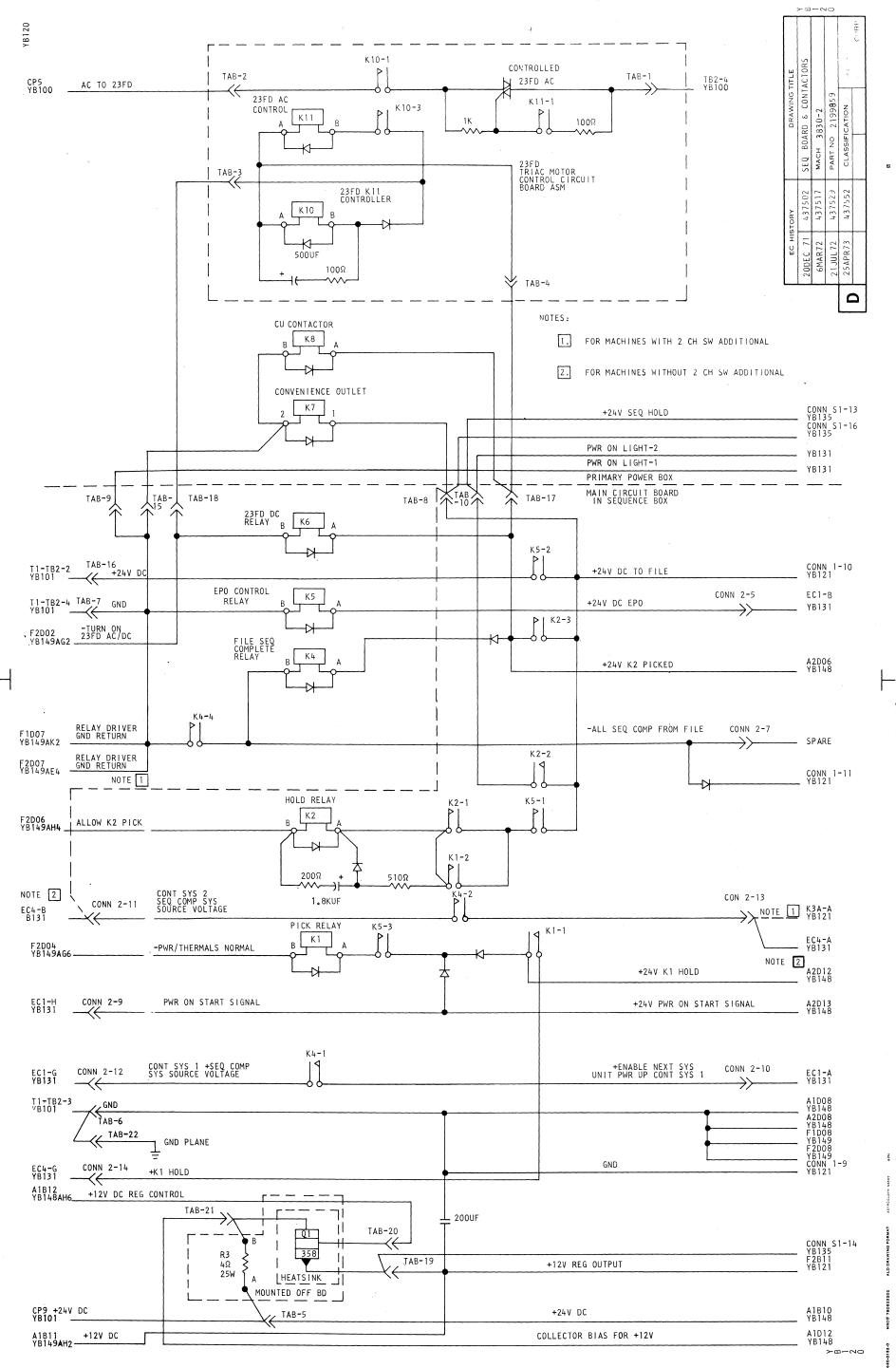
NOTES: 1 THERMAL SWITCH PRESENT IN MACHINES WITH PS FAN NO. 2 ONLY



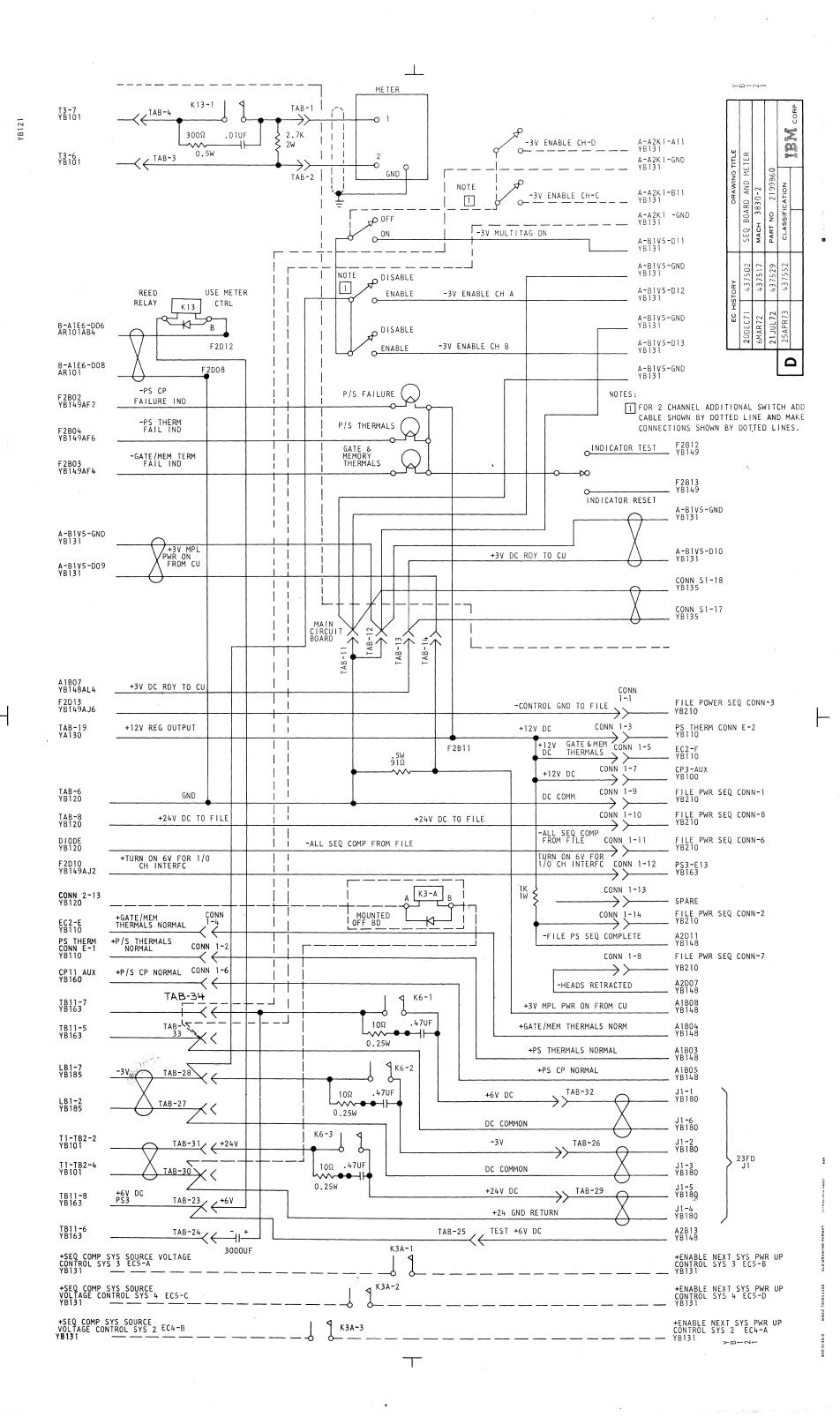
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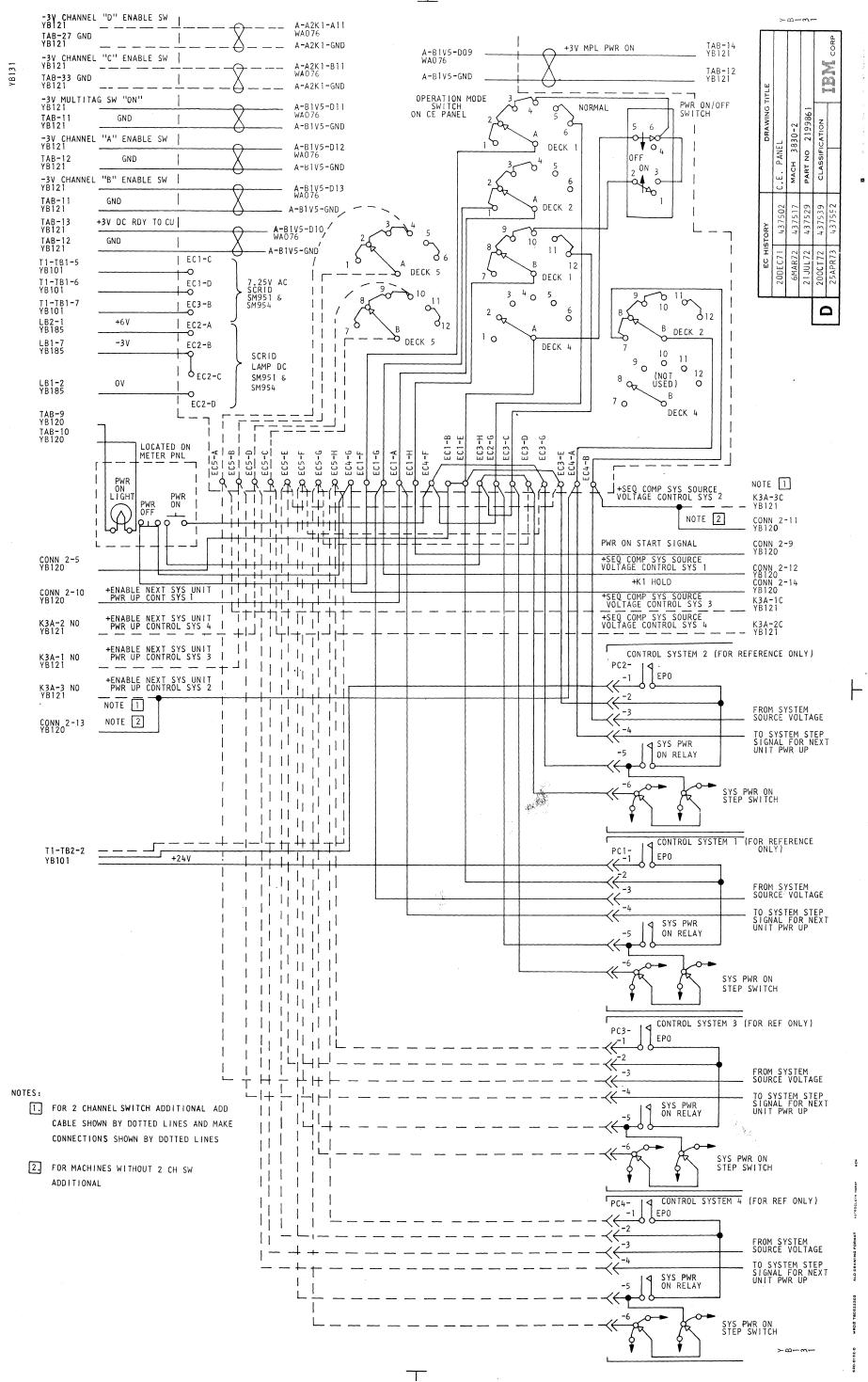
SKITE SET 1972, SKIEDSTIFFE THEADTHEA						
	EC HISTORY			DRAWING	TITLE	
	20DEC71	DEC 7 1 437502 AC GND				
	6MAR72	+37517	MACH 38	30-2	`	
	25 JUL72	437529	PART NO	2199858		
<b>C</b>			CLASSIFICA	ATION	TT: 1	
					DIV CORP	

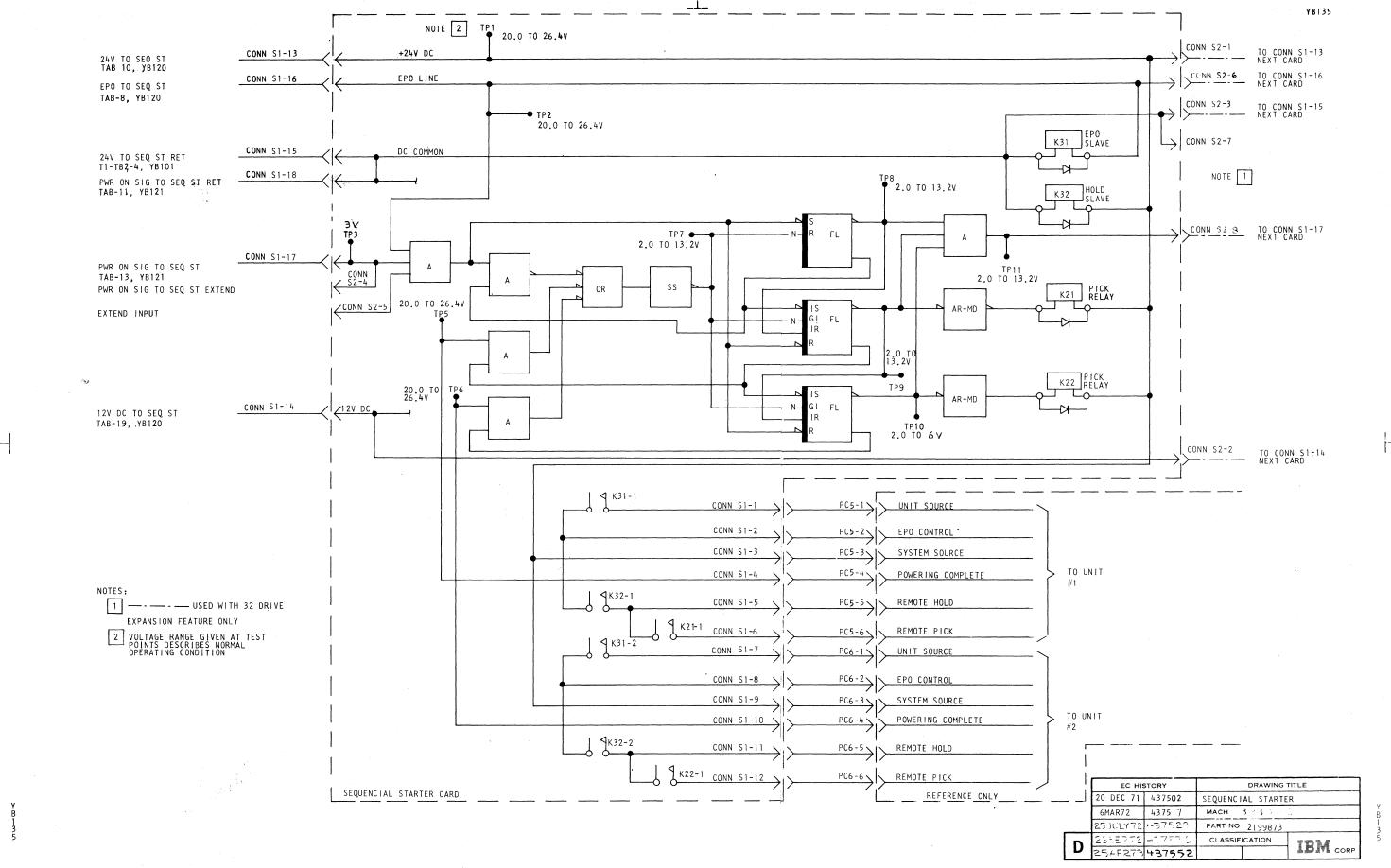
DRAFTING MEDIA GRAPHIC CONTROLS CORPORATION Buffelo, New York Princed in U.S. A.



 $\neg$ 







SINGLE CARD 5860161 L161

CONNECTOR B05 YB148002 B07 YB148004 B08 YB148006 B09 YB1480B2 B10 YB1480B4 B12 YB1480C2 B13 YB1480C4 D02 YB1480C4 D04 YB1480C4 D05 YB1480C6 D07 YB1480D2 D09 YB1480D2 D09 YB1480E6 D11 YB1480D4 D12 YB1480D4 D12 YB1480D4 D13 YB1480D4

DOUBLE CARD 5860690 L690

YB146 A1 B1 B2 B3 B4 B5 B6 B7 B8 B9 C1 C3 C4 D1

SINGLE CARD P783

YB144 01 02 03 04 05 06 07 08 09 10 11 12 13 14 15 16 17

DOUBLE CARD 5860313 L313

YB140 01 02 03 04 05 06 07 08 09 10 11 12 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 30 31 32 33 34 36 37 38 39 40 41 42 43 44 45

40 41 42 43 44 45
46
YB141 47 48 49 50 51 52
53 54 55 56 57 58
59 63 64 66 67 68
69 70 71 72 73 74
75 76 77 78 79 80
B1 82
YB140 83 85
YB141 86 87 88 89 90
YB140 99

DOUBLE CARD 5862698 N698

YB145 R1 R2 R3 R4 R5 R6 R7 R8 R9 UNUSED PORTIONS

STNOLF CORT 5860121 L121

YR142 01 02 03 04 05 06 07 08 09 10 11 12 13 14 15 16 17 18 24 25 26 27 28 29 30 31 32 33

F2

CONNECTOR
RO2 YR142QY4
RO3 YR142QX4
RO4 YR142QX4
RO5 YR149QX4
RO5 YR149QX4
RO8 YR149QX6
RO8 YR149QX6
RO8 YR149QX6
RO2 YR146F4
RO4 YR146RE4
RO6 YR146RE4
RO7 YR146RE4
RO7 YR146RE4
RO7 YR146RE4
RO7 YR146RE4
RO7 YR146RE4

YB1 43 A1

AZ

PART NO ACC TYPE SOCKETS TOTAL L121 F1 L161 A1 L313 D1 L690 B1 N698 E1 CONN A2 F2 P783 C2 01 01 01 01 01 02 01

PAGE 01

PLUG LIST

SOCKET I ISTING
DATE 02-19-75 MACH. 3830-021 Y B 11 3 18 | | I nG 9109 BNARD 01P-0 | | PREV- ENGR- - - - | | PRES- ENGR- 02-07-75 437380 | | P-N- 2348933 BnARD 01 -01 005 IBM CORP. SDD BIK.

SOLID LOGIC DESIGN AUTOMATION-BOARD- 01P-A1

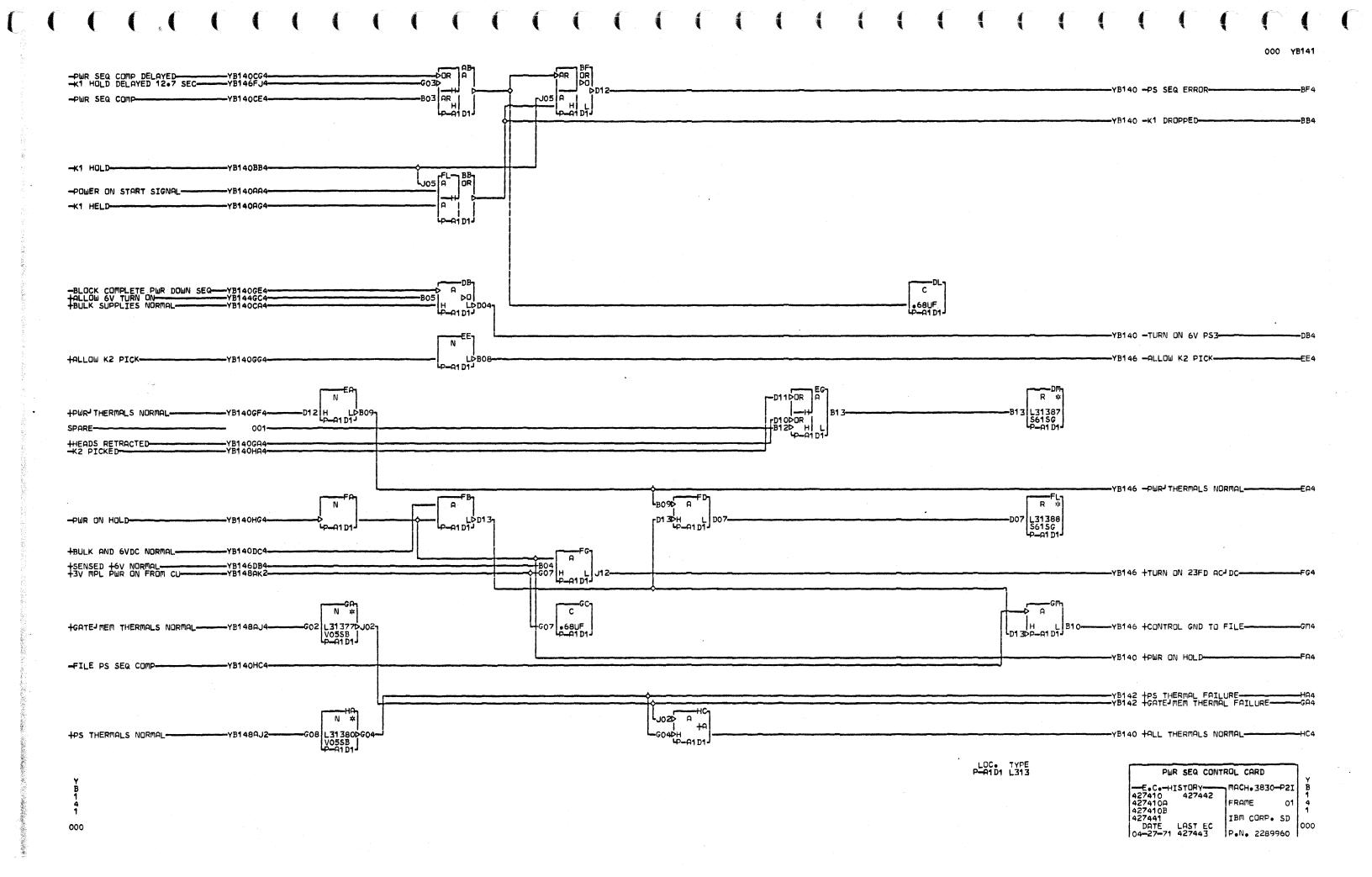
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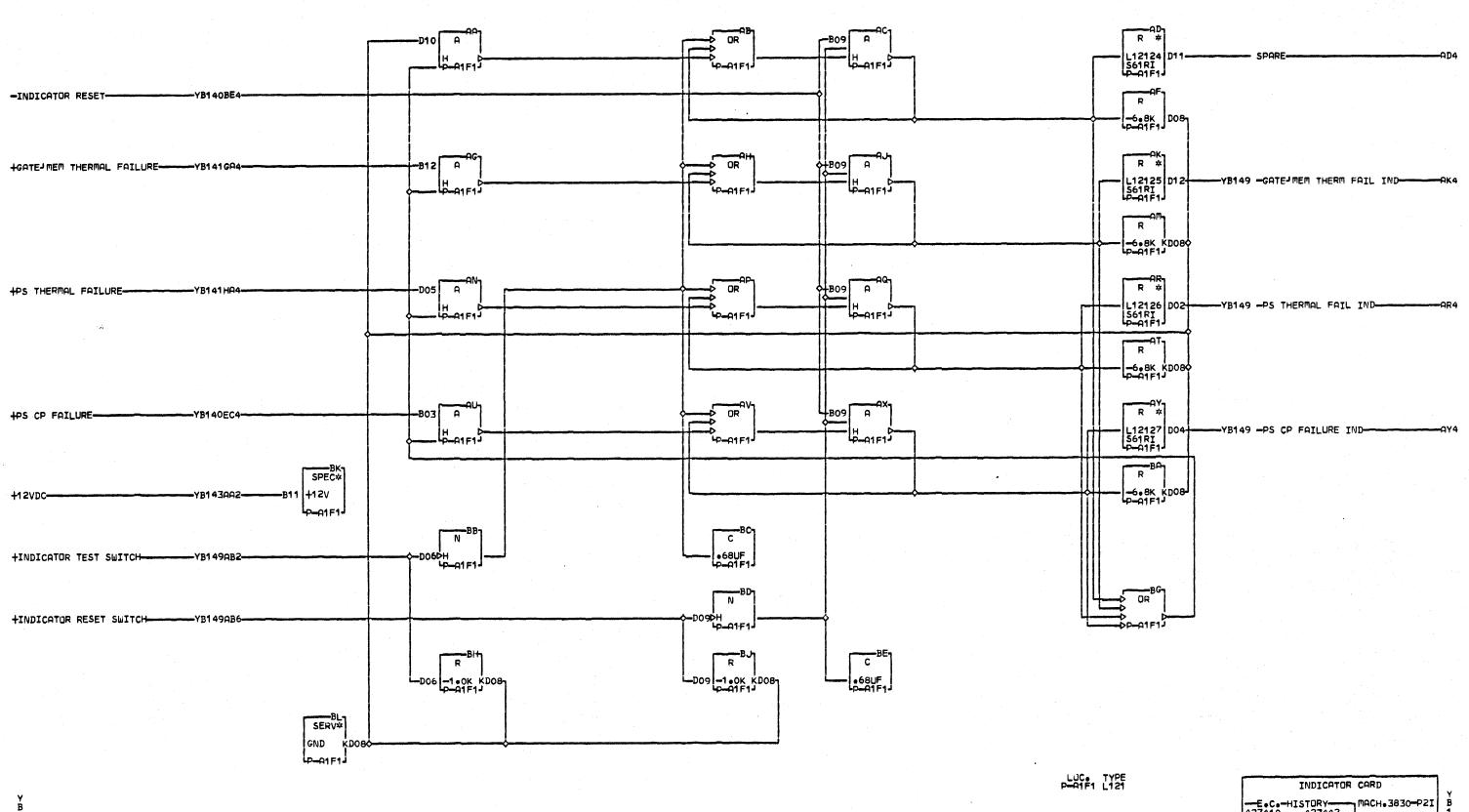
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SYMBOI S

X- NO RUE SOCKET E- CONFI ICT S- PORTIONS I EFT SIDA CHART
DATE 02-19-75 MACH. 3830-P2I Y
ING 9109 BNARD 01P-A1 11
PREV. ENGR. - 3
PRES. ENGR. 02-07-75 437380 9
P.N. 2348934 0005
IBM CORP. SDD BIK.

TD 10mS ANTI RECYCLE N \* FLOR N \* -G10 L313010 V055A J L313040-V055B P-01D1 +24V PWR ON START SIGNAL YB148AE2 -YB141 -K1 HELD-LP--01 D1 -YB141 -POWER ON START SIGNAL--K1 DROPPED--YB141BB4 -BE-Ç A N A L313080J05 V05SA P-01D1 DOT 68UF +24V K1 HOLD--YB148AD6 -YB142 -INDICATOR RESET-HALL THERMALS NORMAL--YB141HC4-OR \* DOT -TURN ON 6V PS3--YB141DB4--YB141FA4-TD 10MS N to N N N N # rD12 050H 01D1 D065H 101 -J070H LJ L313190 V05SB P-01D1 -BULK SUPPLIES NORMAL-YB145DF4 L DO2 YB141 -PWR SEQ COMP DELAYED-LP-01 D1 -K2 PICKED -YB141 -YB141 -- PWR SEQ COMP B075L31383 V055A P-A1D1 -YB141 +BULK AND 6VDC NORMAL-N × A G030 D11DA | L -G03DH L31 326 B02---[V05SC P-01 D1 DD12----YB148 +3VDC DC READY TO CUrB04 LP--01 D1 LD-1017 ip-01D1 P-01 D1 -K1 HOLD DELAYED 12.7 SEC-YB146FJ4rG12 A +SENSED +6V NORMAL-N # SERV\* YB141 -BLOCK COMPLETE PWR DOWN SEQ-GE4 B07DL31328 V055A P-01D1 NO6 L313300G127 GND لهووا -YB141 +PWR-THERMALS NORMAL-4P--01 D1-+PWR SUPPLY CP NORMAL --YB148AJ6--YB142 +PS CP FAILURE-TYB141 HEADS RETRACTED OR GET -D12 A -D12 DOT **-**J09 13 L313360 D05 U110L31337 V05SA P-A1D1 -HEADS RETRACTED -OV +24V1-YB148AD2-DOT -FILE PS SEQ COMPLETE-SPARE-YB141 -FILE PS SEQ COMP HEADS EXTND OR MEM PWR ON-YB144FL4-PS SEQ ERROR-YB141BF4-YB141 HALLOW K2 PICK-A 05 L313400 D11-+24V K2 PICKED-YB148AC6 YB141 -PWR ON HOLD-LOC. TYPE PWR SEQ CONTROL CARD -E.C.-HISTORY-427410 427410A 427410B MACH. 3830-P21 FRAME IBM CORP. SD 000 DATE LAST EC 103-09-71 427442 P.N. 2289959





-E.C.-HISTORY MACH.3830-P2I 427410 427442 427410A 427410B 427441 DATE LAST EC 04-27-71 427443 P.N. 2289961

000 YB143

PWR \*XB11

D12XL161R1

| S255R | P-A1R1 | B12

PB148 +12VDC REG CONTROL

AA2

PB148 +12VDC REG CONTROL

AA3

PB148 +12VDC REG CONTROL

AAA

P-A1A1 L161

12 VOLT REGULATOR

-E.C.-HISTORY MACH.3630-P2I
427410 427442
427410B
427410B
427441 IBM CORP. SD
04-27-71 427443 P.N. 2289962

COLLECTOR BIAS FOR +12V-YB148AK4-

UNCLASSIFIED THEREAFTER 005 YB144

PS7 +1 .25V SENSE-P763080B03-S15RB P-91C2XD081 -YB148 -TURN ON PS6 3.5V-PS7 -3V SENSE -2.3V MIN-+K1 HOLD DELAYED 12 SEC-YB146CJ4-CHD KDOB lp-9102 PS6 +3.5V SENSE--YB141 +ALLOW 6V TURN ON-81 25 OR -YB140 HHEADS EXTND OR MEM PUR ON-THEADS RETRACTED--YB140GA4-+SENSED +6V NORMAL--YB146DB4--K1 HOLD--K2 PICKED--YB140HA4

> LOC. TY P-A1C2 P7

SNIPE MEMORY SEQUENCE

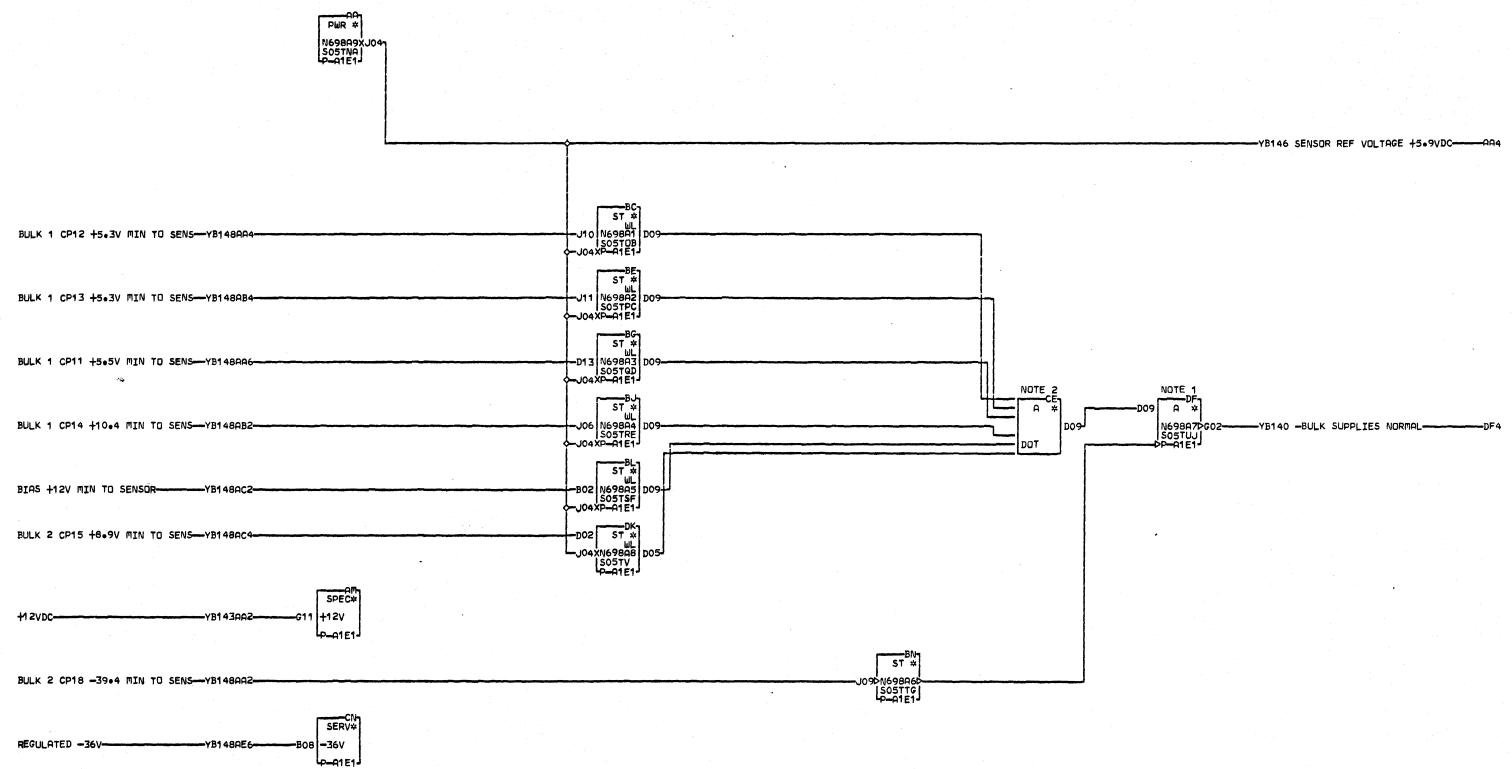
—E.C.-HISTORY—— MACH.3830—P2I
PRECO01 FRAME 01
IBM CORP. SD
02-07-75 437380 P.N. 2348927

Y B 1 4

005 STM TO PN 2289963 EC 427447 ΙBm

CONFIDENTIAL

UNTIL 12-01-75



NOTE NO. 1 THE BULK DC INPUT MIN LEVELS MUST BE MAINTAINED Y TO HAVE A -BULK SUPPLIES B NORMAL OUTPUT 1 4 NOTE 2 5 PLUS LEVEL FOR DO9 IS +12V MINUS LEVEL FOR DO9 IS +11.3V

P-A1E1 N698

PLD SENSO	RIII	1
	MACH.3830-P2I	B
427410 427442 4274100	FRAME 01	4 5
427410B   427441	IBM CORP. SD	000
DATE LAST EC 04-27-71 427443	P.N. 2289964	1000

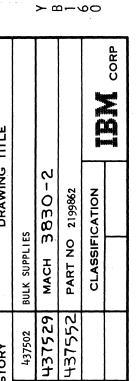
- UNCLASSIFIED THEREAFTER \*\* 000 YB146 ₩₩ IBM CONFIDENTIAL UNTIL 08-01-71 6V SENSOR CV \* TEST +6VDC--YB148AB6-L690A1 D13X5055Q LP-A1B1 - +SENSED +6V NORMAL-**∠**YB140 SENSOR REF VOLTAGE +5.9VDC-YB145AA4-R \* L690B5 S61PG P-01B1 -PWRJ THERMALS NORMAL-YB141EA4 N \* -B05 L690B60 V055D I P-01B1KD07-L690B7DG07-S15RB | P-A1B1KD07-YB149 -PWRJ THERM NORM ALLOW K1 PICK-DE4 N \* N × L690B3DJ06-S15RB | P-A1B1KD07--ALLOW K2 PICK--YB141EE4-YB149 -ALLOW K2 PICK-N \* N \* L690B4DJ07-S15RB | P-01B1KD07-+TURN ON +6V FOR IJO CH INTEC-YB140CM4--YB149 +TURN ON 6V FOR IJO CH INTFC---BJ4 -YB149 -RELAY DRIVER GND-N \* L690B9DG08-S15RE | P-01B1KD07-+CONTROL GND TO FILE--YB141 GM4 YB149 -CONTROL GND TO FILE-N \* L690B8DJ10-\$15RE | P-01B1KD07-+TURN ON 23FD AC DC--YB141FG4--YB149 -TURN ON 23FD AC DC--YB144 +K1 HOLD DELAYED 12 SEC-12 SEC NOTE 1 TD \* G040L690C1 -K1 HOLD--YB140BB4-D12J 0.7 SEC NOTE 2 TD \* -K1 HOLD DELAYED 12.7 SEC-L690C4 V45SD P-01B1 **└**YB140 LP-01 B1 C +12VDC-YB143AA2-NOTE 1 LEADING EDGE DELAY MAX 16.5 SEC MIN 7.5 SEC TRAIL EDGE DELAY NOM O SEC TIMER DRIVER + 6V SENSOR -E.C.-HISTORY 427410 427442 4274100 4274108 42741 DATE LAST EC 04-27-71 427443 MACH.3830-P2I 1 NOTE 2 LEADING EDGE DELAY 4 MAX 2.0 SEC MIN 0.2 SEC 6 TRAIL EDGE DELAY NOM 0 SEC FRAME 01 IBM CORP. SD 000 000

\*\* IBΠ CONFIDENTIAL UNTIL 08-01-71 - UNCLASSIFIED THEREAFTER \*\* 005 YB148 ENTR\* B05\* Bulk 2 CP18 -39.4 MIN TO SENS-\*YB160
Bulk 1 CP12 +5.3 MIN TO SENS-\*YB160
Bulk 1 CP11 +5.5 MIN TO SENS-\*YB160 YB145 BULK 2 CP18 -39.4 MIN TO SENS-AA2 005---ٽ----ٽ B08 12-1 A2-1 /B145 BULK 1 CP11 +5.5V MIN TO SENS-AA6 007---ENTR\* BO9 BULK 1 CP14 +10.4 MIN TO SENS-\*YB160 BULK 1 CP13 +5.3 MIN TO SENS-\*YB160 TEST +6VDC TAB 25-\*YB121 YB145 BULK 1 CP14 +10•4 MIN TO SENS-AB2 008---j /B145 BULK 1 CP13 +5.3V MIN TO SENS-AB4 009~ P-A1 A2 i B1 34 ن--010 BIAS +12V MIN TO SENSOR TB2-3-\*YB165 PS15 BULK 2 CP15 +0.9 MIN TO SENS-\*YB150 +24V K2 PICKED-\*YB120 K2-3 YB145 BIRS +12V MIN TO SENSOR-P--A1 A2 J DO6X 013----HEADS RETRACTED — CONN 1-8\*\*YB121
F7LE PS SEQ COMP — CONN 1-14\*\*YB121
#YB120 K1-1 YB140 -HEADS RETRACTED 1-0V +24V----ADS 015 016----——AE7 ENTR≠1 D1 3\$ #24V PWR ON START SIG CONN 2-9-YB140 +24V PWR DN START SIGNAL-018---REGULATED -36V #YB163 -YB145 REGULATED -36V-024---SERV# EXIT# CND PS3 \_\_\_\_ TB11-3 \*YB163 PS3 ENTROP 028--GND GND #YB120 029\_\_\_ EXIT#1. -TURN ON PS6 3.5V-YB144GE4# -Y8170 -TURN ON +3.5V pS6-E12-YB170 -TURN ON +7V REG --- PS4-E12----AG6 SERV YB143AA2. +12VDC-YB120 +1 2VDC --- TAB 19 EXIT H2VDC REG CONTROL--YB1430A6-YB120 +12VDC REG CONTROL - TAB 20-SERV# BO3 tps THERMALS NORMAL - CONN 1-#YB121 +GATEJ MEM THERM NORM CONN 1-4 001-YB141 +GATEJ MEM THERMALS NORMAL-#YB121

+PS CP NORMAL —— CONN 1-6

#YB121 002-003-+3v MPL PWR ON FROM CU TAB 14—
\*YB121
COLLECTOR BIAS FOR +12v TAB 21—
\*YB120
\*YB120
TAB 5 YB141 +3V MPL PWR ON FROM CU-004-ENTR 030-YB143 +24VDC-021--SERV EXIT -yB1 40DH4-YB121 +3VDC DC READY TO CU - TAB 13-AL4 +3VDC DC READY TO CU-LP-A1 A1J SERV\* GND GND -022----LP-A1A1J YB144GE4 AC4 P-A1A2D02
01P-A1A2D04 AC6 P-A1A2D06
AA2 P-A1A2B05 AD2 P-A1A2D07
AA4 P-A1A2B07 AD4 P-A1A2D11
AA6 P-A1A2B08 AD6 P-A1A2D12
AB2 P-A1A2B08 AD6 P-A1A2D13
AB4 P-A1A2B10 AE6 P-A1A2D05
AB6 P-A1A2B13 AG6 P-A1A2D05 ENTR EXIT -E.C.-HISTORY-MACH. 3830-P21 FRAME IBM CORP. SDD DATE LAST EC 02-07-75 437380 005 SIM TO PN 2289966 EC 427442 P.N. 2348928

\*\* IBM CONFIDENTIAL UNTIL 08-01-71 - UNCLASSIFIED THEREAFTER \*\* 005 YB149 ENTR\* B12\* +INDICATOR TEST SWITCH-\*YB121 PS7 SENSE +1.025V LB3-4-\*YB185 +INDICATOR RESET SWITCH-\*YB121 YB142 +INDICATOR TEST SWITCH نــــا 001 B07\* -YB144 PS7 +1.25V SENSE-007---P-A1 F2j B13\* YB142 +INDICATOR RESET SWITCH 002-ENTR\* B05\* -YB144 PS6 +3.5V SENSE-008-+808 ن-£1 P0− YB144 PS7 -3V SENSE -2.3V MIN-009-SERV\* ENTR\* 006-+70DC GND +7VDC GND \*YB185 +3•5V GND \*YB185 -3VDC GND \*YB185 010---DOS GND 011-P-A1 F2 012-EXI I\* -PS CP FAILURE IND---YB142AY4\* YB121 -PS CP FAILURE IND--GATE MEM THERM FAIL IND---YB142AK4# YB121 -GATE THERM FAILURE IND-YB121 -P'S THERM FAILURE IND--PS THERMAL FAIL IND--TURN ON 23FD AC DC-YB146EE4# EXIT\* YB120 \_TURN ON 23FD AC DC \_\_ TAB 18\_\_AG2 -PWR-THERM NORM ALLOW K1 PICK-YB146DE4\* -D04 P--01 F2 YB120 -- PWR THERMALS NORMAL -- K1 ----- AG6 EXIT\* -ULLOW KS PICK---YB146BE4# YB120 -ALLOW K2 PICK --- K2---لمحما F2 امحما TURN ON 6V FOR IJO CH INTFC-YB146BJ4\* -D10 EXIT# -YB121 +TURN ON 6V IO INTFC CONN 1-12-AJ2 -CONTROL GND TO FILE--γB1 46FF4#--D13 lp--A1 F2. -YB121 -CONTROL GND TO FILE CONN 1-1-AJ6 EXIT\* -RELAY DRIVER GND-YB120 RELAY DRIVER GND RETURN K4-4-AE4 -vB146AE6\* -D07 نـ1 F2 مــــ SERV\* -YB120 RELAY DRIVER GND RETURN K4-4---AK2 i-D07 GND --Do8 <del>iP--</del>A1 F1 j Do8 005---Ĭ SERV\* LDOS GND YB142AK4 01P-A1F2D06 AB2 P-A1F2B12 AB4 P-A1F2B07 YB142AR4 01P-A1F2D10 AB6 P-A1F2B13 AC4 P-A1F2B05 YB142AR4 01P-A1F2D04 AC6 P-A1F2B08 O1P-A1F2D07 YB146AE6 01P-A1F2D02 O1P-A1F2D07 YB146F4 YB146BE4 01P-A1F2D13 ENTR EXIT MACH.3830-P2I IBM CORP. SDD DATE LAST EC 02-07-75 437380 005 SIM TO PN 2289967 EC 427443 P.N. 2348929



25JULT2 L

20DEC71

NOTE 2 50HZ ONLY 50HZ TB TB1-11 YB100 **⊘**-1 TB1-7 DRAWING YB100 TB1-3 **⊘** -3 YB100 AC GND-1 YB115 FRAME BULK 1 +5.5V MIN TO SENSOR A2BO8 YB148 BULK 1 POS TO +2V PS6-E1 CPII O YB170 NOTE 2 BTBl CP8-1 BULK 1 NEG TO +2 PS6-E2 **⊘** -2 YB100 YB170 1 1 BULK 1 DUAL +5.3V MIN TO SENSOR A2BO7 CP8-2 1 YB148 YB100 **Ø** -6 HISTORY ı ١ BULK 1 POS TO DUAL PS7-E1 CP12 O YB 170 CP8-3 YB100 **Ø** -10 1 1 BULK 1 NEG TO DUAL PS7-E2 0 W4 YB170 S 1 1 BULK 1 DUAL +5.3V MIN TO SENSOR A2B10 AC GND-1-10 1 YB148 YB115 BULK 1 POS TO DUAL PS5-E1 CP13 O YB 170 \* BULK 1 NEG TO DUAL PS5-E2 0 W2 YB170 BULK 1+10.4V MIN TO SENSOR A2B09 YB148 BULK 1 POS TO +7 PS4-E1 CP14 O YB170 BULK 1 NEG TO +7V PS4-E2 C12(-) Ø-YB170 BULK #1 (REF YB201) BULK 2 +8.9V MIN TO SENSOR A2DO2 YB148 CP15 BULK 2 POS TO +6V PS3-E1 LOAD YB163 NOTE 2 BTB2 CP7-1 BULK 2 NEG TO +6V PS3-E3 **⊘**-2 w8 0 YB100. YB163 1 1 CP7-2 BULK 2 POS TO +6V FILE PS TB10-1 CP16 0 **-**6 YB200 YB100 1 1 LOAD BULK 2 NEG TO +6V FILE PS TB10-13 CP7-3 1 1 **⊘**-10 0 w8 YB100 YB200 1 1 BULK 2 NEG TO -36V FILE PS TB10-3 CP17 LOAD O YB200 AC GND-1-11 - 1 YB115 BULK 2 POS TO -36V FILE PS TB10-10 0 W9 YB200 FRAME BULK 2 -39.4V MIN TO SENSOR A2B05 YB148 BULK 2 NEG TO -36V PS3-E11 CP18 Ø CP18 0 YB163 LINE LOAD BULK 2 POS TO -36V PS3-E3 0 YB163 **W**9 BULK # 2 (REF YB202) CIRCUIT BREAKER PANEL +PS CP NORMAL CONN 1-6 CP AUX CONTACTS SHOWN ON YB121 CP11 AUX N/0 OC1 N/C OC CP12 AUX N/0 💁 N/C O N/0 001 CP13 AUX INATIONS 0 N/C C CP14 AUX N/0 00 CP15 AUX N/C O N/C O N/C O OC CP16 AUX

C CP17 AUX

N/C O CP18 AUX

N/C O

CP8 AUX YB100

BULK

YB160

NOTES:

1 \* BULK VOLTAGES ARE SHOWN AT REGULATOR DEST

>a-90

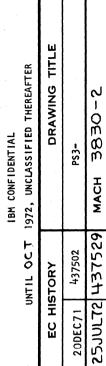
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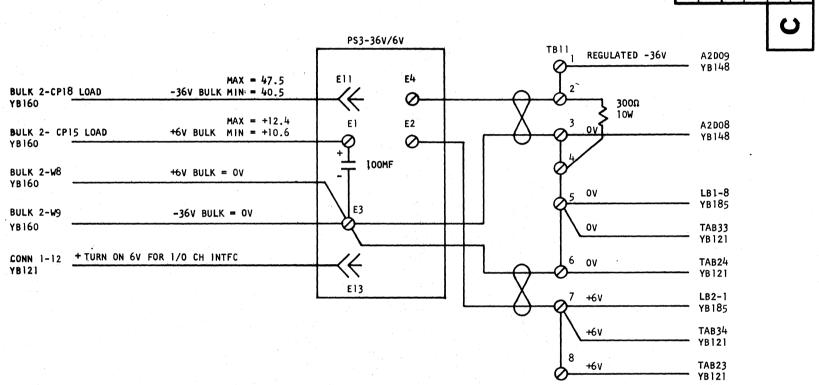
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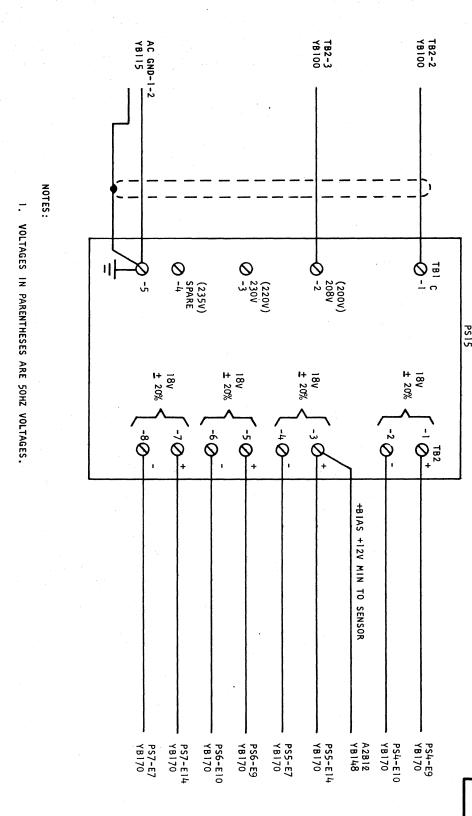
CLASSIFICATION

CORP

DRAFTING MEDIA GRAPHIC CONTROLS CORPORATION







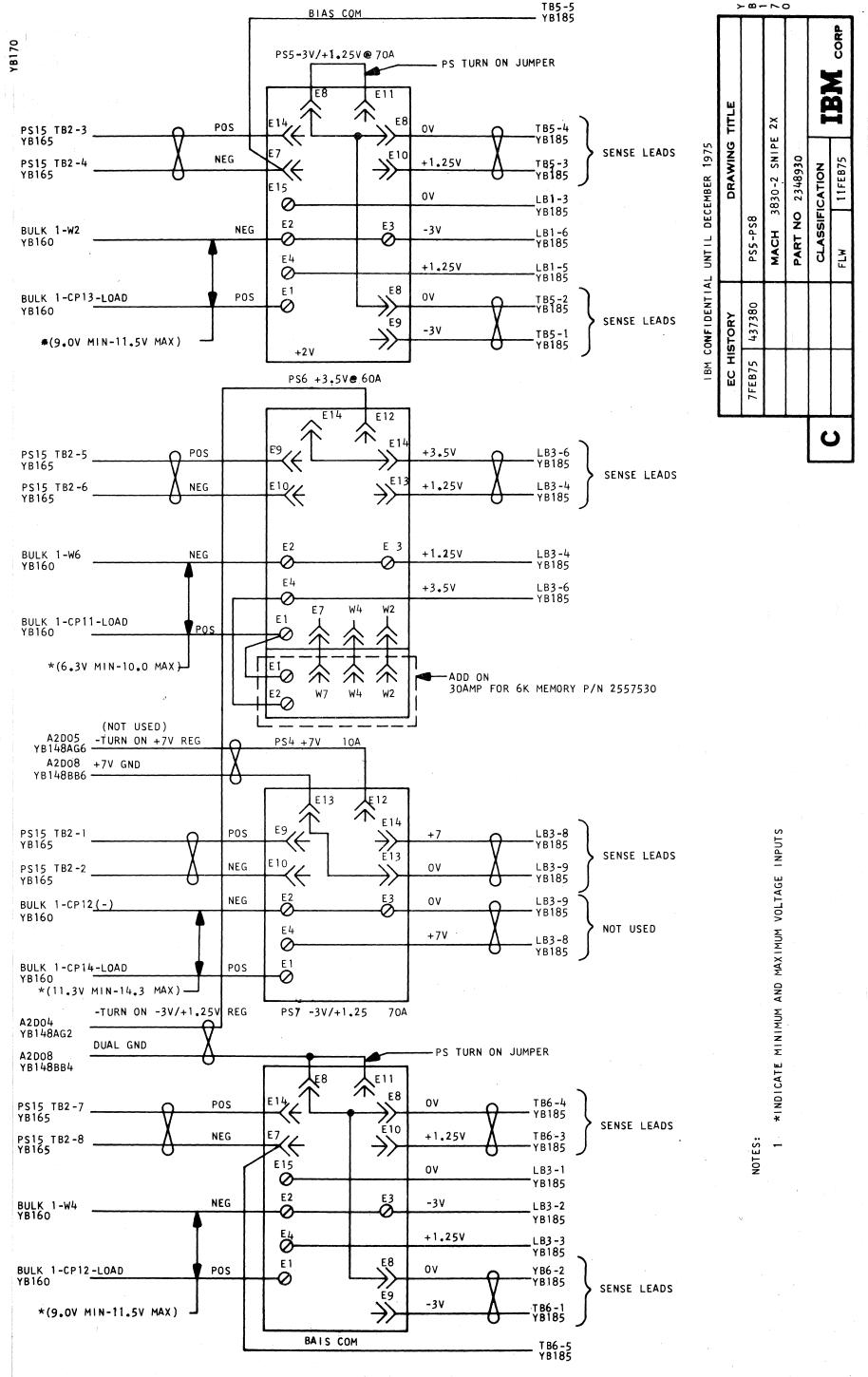
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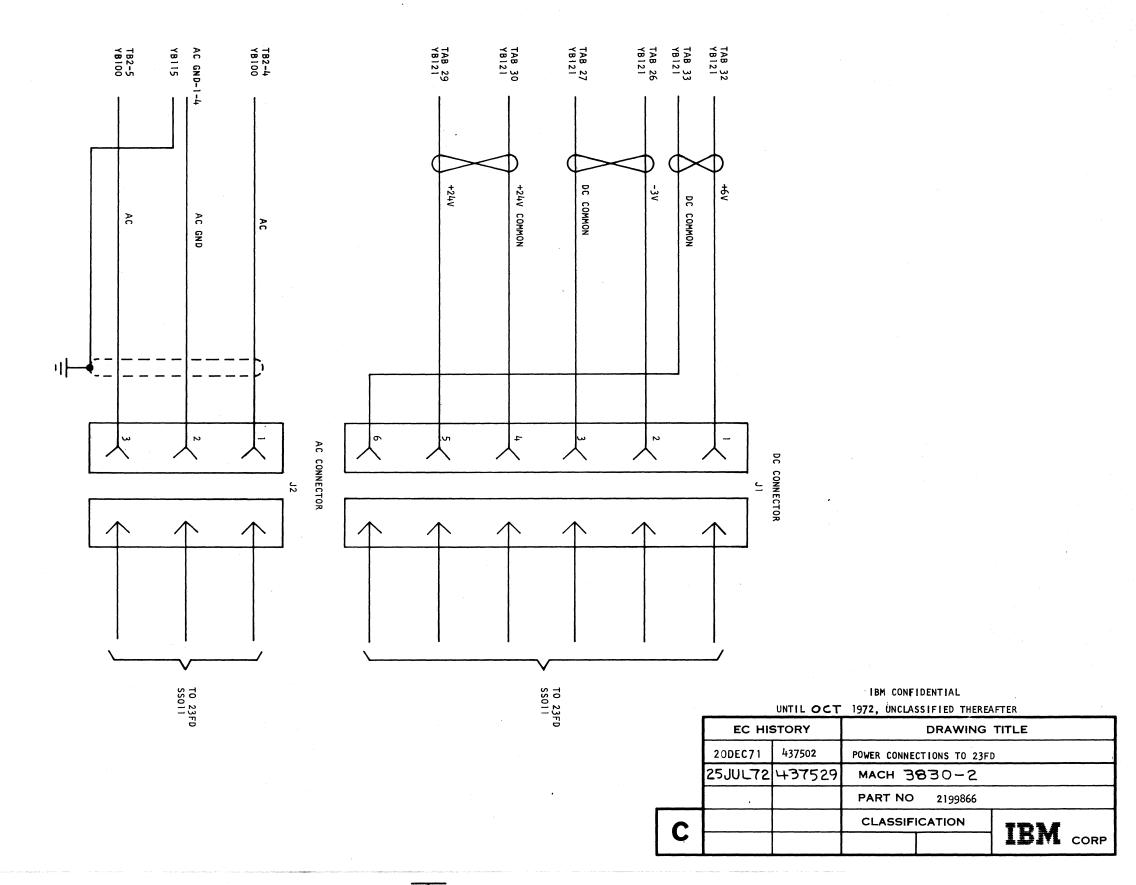
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	25JUL72	437529	MACH 3830-2	
			PART NO 2199864	
<u></u>			CLASSIFICATION	TOL
U.				LDM CORP

ALD DRAWING FORMAT ASTROCLOTH N650T 3730 ABCDE MRO# 780522201





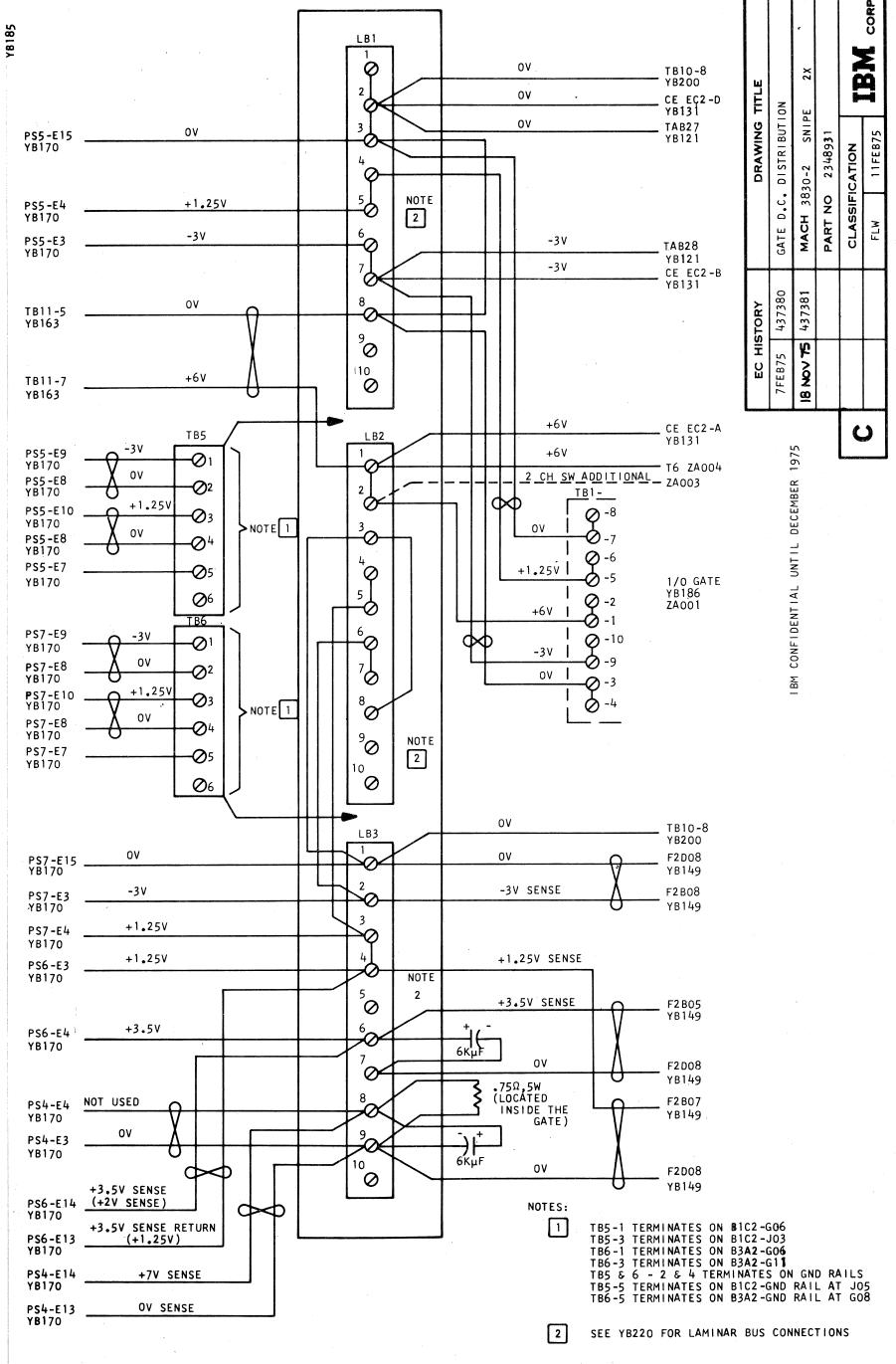




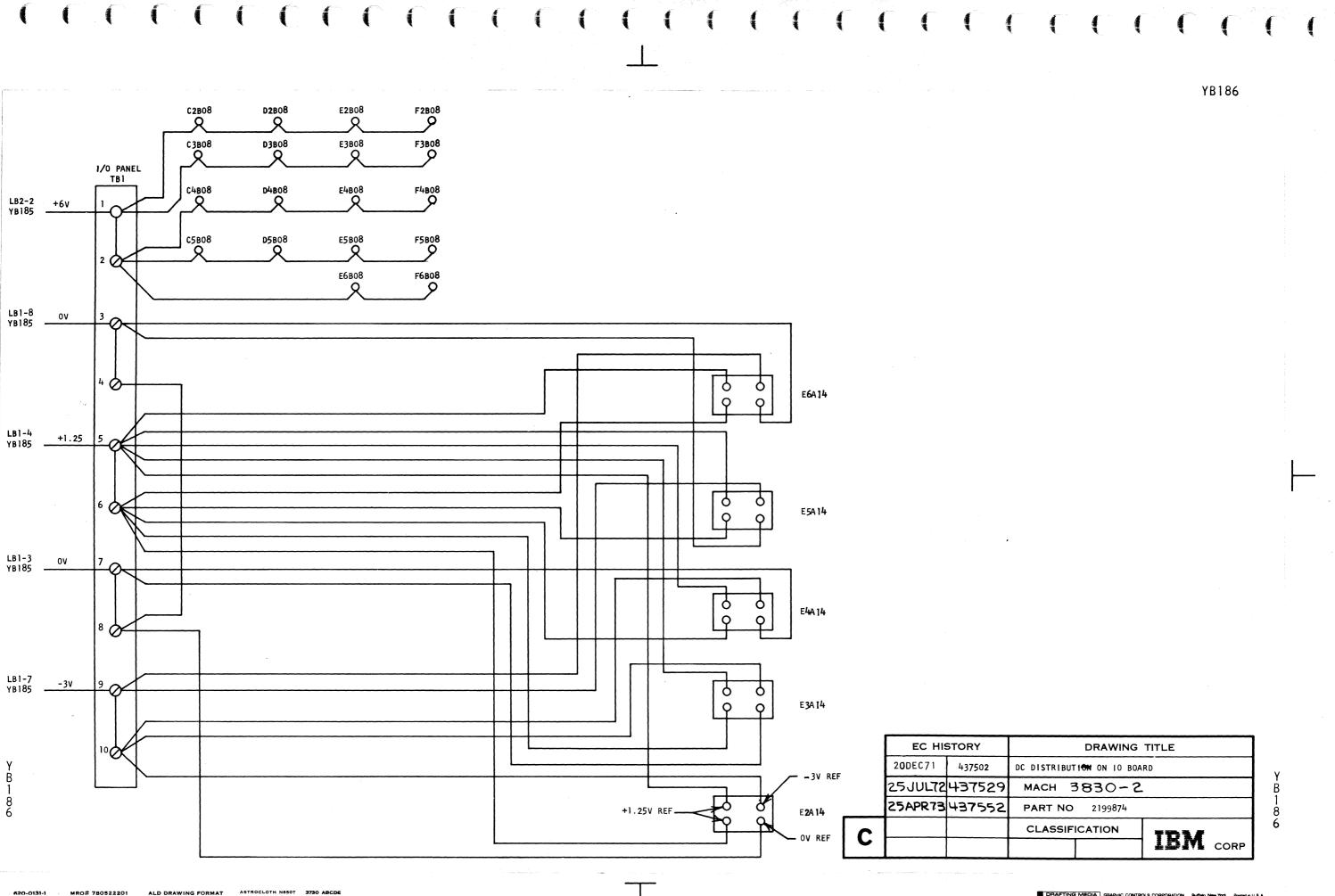
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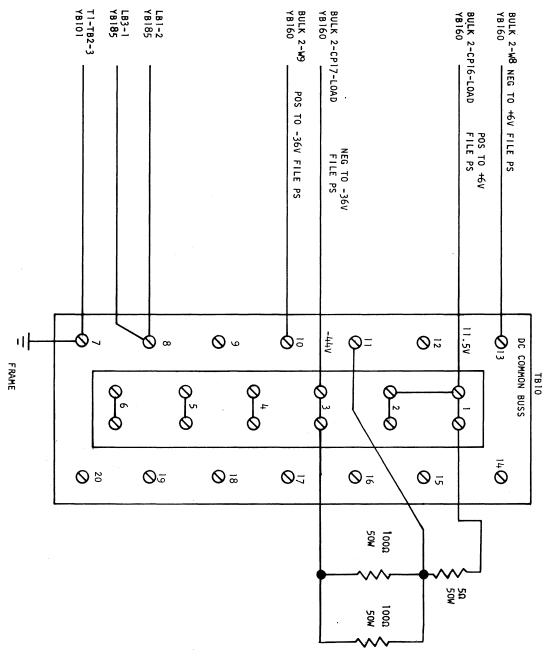


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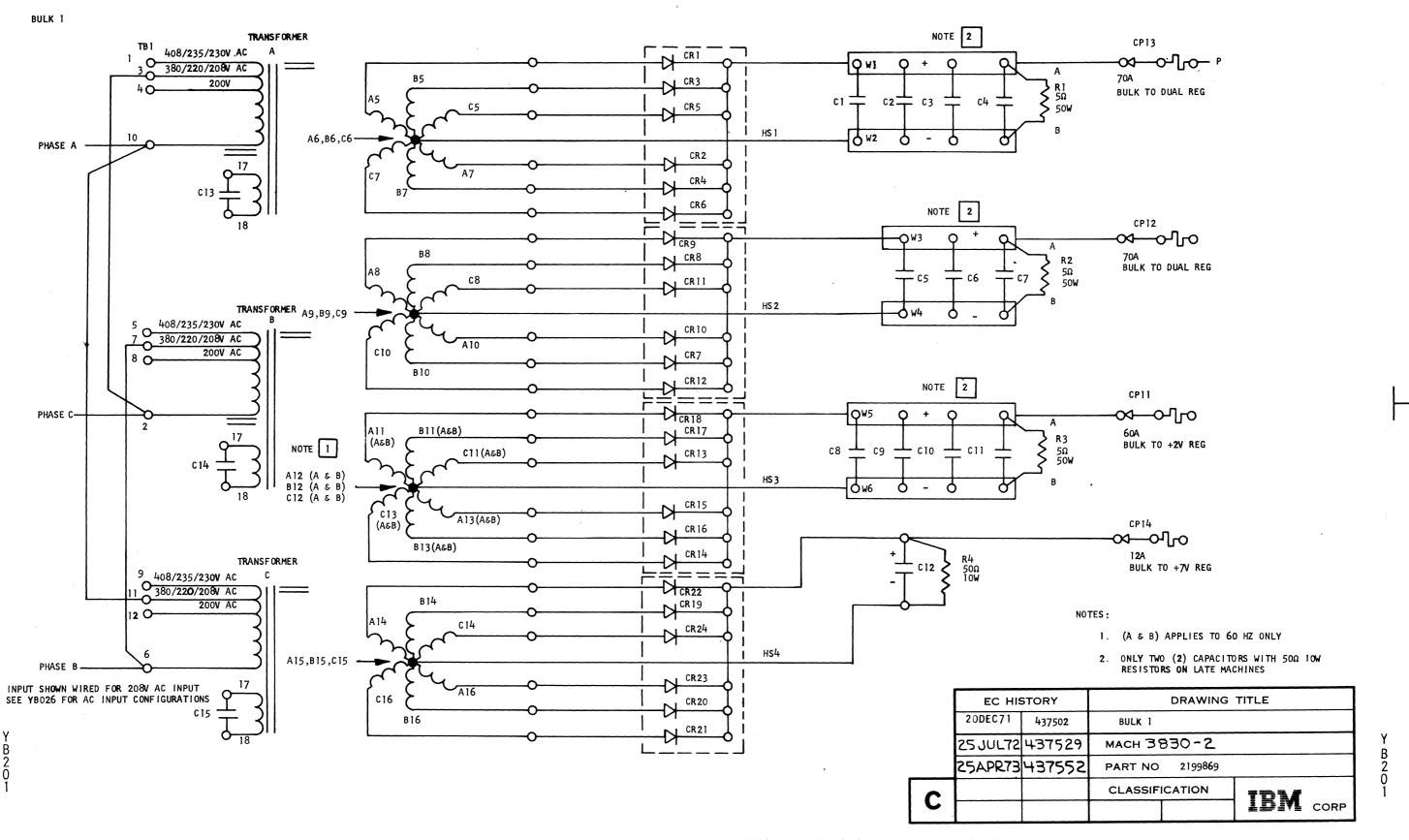
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			LDIV CORP		

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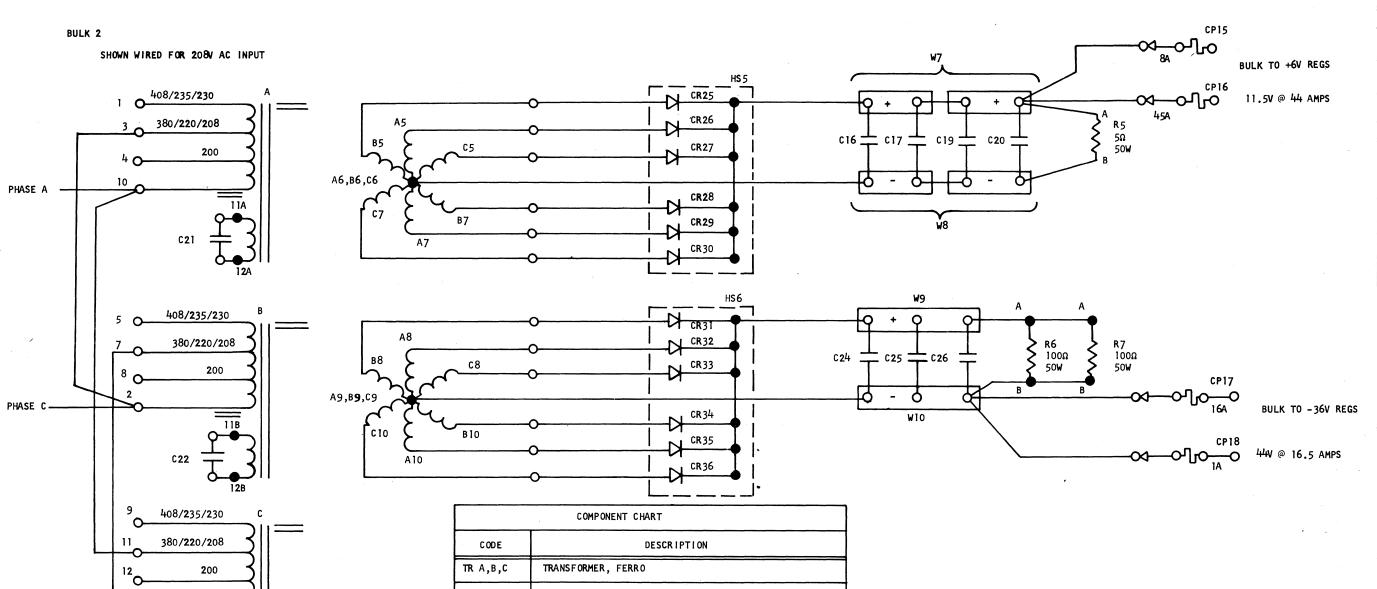
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0131-1 MRO# 780522201 ALD DRAWING FORMAT ASTROCLOTH N650T 3730 ABCDE

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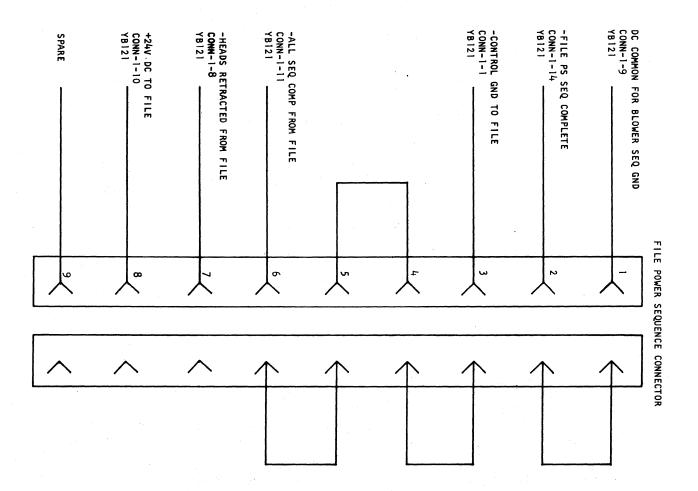
SEE YBO26 FOR AC INPUT CONFIGURATIONS

CODE	DESCRIPTION			
TR A,B,C	TRANSFORMER, FERRO			
CR25-30	RECTIFIER 30A,150V			
CR31-36	RECTIFIER 10A,150V			
C21-23	CAPACITOR 30 µfd 330V AC			
C16-20	CAPACITOR 120K µfd, 15V			
C24-26	CAPACITOR 34K µfd, 55V			
CP16	CIRCUIT BREAKER 45A			
CP17	CIRCUIT BREAKER 16A			
CP15	CIRCUIT BREAKER 8A ,			
CP18	CIRCUIT BREAKER IA			
R5	5Ω, 50W			
R6-7	100Ω, 50W			

IBM CONFIDENTIAL
UNTIL OCT 1972, UNCLASSIFIER THEREAFTER

		EC HIS	STORY	DRAWING TITLE	
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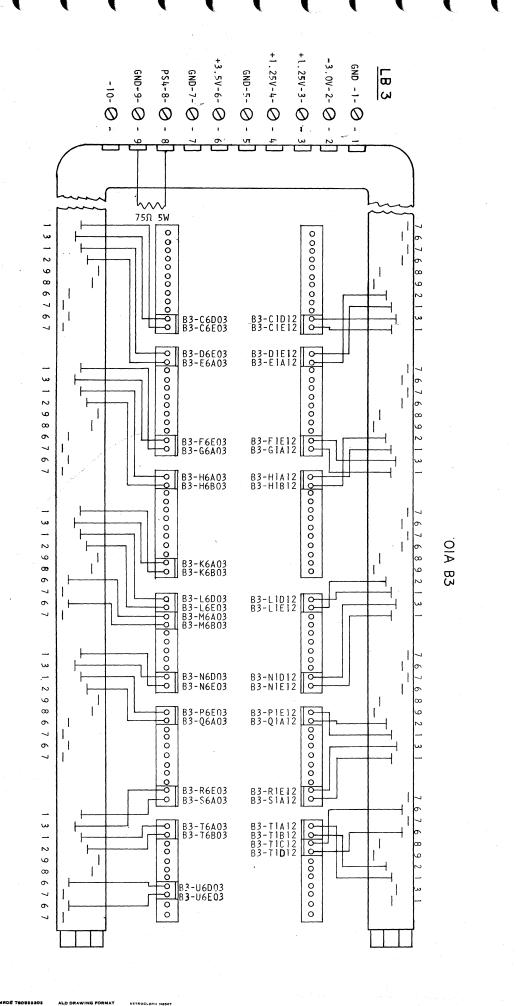
PHASE B

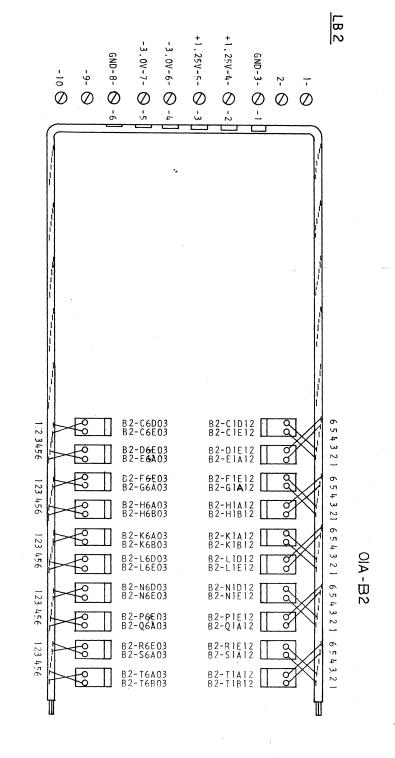


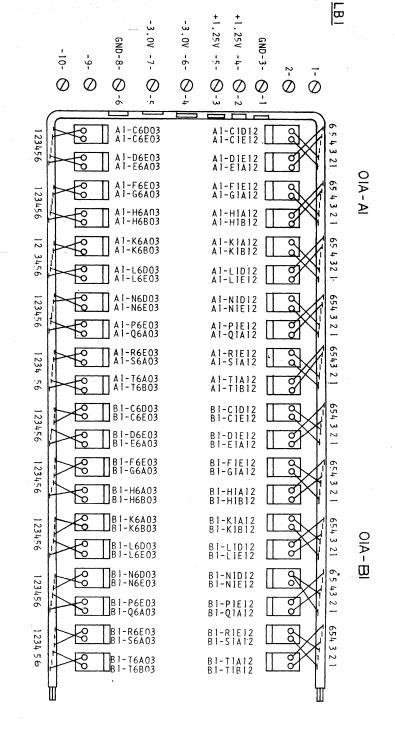
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	25 JUL72	437529	MACH	-058E	2
			PART NO	2199871	
_			CLASSI	FICATION	TD1/
U,	·				IBM CORP

DRAFTING MEDIA GRAPHIC CONTROLS CORPORATION Buffalo, New York







NOTES:

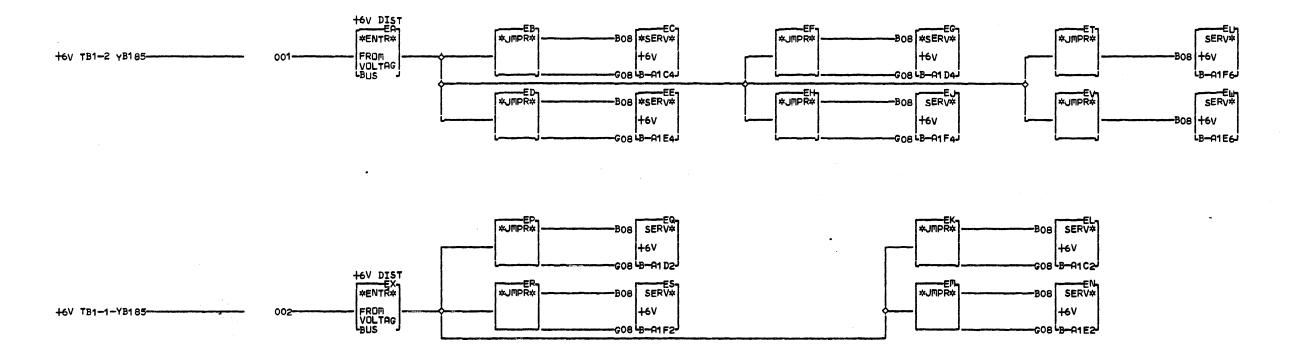
1 SEE YB185 FOR LAMINAR BUS VOLTAGE CONNECTIONS

LONA	CONICIDENTIAL	LINITH	D = C	1075
	CONFIDENTIAL	UNIT	レニし	19/5

		EC HI	STORY		DRAWING T	TLE .
		7FEB75 437380		GATE	LAMINAF	R BUS
	1.			MACH 3830 - 2 SNIPE		
			PART NO	234893	2	
1	n			CLASSIF	ICATION	TDS/
	ע			11 FEB 75	FLW	LDIN CORP

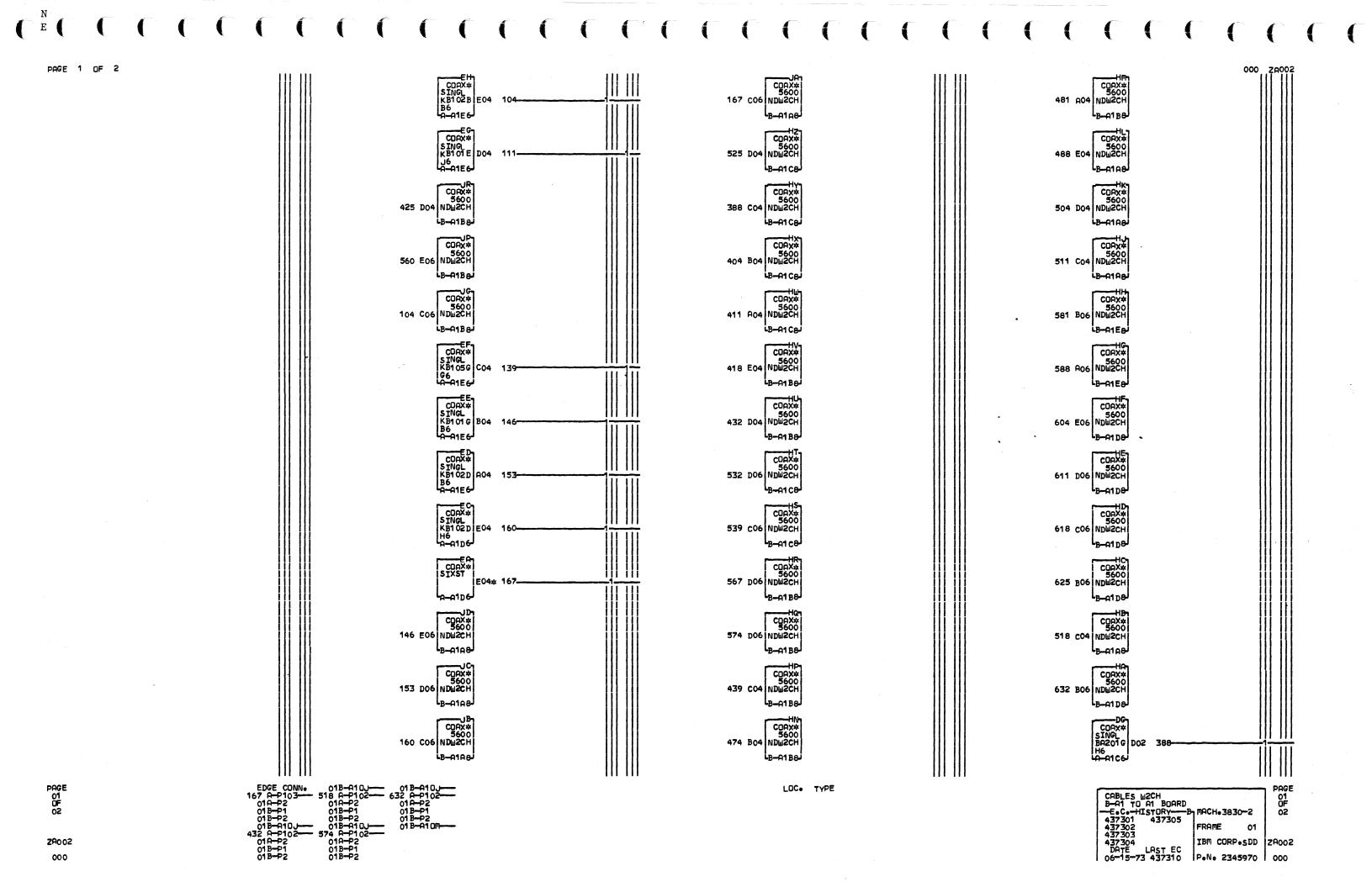
THEREAFTER \*\*

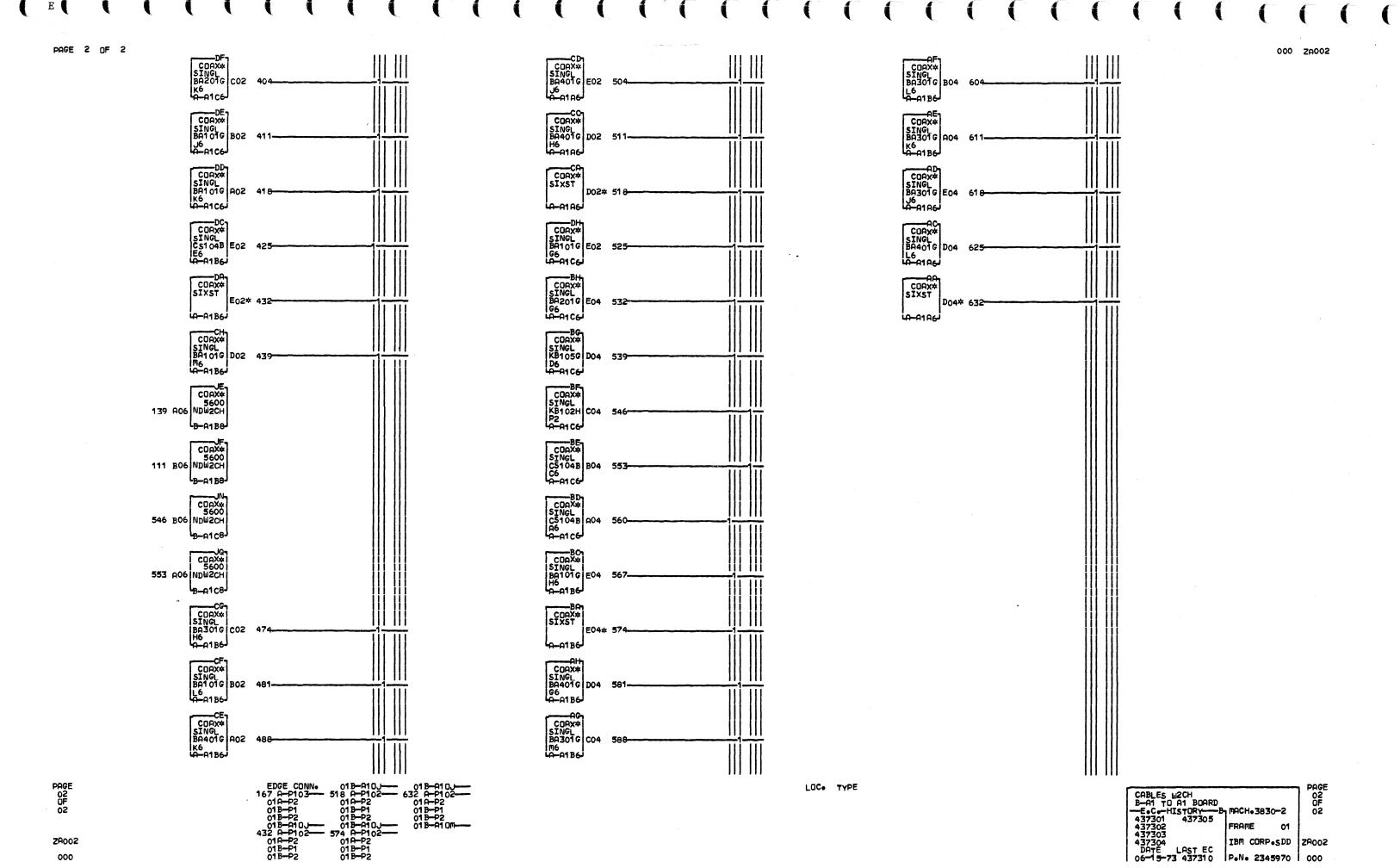
000 ZA001



LOC. TYPE

TAILGATE BOARD
SERVICE VOLTAGES
-E.C.-HISTORY-B MACH.3830-2
437301
FRAME 01
1BM CORP. SDD
00
DATE LAST EC
06-15-73 437310
P.N. 2345969





IBM

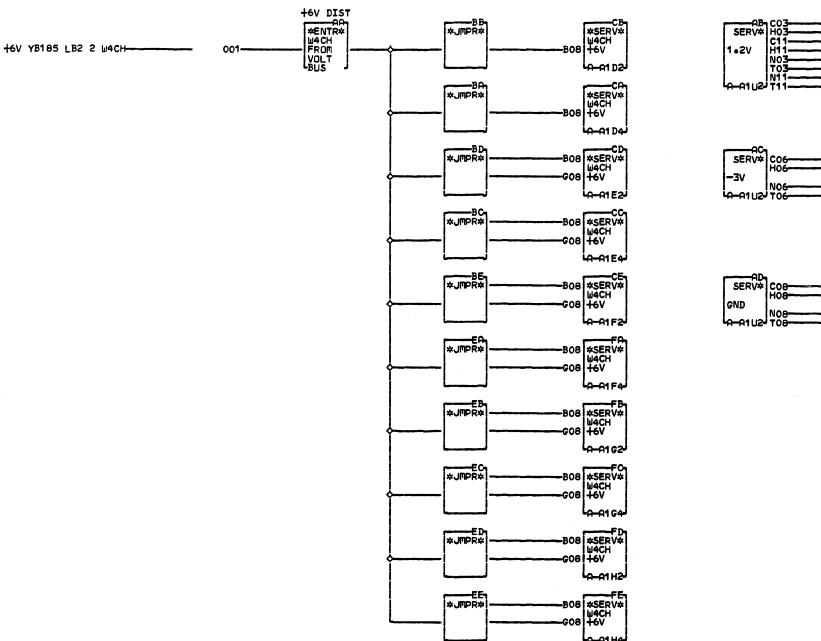
CONFIDENTIAL

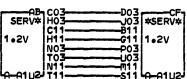
UNTIL 03-30-76

UNCLASSIFIED

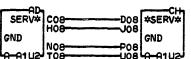
THEREAFTER

005 ZA003



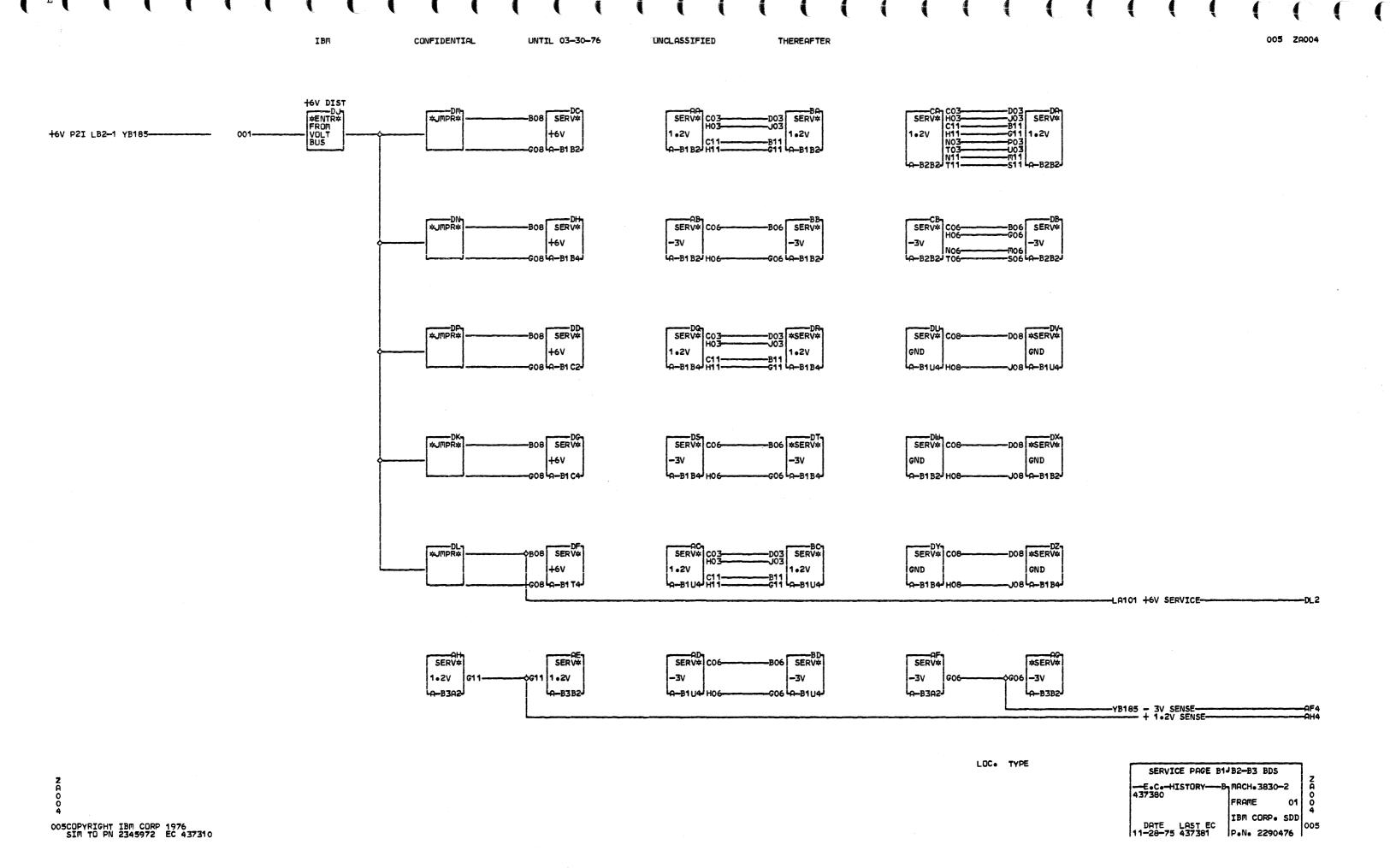


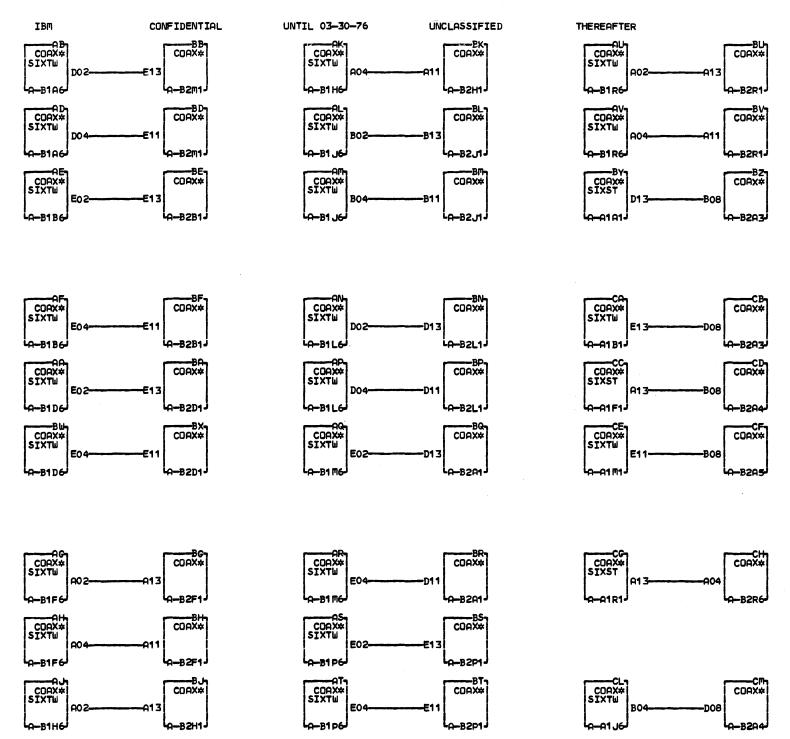




LOC. TYPE

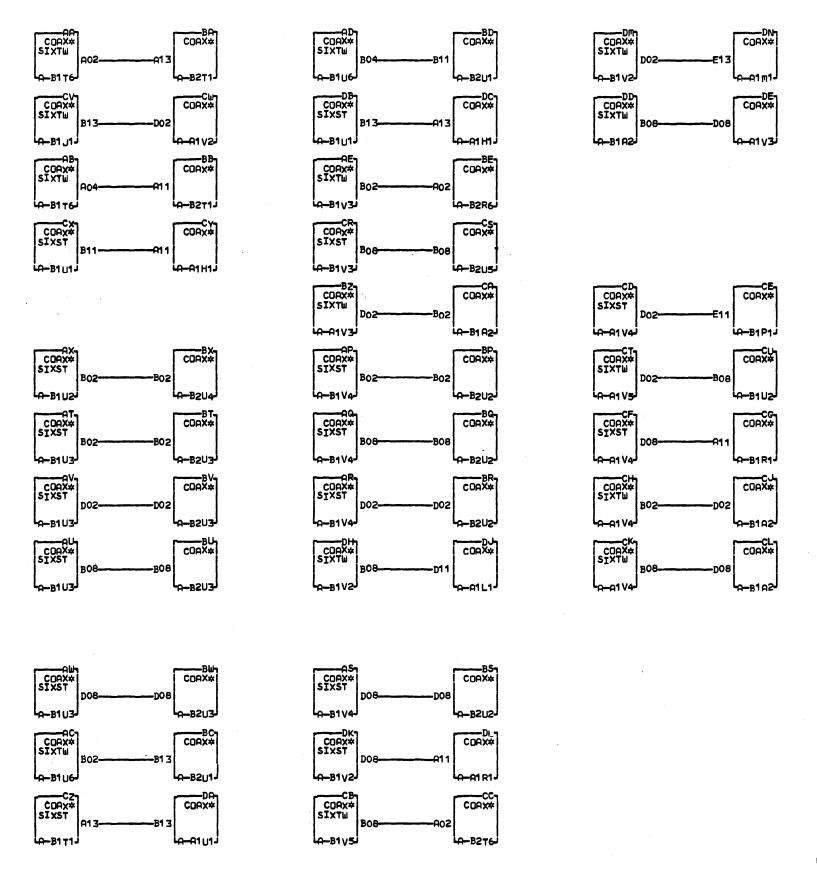
SERVICE PAGE A1 BOARD -E.C.-HISTORY-B, MACH. 3830-2 IBM CORP. SDD DATE LAST EC 11-28-75 437381 P.N. 2290493





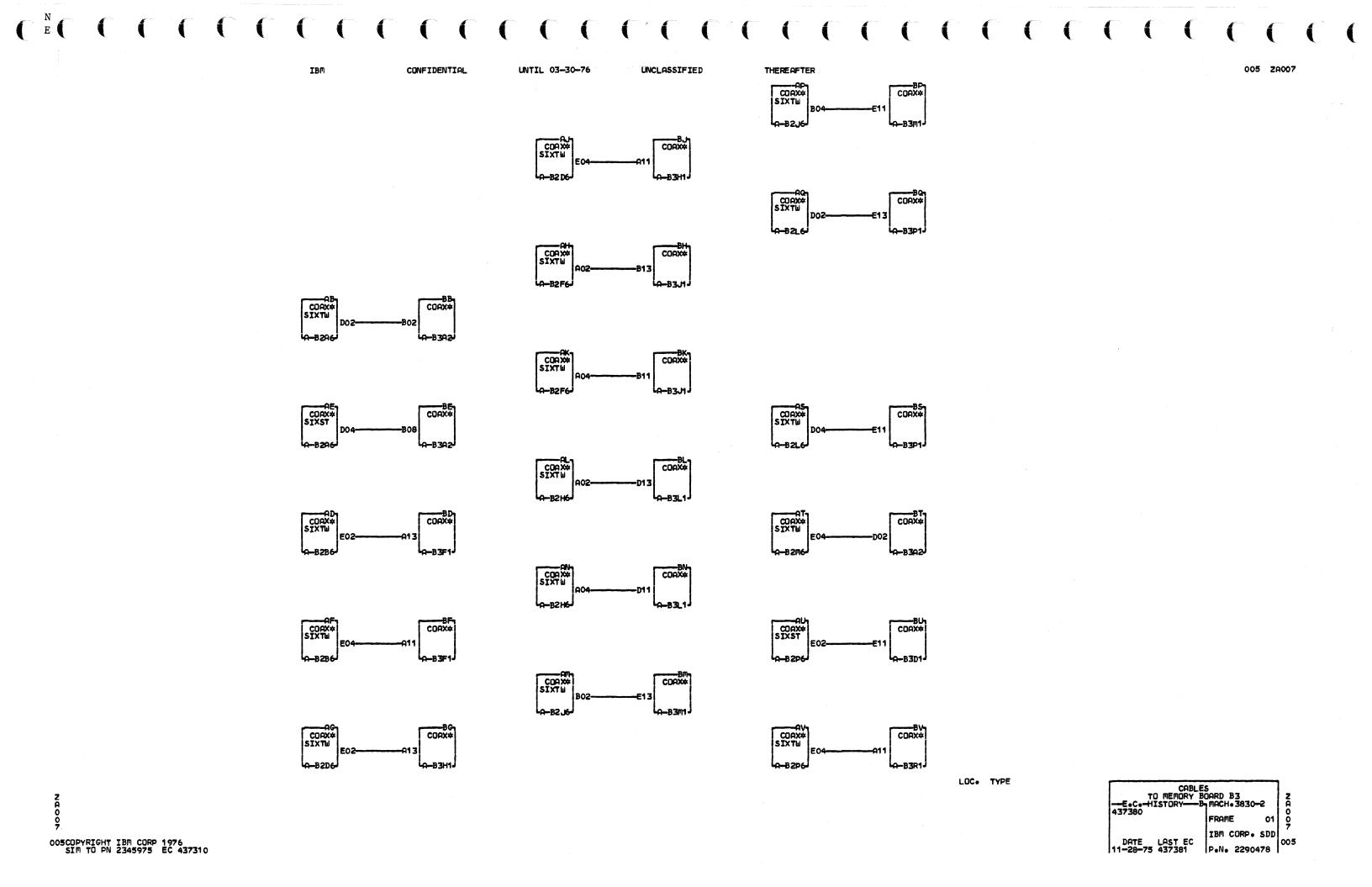
LOC. TYPE

005 ZA005



LOC. TYPE

6-PAK CABLES							
-E.CHISTORYB MACH-3830-2							
437303 437304	FRAME 01	Į					
437305	IBM CORP. SDD						
DATE LAST EC 106-15-73 437310	P.N. 2345974	1000					



CONFIDENTIAL UNTIL 03-30-76 UNCLASSIFIED

THEREAFTER

+ SET ACR-

- GATE ERR 1 REG TO WR BUS-----DE201AV2-

- SET BAR FT180-T100 NOT CALLT-KK205BF4-

DE CLOCK - DE CLK -T060-T100----KK205AD2-

PLUG ON TERMINATORS PLOT --E.C.-HISTORY----By MACH.3830-2

000 ZA012 CDAX\* SINGL AA101G E02 104-CDAX\* 4100 104 CO9 ND CDAX\* SINGL AA301G CO2 504-DO2\* 204-467 DO9 ND G6 A-A1N6 a\_a1 H6 160 HOD UP COAX\* SINGL AA2016 DO2 111-CDAX\* 3900 474 CO9 ND COAX\* 3900 153 BO9 ND COAX SINGL AA301G BO2 511-M6 A-A1H6 H6 A-A1N6 CDAX\* SINGL AA201 G CO2 COAX\* SINGL AR1 02G A02 CDAX\* SINGL AA401G - 167 E09 ND 481 BO9 ND A02 518-LB--A1 D1 نـ1 16 ۾ 174 Do9 ND 146 CO9 ND CDAX\* 3900 ND COAX\* COAX\* SINGL AA1016 B02 A02\* 525-1J6 1A-A1N6 نهH 14-A1 COAX\* SINGL AA1016 A02 132-504 E09 ND CDAX\* CDAX\* SINGL KA105F E02 432-181 CO9 ND 1K6 1A-A1N6 i.B--A1 D1 j COAX\* CDAX\* SINGL AA2016 E02 139-CDAXX SINGL GK701A DO2 439-COAX 188 Bo9 ND 511 DO9 ND 3900 LB-A1 D1-COAX\* COAX\* COAX COAX 3900 204 B09 ND 3900 518 C09 ND GK503A C02 A--A1 M6-CDAX\* SINGL AR102E D02 153-K6 CDAX\* COAX\* SINGL GK503B B2 A-A1T6 CDAX\* 460 DO9 ND 525 C09 ND B02 453-COAX\* SINGL KB105F C02 160-J2 A-A1M6 CDAX\* SINGL AR102X A02 460-139 C09 ND 418 DO9 ND CDAX\* SINGL GK509c B02 167-CDAX\* COAX\* 4100 432 C09 ND 132 DO9 ND A02# 467--A-0176 CDAX\* COAX\* SINGL AA4016 A02 CDAX\* 439 BO9 ND 125 E09 ND CDAX\* SINGL GK601F E02 181-CDAX\* SINGL AA3016 E02 481-CDAX\* 446 A09 ND 118 A09 ND B-A1 C1 COAX\* SINGL GK602F S6 LA-A1L6 CDAX\* 4500 CDAX\* SINGL AA3016 DO2 488-DO2 188-453 E09 ND 111 BO9 ND K6 | LB-A1 B1 j LB--A1 B1 j EDGE CONN•
146 A-A30F
01A-P3
01A-P3
01B-P1
01B-A10J204 A-A30F
01A-P3
01A-P2
01B-P1 01B-A10M-467 A-A30M-01A-P3 01A-P2 01B-P1 01B-A10J-525 A-A30J-01A-P2 01B-P1 LOC. TYPE 01 B-A10M---6-PAK CABLES B-A1 TO A1 BOARD E-C-HISTORY-B 437301 437302 437305 -B7 MACH • 3830-2 IBM CORP.SDD ZA012 DATE LAST EC

06-15-73 437310

ZR012 000

TAILGATE EARLY MACHINES TAILGATE LATE MACHINES CHANNEL C CHANNEL D T4-04 T3-04 T4-01 TAG IN TAG OUT TAG IN DARK 01T3-04 T4-04 LIGHT TAG OUT CHANNEL D T4-02 T4-02 BUS OUT T3-05 DARK BUS IN LIGHT T4-05 BUS OUT T3-01 T1-01 T1-04 T2-01 T2-04 T4-01 CHANNEL C T3\_02 T4-02 T1-02 T1-05 T2-02 T2-05 T1-04 T2-04 CHANNEL B PC 1 CHANNEL A CHANNEL B T1-05 PC 2 T2-05 T1-01 CHANNEL A T2-01 NOTE 8 PC4 T1-02 T2-02 PC 1 PC 11 PC 2 PC 10 NO TES: PC3 1. VIEWS FROM CHANNEL CABLE SIDE. 2. BUS OUT TERMINATOR P/N 2282675 PC4 3. TAG OUT TERMINATOR P/N 2282676 4. OUT CABLE CONNECTORS ARE TYPE A (P/N 2281874) IN CABLE CONNECTORS ARE TYPE B (P/N 2281832) EACH 3830-2 CAN BE CONNECTED TO BLOCK MPX CHANNELS AS FOLLOWS ONE BASIC TWO CHANNEL (SFC 8170) TWO CHANNEL ADDITIONAL (SFC 8171) CONTROLLER INTERFACE CONN ORDER ONE GROUP 791 OR 793 AND 794 PER CHANNEL LAST 3830-2 MUST BE WITHIN 150FT BRACKET MOUNTED ON FRAME BELOW 01B BOARD PC-6 BRACKET MOUNTED ON FRAME BELOW OIB BOARD AND LEFT OF PC3/PC4 BRACKET PC-5 10. EACH 3830-2 CAN BE CONNECTED TO A 2925-10 OR EQUIVALENT SWITCHING CONSOLE FOR REMOTE ENABLE/DISABLE CHANNEL SWITCHING AS FOLLOWS: BUS P1 TWO CHANNEL (SFC 6148) TWO CHANNEL ADDITIONAL (SFC 6149) ONE TAG P2 ORDER ONE GROUP 1794 PER FEATURE

CABLE	NO	KEY NO	PIN	FROM					то							
	OF CA			UNIT	CONN	Y D I ( )		Z DIM (IN)	UNIT	CONN	Y DIM (IN)	Z DIM (IN)	X MAX (FT)	в/м	. NO TES	
791	2	7917	5353920	3830-2 TAG		30	.00		BLOC K			* NOTE 7	TE 7	53 <b>7307</b>		
		791B	<b>^</b>	A	BUS IN		<u>A</u>		MPX CHAN			*	٨	٨		
793		793T	V		TAG OU	1			NEXT			*			٧	
	2	793B	5353 <b>92</b> 0		BUS OU	1			CONTROL UNIT			*		7	53 <b>7</b> 30 <b>74</b>	
794	1	794∧	53511 <b>7</b> 8	V	PC-1		V		SEQ CTRL			30.00	15	0.00	53 <b>97</b> 8 <b>21</b>	
1794	1	179 <del>4</del> A	53511 <b>7</b> 8	3830-	2 PC-10	30	.00		2925-10 OR SW CONSOLE			30.00	150	.00	5397821	9 & 10

\* REFER TO TABLE A

TABLE A (Z DIMENSIONS)

GROUP	791 BLOCK MPLX	793 CTRL	794 SEQ	
2880	40.00"		30,00"	
3155	12.00"		20.00"	
CONTROL		30.00"		
3145	30.00"			
3158	12.00"	30.00"	20,00"	

IBM CONFIDENTIAL UNTIL AUGUST 1973, UNCLASSIFIED THEREAFTER

	EC HISTORY		DRAWING TITLE					
	22 JUN 73 437329		CABLE ROUTING AND IDENT CHART					
	2310173 437310		MACH 3830-2					
			PART NO 2345985					
C			CLASSIFICATION					
		-	LDNL CORP					

F

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